

IEEE P802.3-2015/Cor 1 (IEEE 802.3ce) Multilane timestamping Initial Sponsor ballot comments

Cl 90 SC 90.7 P 14 L 21 # i-1  
Anslow, Peter Ciena Corporation

Comment Type T Comment Status A

The modified text on lines 11 through 14 is "The transmit path data delay is measured from the beginning of the SFD at the xMII input to the beginning of the SFD at the MDI output. The receive path data delay is measured from the beginning of the SFD at the MDI input to the beginning of the SFD at the xMII output."  
This is written in terms of the "beginning of the SFD".  
However, the final sentence on line 21 talks about the arrival of the SFD rather than the arrival of the beginning of the SFD.

*SuggestedRemedy*

Change the final sentence to: "The receive path data delay for a multi-lane PHY is reported as if the beginning of the SFD arrived at the MDI input on the lane with the smallest buffer delay."

Response Response Status C

ACCEPT.