

gement Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balanced Pair of

Cl 147 SC 147.3.2.1 P 181 L 52 # 93
Brandt, David Rockwell Automation

Comment Type E Comment Status D Late

Two parts of split figure are inconsistently labelled as 147-4 (part a) and 147-5 (part b)

SuggestedRemedy

Relabel both parts as 147-4, (part a) and (part b). Renumber remaining figures.

Proposed Response Response Status W

PROPOSED REJECT.

This has been checked several times already, and the conclusion was always that figure labeling in done automatically by Frame as per the requirements, and it is the figure title (and additional labelin in there) that binds them into one.

Cl 147 SC 147.3.8.1.3 P 193 L 28 # 94
Brandt, David Rockwell Automation

Comment Type E Comment Status D Late

WAIT_HB exit transition arrow extends into state box.

SuggestedRemedy

Remove arrow line from inside box.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Already resolved by #48.

Proposed resolution for #48 is as follows:

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PROPOSED ACCEPT.

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Cl 148 SC 148.4.5.1 P 228 L 17 # 95
Brandt, David Rockwell Automation

Comment Type E Comment Status D Late

Exit condition C of EARLY_RECEIVE appears related to exit condition B.

SuggestedRemedy

Move exit condition equation for C next to the arrow line and away from arrow line for B.

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 148 SC 148.4.5.4 P 231 L 7 # 96
Brandt, David Rockwell Automation

Comment Type T Comment Status D Late

to_timer should not map to both clause 30 and clause 45, but only one or the other.

SuggestedRemedy

Change from:

The transmit opportunity timer maps to aPLCATransmitOpportunityTimer. When the MDIO is present, the timer is configured to the content of bits 28.2.7:0. When MDIO is not present, the functionality of bits 28.2.7:0 can be provided by equivalent means.

To:

If the RS is implemented above MII as shown in Figure 148-1, the transmit opportunity timer maps to aPLCATransmitOpportunityTimer. If MDIO is present and the RS is implemented below MII, the timer is configured to the content of bits 28.2.7:0. When MDIO is not present, the functionality of bits 28.2.7:0 can be provided by equivalent means.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Accomodated by comment #109

Proposed response to comment #109 deletes the text about MDIO registers - proposed response to #109 is:

PROPOSED ACCEPT IN PRINCIPLE.

Remove MMD registers for PLCA - PLCA will be managed as a clause 30 object.

Delete changes on P42 to text in 45.2, tables 45-1, and 45-2.

Delete 45.2.13 and subclauses.

In 148.4.5.2

Delete paragraph "When the MDIO is present" on P 229 L 7-11. (under plca_reset) Delete paragraph "When the MDIO is present" on P 229 L 16-21. (under plca_en) Delete third and fourth sentences of paragraph under local_nodeID "When MDIO is present... equivalent means." On P229 L49-51.

Delete second and third sentences of paragraph under plca_node_count "When MDIO is present... equivalent means." On P230 L4-6 Delete third and fourth sentences of paragraph under max_bc "When MDIO is present, ... equivalent means." On P230 L27-30

In 148.4.5.4

Delete third and fourth sentences of paragraph under burst_timer "When MDIO is present,... equivalent means." On P230 L49-51 Delete second and third sentences of paragraph under to_timer "When the MDIO is present... equivalent means." On P 231 L7-9

148.4.7.2

Delete third sentence of paragraph under plca_status "When MDIO is present this signal maps to register 28.15.15." at P237 L1

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

SORT ORDER: Clause, Subclause, page, line

Cl 148

SC 148.4.5.4

Page 1 of 1

2/13/2019 3:29:05 PM