Cl **148**   *SC* **148.4.6.6**   *P* **249**   *L* **4**   # r02-67

Beruto, Piergiorgio

Comment Type **T**   *Comment Status* **D**   *Late*

The delay_line_length constant should count nibbles instead of bits, according to the way it is used in the State Diagrams.

SuggestedRemedy

Change "This constant is implementation dependent and specifies the maximum length of the PLCA RS variable delay line depicted in Figure 148-2.
Value: up to 396 bit times."
to
"This constant is implementation dependent and specifies the maximum number of nibbles that the PLCA RS variable delay line can hold.
Value: up to 99"

Proposed Response   *Response Status* **W**

PROPOSED ACCEPT.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
COMMENT STATUS: D/dispatched A/accepted R/rejected   RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
SORT ORDER: Comment ID

Comment ID **r02-67**   Page 1 of 1
8/8/2019 3:20:51 PM