CI 30	SC 30.3.9.2.3	P 39	L 4	# i-266	CI 78	SC 78.5	P 71	L 49	# <u>i</u> -315
Thompso	n, Geoffrey	Independent C	Consultant		Kabra, Lo	okesh	Synopsy	/s, Inc.	
Comment	Type TR Comn	nent Status D		PLCA	Commen	t Туре Т	Comment Status D		EE
As fai certai has b	as I know, the actual viab hly true that a 255 node PL een asserted in an ad hoc iched 802 3 error detection	ility of a 255 node ne _CA network is not w that such a high nod	etwork has not b vithin our goal se le count would ir	een established. It is t (Ref: Obj. 11b) and it terfere with long	As pe Tw_s value	er equations giv ys_tx(min) = T s given in Tabl	ven in Figure 78-5 of 802.3 w_sys_rx(min) + Tphy_shr e 78-4 does not satisfy this	-2018, ink_tx(max) + Tphy_s s equation	hrink_rx(max)". The
addre	ss space (255) is appropria priate default value.	ate so that it will not	have to be revis	ted, 255 is not an	Suggeste Char	<i>dRemedy</i> ge value for Tv	v_sys_tx from 220 to 450		
Suggeste	dRemedy				Proposed	l Response	Response Status W	I	
In acc	ordance with our objective	es, change the defaul	It value to 8.		PRO	POSED ACCE	PT IN PRINCIPLE.		
Proposed	Response Respon	nse Status W			Acco	modated by co	mment i-62		
PROF	OSED ACCEPT IN PRIN	CIPLE.			1000				
A	amadatad by commant i 1	90			Resp	onse to comme	ent i-62 is:		
ACCO	nmodated by comment I- I	69.			PRO	POSE ACCEP	г.		
Resp	onse to Comment i-189 is:				Use 1 250.5	he following va 5 us, Tphy_shri	lues within Table 78-4 for ⁻ nk_tx: 10 us, Tphy_shrink_	10BASE-T1L: Tw_sys _rx: 240 us, Tw_sys_r:	_tx: 270 us, Tw_phy: x: 20 us
At page	ge 39, line 12 insert " The o	default value is 255.;	" to " The defaul	t value is 0.;"	C/ 146	SC 146 1	P 104	/ 15	# i-206
At pag	ge 39, line 22 insert " The o	default value is 255."	after "This valu	e is assigned to define	Schicket	anz Dieter	Liniversi	ty of Applied Science	Reutlingen
the ID	of the local node on the P	LCA network."			Commen	t Type T	Comment Status		PMA Electric
C/ 30	SC 30.3.9.2.3	P 39	L 12	# i-189	As th	ere are 2 link s	egment implementations (one for 2.4 Volt and o	ne for 1 Volt) this
Beruto, P	ergiorgio	Canova Tech	S.r.l.		sente	ence needs to b	e defined differently. As th	is occurs at a lot of pl	aces it is proposed to
Comment	Type T Comm	nent Status D		PLCA	Current		2.4 1000111111K 01119		
aPLC	ANodeCount has a default	value of 255. This m	nakes no sense	at all since this	Suggeste	areneay	' this clause are mot" For i	nsortion loss take Equ	untion 1/6 10
attribu	ite is to set the maximum numbe	er of nodes that will c	net a transmit or	portunity on the	Adda				allon 140-10.
local	collision domain, as specifi	ied in Clause 148.	got a tranomit op	portainty on the	Proposed	Response	Response Status W	1	
This i	s one of the parameters the	at have to be set pric	or to enable PLC	A operations, as	PRO Exist	POSED REJEC	i. ces the normative requiren	nents in this clause	The normative
148.4	.5.1.				requi	rements for the	link segment would be rel	ative to the transmit o	utput voltage modes
					that t	he PHY support	ts. When the (optional) 2.	4 Vpp mode is support	rted and selected, that
On th as va show	e other hand, aPLCALocal ue 255 is used to prevent n in figure 148-3 in the tran	NodeID has no defai PLCA from starting a sition from DISABLE	ult value, which a cycle of transm to RESYNC sta	also makes no sense it opportunities as ate.	would	be Equation 1 be 146-11.	40-10, but when the (man	datory) 1.0 vpp mode	is supported, that
Suaaeste	dRemedv				This	s clear in 146.	7.1.1		
At line At line the lo	 2 change " The default value 22 add " The default value cal node on the PLCA network 	value is 255.;" to " Th e is 255." after "This work."	ne default value i value is assigne	s 0.;" ed to define the ID of					
Proposed	Response Respon	nse Status W							
PROF	POSED ACCEPT.								
-									
TYPE: TR COMMEN	/technical required ER/ed T STATUS: D/dispatched	itorial required GR/g A/accepted R/rejec	general required ted RESPON	T/technical E/editorial C SE STATUS: O/open W/	6/general written C/close	ed U/unsatisfie	C d Z/withdrawn S	C/ 146 SC 146.1	Page 1 of 23 5/23/2019 6:3

SORT ORDER: Clause, Subclause, page, line

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i-315

PMA Electrical

EEE

C/ 146	SC 146.3.3.1.	1 F	[,] 118	L 35	# i-346		C/ 146	SC 14	6.3.3.1.	3	P 119	L 18	# i-349
Law, Dav	id	He	wlett Packard	I Enterprise	-		Law, David	d			Hewlett Pack	ard Enterprise	
Comment	Туре Т	Comment Statu	is D			PCS	Comment	Туре	TR	Comment S	Status D		PMA
It is n encoo the en ternal If it is symb tx_syn assig	ot clear to me on r ded in to ternary tri ncoding defined in ry triplets such as the former, only th ols generated by th mb_triplet variable ned values such a	reading the draft if iplet as defined in Figure 146-5 'PC COMMA and ESD ne encoding define he PCS Transmit s definition will nee s COMMA (see Si	4B3T encodi Table 146-1 S transmit sta 4. ed in Table 14 function after d to be updat SD COMMA1	ing is only whe '4B3T encodin ate diagram' wh 46-1, the text '/ 4B3T encodir ted as tx_symb VECTOR sta	n Sdn[3:0] is bein g' or if it includes nich also include A triplet of ternary g.' in the p_triplet is also te) and ESD4 (see	ig all e	As illu: diagra from th specifi symb_ Suggestec [1] Cha PMA_	strated in m', and d ne PHY to cation of triplet_tin <i>IRemedy</i> ange the UNITDAT	Figure 1 efined in the RS TX_CLK ner and descripti	146-2 '10BASI n IEEE Std 802 , not the other (in Clause 14 that symb_trip tion of the sym	E-T1L PHY int 2.3-2018 subc way round. D 6. Suggest that let_timer be g b_timer to rea are is issued t	erfaces' and 146 lause 22.2.2.1, T Despite this, I was at TX_CLK is gen generated from s ad 'A continuous by the PCS conc	S-3 'PCS reference FX_CLK is sourced s unable to find a herated by ymb_timer. free-running timer. urrently with
ESD	VECTOR state).	,			,		symb_	_timer_do	ne.'.				
Suggeste	dRemedy						[2] Ch	ange the	descripti	ion of the sym	b_triplet_time	r to read 'A conti	nuous free-running
See o	comment.						timer t	hat shall	expire sy	ynchronously	with every thir	d expiration of sy	/mb_timer. TX_CLK
Proposed	Response	Response Statu	s W				(see 2 denera	2.2.2.1) s ated sync	hall be g hronous	generated from	n symb_triplet	_timer with the ri tone '	ising edge of TX_TCLK
PROF Chan encoo "A trip encoo	POSED ACCEPT I ge "'A triplet of tern ding." in the tx_syn olet of ternary sym ded data and assig	IN PRINCIPLE. nary symbols gene nb_triplet variable bols generated by gned values (see 1	erated by the definition (14 the PCS Tra 46.3.3.2.6)."	PCS Transmit 6.3.3.1.1, P11 nsmit function	t function after 4B 8 L35)" to . These include 4	3T •B3T	Proposed PROP (comm [1] Cha PMA_ symb_	Response OSED AG nenter's p ange the UNITDAT timer_do	e CCEPT I roposed descripti 'A.reque ne.'.	Response S IN PRINCIPLE resolution + c ion of the sym st messages	<i>tatus</i> W hange to 146 b_timer to rea are is issued b	.4.5.4) ad 'A continuous by the PCS conc	free-running timer. urrently with
							[2] Cha timer t (see 2 genera	ange the hat shall 2.2.2.1) s ated sync	descripti expire sy hall be g hronous	ion of the sym ynchronously generated fron ly with symb_t	b_triplet_time with every thir n symb_triplet triplet_timer_d	r to read 'A conti d expiration of sy t_timer with the r done.'	nuous free-running /mb_timer. TX_CLK ising edge of TX_TCLK
							[3] Cha "The c While source	ange 146 lock reco it may no e of RX_C	.4.5.4 (P very pro t drive th CLK."	2139 L43) to a vides a synch ne MII directly,	dd new first pa ronous clock f the Clock Re	aragraph: for sampling the covery function i	signal on the pair. is the underlying root

C/ 146 SC 146.3.3.1.3

C/ 146	SC 146.3.3.1.5	P 120	L 7	# i-351	'PCS	eceive state diag	ram (part b)'.		
Law, Dav	rid	Hewlett Packa	rd Enterprise		C/ 146	SC 146.3.3.2	P 124	L 43	# i-284
Commen	t Type T Con	nment Status D	·	State Diagram	McCarthy	Mick	Analog Dev	ices Inc.	
In sou the E DISP in the Sugg	me cases, the result of a SD DISPRESET VECTO RES(tx_disparity), yet in SEND IDLE state is EN est that there should be a	function is assigned to R state is tx_symb_trip other cases, there is n CODE(Sdn[3:0], tx_dis a consistent assignment	a variable, for e blet <= o assignment, fo parity). nt of the result o	example, the action in or example, the action f a function to a	Comment The d same interpathe the tra could	<i>Type</i> T elimiters SSD4 ar If a PHY is trans acket gap, there v nsmitted signal. 1 be avoided by rar	Comment Status D d ESD4/ESD_ERR4, as d mitting a stream of packets vill therefore be a non-zero This will produce a harmoni domizing the sign of the de	efined in Table 14 of constant lengt value in the auto- ic in the transmit p elimiters.	<i>PCS</i> 6-3, are always the h and with a fixed correlation sequence of power spectrum. This
varia	ole within actions in state	diagrams. Based on ti	nis:		Suggestee	Remedy			
[1] CI	hange 'ENCODE(Sdn[3:0], tx_disparity)' to read	'tx_symb_triplet	t <=	Add s	cheme to random	ize the sign of the delimiter	S.	
ENC PCS	ODE(Sdn[3:0], tx_dispari transmit state diagram'.	ty)' in the SEND IDLE a	and TRANSMIT	DATA in Figure 146-5	Proposed	Response	Response Status W		
[2] Cl rx_di: DISP BAD 'PCS	hange 'DECODE (Rxn-5, sparity)' in the DATA, FO RESET3, ESD, BAD ESI END states in Figure 146 receive state diagram (p	rx_disparity)' to read 'F URTH SSD, CHECK E D2, BAD ESD3, RX EF S-8 'PCS receive state art b)'.	RXD[3:0] <= DE(SD COMMA2, (RROR, CHECK diagram (part a)	CODE (Rxn-5, CHECK ESD ESD ESD4 and the ' and Figure 146-9	PROF TFTD Comn	OSED REJECT.	remedy is unclear, as is th	e magnitude of th	e issue.
Suggeste	dRemedy								
Sugg varial	est that there should be a ble within actions in state	a consistent assignmer diagrams. Based on tl	nt of the result o nis:	f a function to a					
[1] CI ENCO 'PCS	hange 'ENCODE(Sdn[3:0 ODE(Sdn[3:0], tx_dispari transmit state diagram'.	i], tx_disparity)' to read ty)' in the SEND IDLE a	'tx_symb_triplet and TRANSMIT	t <= DATA in Figure 146-5					
[2] Cl rx_dia DISP BAD 'PCS	hange 'DECODE (Rxn-5, sparity)' in the DATA, FO RESET3, ESD, BAD ES END states in Figure 146 receive state diagram (p	rx_disparity)' to read 'F URTH SSD, CHECK E D2, BAD ESD3, RX EF S-8 'PCS receive state art b)'.	RXD[3:0] <= DE(SD COMMA2, (RROR, CHECK diagram (part a)	CODE (Rxn-5, CHECK ESD ESD ESD4 and the ' and Figure 146-9					
Proposed	Response Resp	oonse Status W							
PRO ENC sugg	POSED ACCEPT IN PRI ODE and DECODE both ested remedy is modified	NCIPLE. update not only the trip to reflect this.	plet but the dispa	arity. Commenter's					
[1] CI ENC 'PCS	hange 'ENCODE(Sdn[3:0 ODE(Sdn[3:0], tx_dispari transmit state diagram'.	r], tx_disparity)' to read ty)' in the SEND IDLE a	'{ tx_symbol_tri and TRANSMIT	plet, tx_disparity } <= DATA in Figure 146-5					
[2] CI (Rxn- DISP BAD	hange 'DECODE (Rxn-5, -5, rx_disparity)' in the DA RESET3, ESD, BAD ES END states in Figure 146	rx_disparity)' to read '{ \TA, FOURTH SSD, C D2, BAD ESD3, RX EF }-8 'PCS receive state	RXD[3:0], rx_di HECK ESD COI ROR, CHECK I diagram (part a)	sparity } <= DECODE MMA2, CHECK ESD ESD ESD4 and the ' and Figure 146-9					
TYPE: TR	R/technical required ER/e	editorial required GR/g d A/accepted R/rejec	jeneral required ted RESPON	T/technical E/editorial G/ SE STATUS: O/open W/w	/general vritten C/close	d U/unsatisfied Z	C/ 1 //withdrawn SC 1	46 46.3.3.2	Page 3 of 23 5/23/2019 6:37:57 P

SORT ORDER: Clause, Subclause, page, line

C/ 146	SC 146.3.3.2.1	P 121	L 27	# <u>i-35</u> 4		C/ 146	SC 146	6.3.3.2.5	5	P 123	L 45	# <u>i-358</u>
Law, David	Ł	Hewlett Packa	ard Enterprise			Law, Davi	b			Hewlett Pac	kard Enterprise	
Comment	Туре т Со	omment Status D			PCS	Comment	Туре Т	R	Comment	Status D		State Diagram
Subcla 'Gener bits Sc input to 146-5 Subcla subcla 146.3. perforr 'Conve diagra	use 146.3.3.2.1 'Side ration of Syn[3:0]' in co In[3:0]' define the requ to the ENCODE() funct 'PCS transmit state di use 146.3.3.2.4 'Gene use 146.3.3.2.5 'Gene 3.2.6 'Generation of sy ned by Figure 146-5 'I entions in this clause' so m and descriptive text	-stream scrambler polyr ombination of subclause uirements in respect to the tion in the SEND IDLE a agram'. eration of ternary triplet i ymbol sequence' then de PCS transmit state diagnes states that ' Should there, the state diagram prev	nomial', subclause 146.3.3.2.3 'Gen he generation of S and TRANSMIT D in mode SEND_N n mode SEND_Z' escribes the enco ram'. Since subcla be a discrepanc ails.' the state dia	a 146.3.3.2.2 eration of scrar Sdn[3:0] which ATA states of F and SEND_I', and subclause ding that is act ause 146.1.3 y between a st gram requirem	mbled is Figure aually ate ents	There output tx_syn Figure tx_syn shall b and 14 If tx_m be in t tx_erro state o	seems to b s tx_symb_ hb_triplet fr 146-6 sho hb_triplet, a e a zero ve l6-5 would node = SEN he 'DISABI or_mii to Fa liagram' wi	be a dis _triplet, rom a '4 wws tx_r and sub ector (0 seem t ND_Z th LE DAT ALSE. I ill, if nec	sconnect betw Figure 146-6 4B3T ENCOE mode as an ir pclause 146.3 , 0, 0) when to produce a me Figure 146 FA TRANSMI In turn, if tx_e cessary return	veen Figure 1. 5 'PCS transm DER', and the 1 put to the 4B 3.3.2.5 says th 1x_mode = SE different resul 3-4 'PCS data SSION' state, nable_mii = F n to and, rema	46-5 'PCS trans it symbol genera text in subclaus 3T ENCODER t at 'The ternary t 'ND_Z.' the state t. transmission en setting both tx_ FALSE the Figur ain in the 'SEND	mit state diagram' which ation' that outputs e 146.3.3.2.5. While hat produces riplet (TAn, TBn, TCn) es diagrams in 146-4 habling state diagram' will enable_mii and e 146-5 'PCS transmit 0 IDLE' state. This will
overric Suaaestea	le the subclause 146.3 Remedv	3.3.2.4 shall statements				result (0, 0, 0	in tx_symb)) as requir	_triplet red by s	being set to subclause 14	the result of E 6.3.3.2.5.	NCODE(Sdn[3:	0], tx_disparity) and not
[1] Cha to read [2] Add the tx_ [3] Inso reads	ange the block '4B3T I I 'PCS transmit state of J TX_CLK as an input symb_triplet clock. ert a new subclause 1 The scrambled bits S	ENCODER' in Figure 14 diagram'. to the 'PCS transmit sta 46.3.3.3 titled 'Generation dn[3:0] used by the ENC	6-6 'PCS transmi ate diagram' block on of scrambled b CODE function de	t symbol gener as this is used its Sdn[3:0]' that fined in 146.3.3	ation' 1 as at 3.1.2	This a respec that 'S diagra (0, 0, 0	opears to b t to tx_syn hould there m prevails.)). I don't b	be a dis nb_triple e be a d .' tx_syr believe t	crepancy bet et, and since discrepancy b mb_triplet has that this is int	tween the stat subclause 14 between a stat s to be set to ended.	e diagram and t 6.1.3 'Conventio te diagram and o ENCODE(Sdn[3	ext requirements in ons in this clause' states descriptive text, the state b:0], tx_disparity) and not
are ge	nerated as follows.				-	Suggested	Remedy					
[4] Rer and su	number subclause 146 Ibclause 146 3 3 2 3 to	5.3.3.2.1 to 146.3.3.3.1, 146.3.3.3.3	subclause 146.3.	3.2.2 to 146.3.3	3.3.2	[1] Ad	d the follow	ving def	finition to sub	clause 146.3.	3.1.5 'Constants	; :
[5] Inst PCS tr [6] Rer and su	ert a new subclause 1 ansmit state diagram number subclause 146 bclause 146.3.3.2.6 tr	46.3.3.4 titled 'Generation generates ternary triplet 5.3.3.2.4 to 146.3.3.4.1, o 146.3.3.4.	on of ternary triple is as follows. subclause 146.3.	et' that reads 'T 3.2.5 to 146.3.3	he 3.4.2	ZERO A ve 146.3.	ctor of thre 3.2.5.	e zero	symbols sen	t when tx_mo	de = SEND_Z a	s specified in subclause
[7] Rev norma Proposed	word subclause 146.3 tive. Response Re	.3.4.1, 146.3.3.4.2 and 1 sponse Status W	46.3.3.4 to be de	scriptive rather	than	[2] Re 146-5	place the a PCS trans	action E smit stat	NCODE(Sdn te diagram' w	[3:0], tx_dispa /ith:	arity) in the SEN	D IDLE state of Figure
PROP	OSED ACCEPT.					IF(tx_r tx_s tx_d ELSE EN0 END	node = SE ymb_triplet isparity <= CODE(Sdn	ND_Z) t <= ZE 2 [3:0], tx	THEN RO <_disparity)			

PROPOSED ACCEPT.

Proposed Response

C/ 146 SC 146.3.3.2.5

Response Status W

C/ 146	SC 146.3.4.1.3	P 127	L 25	# i-163	C/ 146	SC 146.3.4	4.1.3	P 127	L 25	# <u>i-93</u>
Zimmerm	an, George	ADI, APL Gro	oup, Aquantia, E	MW, Cisco, Commscop	Graber, S	teffen		Pepperl+Fuc	chs GmbH	
Comment The d Done symb receiv	<i>Type</i> T lefinition of RSTCD ". This appears to _triplet_timer in 144 ve clock. Also, this	Comment Status D is unclear. From the phras be a symbol timer for triplet 6.3.3.1.3. The text only say timer is not explicitly started	e "Receive Sym ts of received sy s it is synchronia d anywhere.	State Diagram bol Tripled Conversion mbols, similar to zed with the PCS	Comment Perioc define Suggested Define	<i>Type</i> T I and behavior d. <i>dRemedy</i> a new timer:	Comn for timer RS	nent Status D STCD are not defin	ed the timer beh	State Diagram nd RSTCD is not
Suggester Chan endin Recei expira Resta Recei Dura Abbre Proposed PROF	dRemedy ge RSTCD to Rece g "RX_CLK." (new vved_symbol_triplet ation. art time: Immediate ived_symbol_triplet tion: Three symbol eviations, with text: <i>Response</i> POSED ACCEPT.	ived_symbol_triplet_conve line, after line 25) "Continuu _conversion_timer_done (f ly after expiration, timer res _conversion_timer_done (f times (see 146.5.4.5)" Al "RSTCD Received_symbol <i>Response Status</i> W	rsion_timer. In ous timer: The c RSTCD) become start resets the c RSTCD). so, add new sul _conversion_time	sert after sentence condition es true upon timer condition poclause 146.3.4.1.4 her_done."	Define rcv_sy the PC Contir expira Resta rcv_sy Durati Modify Done, <i>Proposed</i> PROF Define rcv_sy every << NI DEFE RX_C edge of Contir expira Resta Durati	a new timer: ymb_triplet_tin CS receive clo- huous timer: The tion. rt time: Immed ymb_triplet_tin on: Three sym y existing text 1 which is equiv Response POSED ACCEI a new timer: ymb_triplet_tin third clock from EED TO RESC R LK (see 22.2.2 of RX_CLK ge huous timer: The tion. rt time: Immed on: Three sym y existing text 1	her - The row ck RX_CLK. he condition diately after of her_done. hbol times (s for RSTCD a valent to the <i>Respon</i> PT IN PRING her - The row m the PMA DLVE HOW 2.1) shall be nerated sync he condition diately after of hbol times (s	<pre>r_symb_triplet_time rcv_symb_triplet_t expiration, timer res ee 146.5.4.5) as: Abbreviation for timer condition rcv mse Status W CIPLE. '_symb_triplet_time TO SYNCH AND H generated from rcv chronously with rcv rcv_symb_triplet_t expiration. ee 146.5.4.5) as: Abbreviation for</pre>	er shall be genera timer_done beco start resets the c r Receive Symbo r_symb_triplet_tin er shall be genera HOW TO SPECII v_symb_triplet_tin r_symb_triplet_tin timer_done beco	ated synchronously with mes true upon timer ondition I Triplet Conversion ner_done. ated synchronously on FY THIS>>> mer with the falling ner_done. mes true upon timer

C/ 146	SC 146.3.4.1.3	P 128	L 41	# i-98	C/ 146 SC	146.4.3	
Graber, St	teffen	Pepperl+Fuch	ns GmbH		Kim, Yongbum		
Comment	Туре т С	omment Status D		State Diagram	Comment Type	TR	Comn
Within and al over th	the PCS receive state so by a wrong ESD. V ne MII. According to ot be MII. if a wrong SSD	e diagram the BAD DEL Vithin BAD DELIMITER ther Clauses within 802.	IMITER state is state a false car 3 a false carrier	called by a wrong SSD rier indication is sent indication is only sent	"The sequen cancellation. SLAVE PMA	ce of symb " is not suff . clock reco	ols assig ficient.
Succestor	Pomodu				SuggestedReme	dy	
Renar SSD s	ne the BAD DELIMITE tate. Add a new state	ER state to BAD SSD. R BAD ESD right from the	emove the "B" in BAD SSD state	nput arc from BAD e and add the "B" input	Change the f "In addition t assigned to f	ext to read o the PMA x_symb_ve	Clock R ector is r
arc to with bi RX_D	this new BAD ESD sta ranch condition "check V <= FALSE, RXD[3:0	<pre>c_idle". Connect the output c_idle". Content of the B] <= 0000, receiving <=</pre>	of the BAD ESD AD ESD state is TRUE"	s: "RX_ER <= TRUE,	Proposed Respo PROPOSED	nse REJECT.	Respo
Proposed	Response Re	sponse Status W			The commen		
PROP	OSED ACCEPT.				Commenter	is incorrect	
C/ 146	SC 146.3.4.1.3	P 128	L 45	# i-318	echo can be	removed a	n any im
Beruto, Pi	ergiorgio	Canova Tech	S.r.l.		intended to b	e a tutorial	on sign te the tin
<i>Comment</i> tag [IN The fu	Type T Co IDEX] Inction CHECK DISP(omment Status D (RXn-5, rx_disparity) sho	ould be checking	State Diagram RXn-4, not RXn-5.	time domain. Additionally, not have a cl	146.4.6 sta	ates it is ery function
lf it ch entry a	ecks RXn-5, it is chec arc is SSD4.	king the value of RXn in	the SSD state,	which, according to the	C/ 146 SC	146.4.4	
The sa	ame offset error occur	s multiple times also in t	the DECODE fui	nction.	McCarthy, Mick		
Suggested	lRemedy				Comment Type	т	Comr
In Figu In Figu <i>Proposed</i> PROP	ure 146-8, in all states ure 146-9, in all states <i>Response Re</i> OSED ACCEPT.	, replace all occurrences , replace all occurrences sponse Status W	s of "RXn-5" to " s of "RXn-5" to "	RXn-4". RXn-4".	10BASE-T1L No attempt h cycling betwe There is little implementati	LPI signal as been ma een MASTE predictabil ion more co	lling is di ade to ir ER and S lity to LP omplex.
					SuggestedReme	dy	
					Add LPI quie A PHY imple LPI refresh s See attached	t/refresh cy mentation tate. d document	/cling, sy could us t.
					Proposed Respo	nse	Respo
					PROPOSED Only 1000B/ PHYs with a do not. Addi essential fea	REJECT. SE-T1 has similar quie ng synchro ture to 802	s synchro et-refres nization .3cg with

P 133 L 35 # i-409 NIO nent Status D PMA

gned to tx_symb_vector is needed to perform echo It should also include reference to the MASTER and ction.

ecovery function (see 146.4.6), the sequence of symbols needed to perform echo cancellation."

nse Status W

ial and the standard is not a tutorial - no change required.

herently needed is the transmitted symbol stream. The plementation-dependent manner. The standard is not al processing or constrain possible solutions. For example, ning separately from the data, or cancel in the continuous

only for the SLAVE to recover the clock. MASTER does on.

C/ 146	SC 146.4.4	P 137	L 1	# i-285	
McCarthy,	, Mick	Analog Devic	es Inc.		
Comment	Туре Т	Comment Status D			EEE
10BA\$	SE-T1L LPI sig	nalling is driven primarily by M	II data traffic.		
No att	empt has been	made to introduce a scheme t	that synchroniz	es I PI quiet/refresh	

SLAVE PHYs.

I quiet/refresh cycling because of this, making

/nchronized using loc_lpi_req signalling during link startup. e this scheme to know when link partner will be sending an

nse Status W

onization for LPI quiet-refresh, whereas the other BASE-T h cycle (10GBASE-T and the other MultiGBASE-T PHYs) of quiet/refresh cycling would be the addition of a new, nonnout quantified benefit.

	SC 146.4.4.2	P 136	L 14	# i-104
Graber, St	effen	Pepperl-	⊦Fuchs GmbH	-
Comment	Туре Т	Comment Status D		State Diagram
The tin toleran	ner shall expire 10 ce of the timer of	00 ms after being starte +/- 1 ms from the origi	ed. (it has been m nal presentation to	issed to transfer the o the draft).
Suggested	Remedy			
The tin	ner shall expire 10	00 ms +/- 1 ms after be	eing started.	
Proposed I	Response	Response Status W	1	
PROP	OSED ACCEPT.			
C/ 146	SC 146.4.4.2	P 136	L 17	# i-105
Graber. St	effen	Pepperl-	 ⊦Fuchs GmbH	
Comment .	Type T	Comment Status D		FFF
Modify better	the LPI timers fo synchronization b	r 10BASE-T1L to supp y using precise timers,	ort a wider range ort a wider range or synchronous with	of implementations and the symbol transmit rate.
			-	· · · · · · · · · · · · · · · · · · ·
Suggested	Remedy		-	, , , , , , , , , , , , , , , , , , , ,
Suggested Chang expire "The tii lpi_refr being s symbo	Remedy e the expiration ti 250 us (625 triple mer shall expire 6 resh_timer (line 2 started.", lpi_wake ls) after being sta	mes in the following wa ternary symbols) after 000 us (15 000 triple to 7): "The timer shall exp a_timer (line 30): "The to rted."	ay: lpi_sleep_timer being started.", lp ernary symbols) a ire 250 us (625 tri imer shall expire 3	(line 20): "The timer shall oi_quiet_timer (line 23): ter being started.", ple ternary symbols) after 250 us (625 triple ternary
Suggested Chang expire "The tii lpi_refr being s symbo Proposed I	Remedy e the expiration ti 250 us (625 triple mer shall expire 6 resh_timer (line 2' started.", lpi_wake ls) after being sta Response	mes in the following wa ternary symbols) after 2000 us (15 000 triple to 7): "The timer shall exp e_timer (line 30): "The t rted." Response Status W	ay: lpi_sleep_time being started.", lp ernary symbols) a ire 250 us (625 tri timer shall expire 2	(line 20): "The timer shall oi_quiet_timer (line 23): (ter being started.", ple ternary symbols) after 250 us (625 triple ternary
Suggested Chang expire "The til lpi_refr being s symbo Proposed I PROP Chang expire after b started	Remedy e the expiration ti 250 us (625 triple mer shall expire 6 esh_timer (line 2' started.", lpi_wake ls) after being sta Response OSED ACCEPT I e the expiration ti 250 us after being eing started.", lpi_ .", lpi_wake_time	mes in the following wa ternary symbols) after 1000 us (15 000 triple to 7): "The timer shall exp e_timer (line 30): "The t rted." <i>Response Status</i> W N PRINCIPLE. mes in the following wa g started.", pi_quiet_tir refresh_timer (line 27) r (line 30): "The timer s	ay: lpi_sleep_time being started.", lp ernary symbols) a ire 250 us (625 tri imer shall expire 2 ay: lpi_sleep_time ner (line 23): "The : "The timer shall shall expire 250 us	• (line 20): "The timer shall bi_quiet_timer (line 23): fter being started.", ple ternary symbols) after 250 us (625 triple ternary • (line 20): "The timer shall timer shall expire 6000 us expire 250 us after being after being started."

Graber, Steffen Pepperl+Fuchs GmbH

Comment Type T Comment Status D

Initializing of variable "loc_lpi_req" in TRAINING state is missing. This is necessary because loc_lpi_req is used in the PCS scrambler definition, which can change the SEND_I encoding used in SEND IDLE, thus this variable needs to be initialized before starting to transmit idle data.

SuggestedRemedy

Add "loc_lpi_req <= FALSE" to TRAINING state.

Proposed Response Response Status W

PROPOSED ACCEPT.

C/ 146	SC 146.5.5.3	P1	44	L 28	# i-297
Schicketa	anz, Dieter	Unive	ersity	of Applied Science	Reutlingen
Comment	Туре Т	Comment Status	D		PMA Electrical
There	e are 2 link equatio	ns either use one oi	defin	ie for both.	
Suggeste Insert	dRemedy after 146.7 with II	from equation 146-	10		
Proposed PROF	Response POSED REJECT.	Response Status	w		
The e the in transr	existing reference to sertion loss limit of mitters are in 1.0 V	o 146.7 is clear. Wi f a link compliant to pp mode, the limit is	nen th 146.7 s equ	he link is using the o is equation 146-10 ation 146-11.	ptional 2.4 Vpp mode, , when the

C/ 146	SC 146.11.4.4	P 165	L 26	# i-126
Graber, S	Steffen	Pepperl+Fu	chs GmbH	
Comment	Туре Т	Comment Status D		PICS

Clause 146.11.4.4 requires mandatory ticking of most of the items (besides LMF2) for a PHY. The link segment Clause provides requirements for the link segment (which are in principle not testable by the PHY) and not for the PHY itself. The PHY needs to be designed to work in conjunction with the (worst-case) link segment definition, but not meet the link segment definition by itself.

SuggestedRemedy

EEE

Please add for each support field also a N/A [] option (so that ticking this N/A field is allowed for a PHY), as e.g. done in IEEE802.3bp or make otherwise clear, that the PHY itself does not need to fulfil the link segment spec itself, but only need to work with a link segment meeting the link segment specification with the BER specified for the PHY.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Add new row to table of Major Capabilities and Options (146.11.3) P159 L21: Item: *INS Feature: Installation / cabling Subclause: 146.7 Value/Comment: Items marked with INS include installation practices and cabling specifications not applicable to a PHY manufacturer. Status: O Support: Yes [] No []

Change Status of items in 146.11.4.4 (Link Segment Characteristics) to INS:M (LMF2 becomes INS:O, RTDL:M)

Make similar changes to 147.12.3 and 147.12.4.6 and 147.12.4.7

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Clause, Subclause, page, line

Cl	146
SC	146.11.4.4

Page 7 of 23 5/23/2019 6:37:57 PM

0.447	00 4 17 0 0 4	D (7 0	1.10	"	01.4.1	00 4 17 0 0 0	D (00		
C/ 147	SC 147.3.2.4	P 179	L 10	# 1-247	C/ 14/	SC 147.3.3.6	P 183	L 5	# 1-319
Thompso	on, Geotfrey		Consultant	500	Beruto, P	ergiorgio	Canova	ech S.r.l.	
Comment The n such. Suggeste Those mode	t Type TR non-data entries in h dRemedy e codes not used in e.	Comment Status D is table should be condition CSMA/CD should be mark	nal on access m and as "Reserved	PCS ethod and marked as I" when in CSMA/CD	Comment tag [II The fu If it ch the ar Suggeste	Type T NDEX] unction DECODE(ecks RXn-4, it wo c to GOOD_ESD dRemedy	Comment Status D RXn-4) should be check uld decode one less nib state.	ing RXn-3, not RXr ble than it ought to	State Diagram I-4. when evaluating
Proposed	l Response	Response Status W			In Fig	ure 147-8 In the D	ATA state change RXn-	4 to RXn-3.	
As a MII, a as sp Comr	POSED REJECT. PHY, proper implem and the table indicate ecified in Clause 22 menter would break as a MAC-layer pa	entation of layering require es encoding of the various of this amendment. layering by specifying the rameter in other comments	es support of the codes which ma PHY act differen s.	codes provided via the y be present at the MII, tly based on what he	PROF	POSED ACCEPT.	Nesponse Status w		
C/ 147	SC 147.3.3.4	P 181	L 23	# 1-281					
Huszak, (Gergely	Kone							
Desci Desci feedir Accor DATA If it is not ye A fix i so tha the de Moree	rambler needs 17 bi rambler is fed by 4B ng. rding to the current s A state. done this way, the et locked. is to spec PCS_RX at by the time DATA escrambler locked p over it is not specifie	ts to lock and that is achier symbols, so DECODE mu specification of the PCS_R first 5 actual data symbols so, that this DECODE-and state is reached, meaning reviously. ed what descrambler is to b	ved by receiving ust be called to b X FSM, DECOD would be garbag -feed task is alre ful descrambling be fed, when DE	5 symbols. e able to do the E is called only in ge, as descrambler is ady run in PRE state, g could be done, using CODE fails.					
Suggeste	dRemedy								
1. Ad closin 2. Ad	d the following senten ng dot): ", and the re d the following new	ence to the end of the para turn value of this function i condition to the end of the	graph that ends s implementation current content	181/23 (replacing its n-dependent." of PCS_RX/PRE:					
==== IF pre <tab> END ====</tab>	ecnt > 3 THEN DECODE(RXn-3)	'n 2 olroedu ingernesster ti		is submitted by					
Pierg	iogio Beruto tagged	INDEX	ie comment that	is submitted by					
Proposed PROF	l Response POSED ACCEPT.	Response Status W							

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Clause, Subclause, page, line

C/ 147 SC 147.3.3.6 Page 8 of 23 5/23/2019 6:37:57 PM

C/ 147	SC 147	7.3.3.6	P 183	L 12	# i-278	C/ 147	SC	147.3.3.10	P 185	L 10	# i-414	
Huszak, G	Gergely		Kone			Kim, Yong	bum		NIO			
Comment	Туре Т	r Co	omment Status D		State Diagram	Comment	Туре	TR	Comment Status D		PCS	
Condi but the Impler space	Conditions on DATA->BAD_ESD and DATA->GOOD_ESD should be mutually exclusive, but those are not. Implementations would work, due to the usual if - else construct, however this leaves space for implementation-dependent divergence in PHY behavior.						Generation of Commit indication states PHY shall notify RS of received Commit by the means of MII interface in 22.2.2.8. This statement makes support of PLCA RS in 10BASE-T1S PHY not optional. PLCA RS is advertised as optional RS. The use of COMMIT (in proposed changes to CL22) requires support of the optional RS, but this					
SuggestedRemedy						subclause makes it mandatov implementation in all 10BASE-T1S PHYs.						
1. Change the condition on DATA->BAD_ESD from:						Suggested	Remed	dy				
RSCD) *					Delete	CL147	7.3.3.10 req	juirements.			
(((RXr ====	1-2 = ESD ·	+ RXn-2 = E	SDBRS) * RXn-1 != I	ESDOK) + RXn-3	= SILENCE)	Proposed I	Respor	nse	Response Status W			
to: ==== RSCD (((RXr ESDB ==== 2. Cha ====) * h-2 = ESD · RS) + RXn ange the cc	+ RXn-2 = E h-3 = SILEN(pondition on E	SDBRS) * RXn-1 != I CE) DATA->DATA from:	ESDOK * RXn-3 !	= ESD * RXn-3 !=	PROP Comm The de the sig When would implen See al	OSED enter is coding naling the PL cause nented so 215	REJECT. s incorrect. g and signal onto the MI CA is not e the signals /51 ("148.4	ling of the COMMIT and B II does not make support of nabled or not supported, F to be ignored because the .2 Reconciliation Sublayer	EACON indicati of PLCA mandat S operation sha state diagrams operation").	ons, and presentation of tory. all conform to C22, which they effect are not	
RSCD) * n-2 = ESD) + RXn-2 = E	ESDBRS) * RXn-1 !=	ESDOK) + RXn-3	= SILENCE) *	C/ 147	SC	147.3.5	P 184	L 27	# i-248	
!((RXr	n-3 = ESD ·	+ RXn-3 = E	SDBRS) * RXn-2 = E	SDOK)	,	Thompson	, Geof	frey	Independen	t Consultant		
==== to:						Comment	Туре	TR	Comment Status D		PCS	
to: ==== RSCD * !(((RXn-2 = ESD + RXn-2 = ESDBRS) * RXn-1 != ESDOK * RXn-3 != ESD * RXn-3 != ESDBRS) + RXn-3 = SILENCE) *					= ESD * RXn-3 !=	The text of this sub-clause does not meet the fundamental functional requirements of a bussed CSMA/CD system (Ref.: cl. 8.2 c)). It is just flat out incorrect. The last sentence of the 1st paragraph is technically incorrect. Statement a) is technically incorrect. Statement b) is true but technically insignificant to the operation of a MAC.						
!((RXr	n-3 = ESD	+ RXn-3 = E	SDBRS) * RXn-2 = E	SDOK)		Suggested	Remed	dy				
Note: condit	Separate c ions from [comment on DATA was s	changing all the inde ubmitted. Consider th	xes in the RXn-# i bese comments to	notation on all 3 exist gether.	Add a functio	full spe n, relia	ecification for bility and time	or Collison Detect that me ming.	ets the full Ether	met requirements for	
Proposed	Response	Res	sponse Status 🛛 🛛 🛛 🛛 🛛 🛛 🖉			Proposed I	Respor	nse	Response Status W			
PROF	POSED AC	CEPT.				PROP The pr can un Comm	OSE R oposed idersta ienter f	EJECT. d change in nd the spec ails to adeq	the comment does not co cific changes that satisfy th quately explain the problem	ontain sufficient on ne commenter. n and does not p	detail so that the CRG provide a sufficient	

remedy.

C/ 147	SC 147.3.5	P 1	84	L 30	# i-417	
Kim, Yongbu	ım	NIO				
Comment Ty	rpe TR	Comment Status	D			PCS

[CSD/Compatibility] [Collision Detect, no assurance thereof]

In IEEE 802.3 project where CSMA/CD ("half-duplex") is supported, the collision detection method always has been specified, AND the assurance of 100% collision detection has been obvious, i.e. DC bias voltage rise from two or more transmitters using current source into a known resistance, or simple logical AND function of PMA TXD enable and RXD enable. This project, however, does not specify any collision detection method except to say 1) data corruption == collision, and 2) require, without specification, find two or more stations transmitting somewhere in the network and assert CRS during that time.

We all know what collision condition is, 'two or more simulanous transmittion into a shared collision domain" or there about. It is the responsibility of the project to specify how this is done, and also assure us that collision detection confidence is at least ar PAR with prior projects. This project does not specify the collsion detection method; therefore, it is incomplete.

That said, there are tactical issues with the current draft, and I do not wish to indicate that fixing any of these tactical issues would be satisfactory to requiring 100% assurance of collision detect. But here goes.

1) "corrupted signal while transmitting" == collision. This has an obvious flaw that one station may see random bit-error (e.g. from a local noise hit) and detect collision and backsoff, the other station does not see a collision 'corrupted signal while transmitting" and completes transmission. Some receivers may see errored frames, some may not see errored frame. Result = non-determinstic behavior and lost packet.

2) Local strong TX and remote weak TX may not assure corruption.

- Max Attenuation: Attenuation of the TX signal on the nominal-length worst-case channel is 65% (3.7 db)

- Max TX power of local, so +20% P-P from 147.5.4.1 transmit output voltage is 1V +/-20% P-P. + minimum droop and power spectral density (highest power allowed).

- Min TX power of remote, so -20% P-P, with max droop.

so power diff give another ~66%. Or ~43% max interference from remote, and it could be as little as ~35% considering droop.

In addition, COL assertion within 256 bit times from the begining of a transmission seems insufficient -- a minimum collision duration is 96 bit times. A min collision + IPG would allow a new transmission to occur at 192 bit times from the initial collision. So allowing collisoin to assert up to 256 bit time later, would potentially affect the subsequent packet transmission.

Without receiver specification we have NO CLUE how receiver would behave -- whether or not data corruption would be detected from the worst case remote TX interference.. And we've opted for TX and channel spec and leave RX to implementors to *recover* tx data over channel.

From 147.3.5 Collision Detection:

"When operating in half-duplex mode, the 10BASE-T1S PHY shall detect when a

transmission initiated locally results in a corrupted signal at the MDI as a collision. When collisions are detected, the PHY shall assert the signal COL on the MII for the duration of the collision or until TX_EN signal is FALSE. The method for detecting a collision is implementation dependent but the following requirements have to be fulfilled. a) The PHY shall assert COL within 256 bit times from the beginning of a transmission when one or more stations are transmitting at the same time. b) The PHY shall assert CRS in the presence of a signal resulting from a collision between two or more stations."

SuggestedRemedy

The draft is incomplete without 100% collision detection specification. 100% defined to be as obvious as prior 802.3 CSMA/CD PHY projects. Please complete the draft by including collision detection specification.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Comment appears to comment on multiple issues.

1. With regards to the 256 bit times delay in asserting COL, at 184/35-37 change this: ====

The PHY shall assert COL within 256 bit times from the beginning of a transmission when one or more stations are transmitting at the same time.

==== to this:

The PHY shall assert COL when one or more stations are transmitting at the same time.

effectively removing "within 256 bit times from the beginning of a transmission".

This proposed resolution to comment #i-45 clarifies the possible misinterpretation of this requirement.

2. CRG disagrees with the rest of commenter's statements.

Analysis has been presented (see

http://www.ieee802.org/3/cg/public/adhoc/beruto_3cg_collision_detection.pdf) to address issues of existence, feasibility and reliability of collision detect (CD).

The highlights of this analysis relevant to this comment are:

- Target level of reliability (less-than-or-equal-to one miss-categorization per lifetime of universe) can be achieved based on the current specs.

- In the analogue domain, in presence of the specified Gaussian noise, reliable CD can be achieved. The commenter's calculation seems to confirm most of these (see commenter's figure compared to pages 4 and 5 of the study), but CRG has difficulty following commenter's calculations in full.

- Using the properties of the DME, the self-synchronizing scrambler and network geometry (reach, exclusion of the repeaters) and other properties of the Ethernet frame, the same can be achieved.

- At least one implementation exists that meet these requirements in specified noise environment.

THE PROPOSED RESPONSE OF #i-45 IS AS FOLLOWS:

>>>> PROPOSED ACCEPT.

Change the "event" in Row 6 (Lines 43-45) from: "COL input to CRS asserted"

TYPE: TR/technical required ER/editorial required GR/gener	al required T/technical E/editorial G/general	C/ 147	Page 10 of 23
COMMENT STATUS: D/dispatched A/accepted R/rejected	RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn	SC 147.3.5	5/23/2019 6:37:58 PM

SORT ORDER: Clause, Subclause, page, line

to							
"MDI input to COL asserted"	C/ 147	SC 147.3.7.	I <i>i</i>	[⊃] 185	L 19	# i-412	
	Kim, Yong	gbum	NI	0			
Change the "Output timing reference" in Row 6 (Lines 43-45) from: "Rising edge of CRS"	Comment	Type TR	Comment Stat	us D		PCS	
To: "Rising edge of COL" == Change the "event" in Row 7 (Lines 46-47) from: "COL input to CRS deasserted" to: "MDI input to COL deasserted" ==	WRT to "When the PHY is not in multidrop mode and a BEACON is received either over the MII or from the line, the state diagram in Figure 147-10 enters the DISABLE_HB state and stays there until PCS Reset is asserted,". This statement makes support of PLCA RS in 10BASE-T1S PHY (current all three of 10BASE-T1S PHYs) not optional. PLCA RS is advertised as optional RS. The recognition of BEACON (in proposed changes to CL22) requires support of the optional RS, but this clause does not specify the optional RS bevior. This and two other shalls in this subclause makes it mandatoy implementation in clutopRSE_T4S_DHYc						
Change the "Output timing reference" in Row 7 (Lines 46-47) from:	Suggestee	dRemedy					
To:	Delete CL147.3.7.1 requirements.						
"Rising edge of COL"	Proposed	Response	Response Statu	ıs W			
http://www.ieee802.org/3/cg/comments/Comment_i-45_Baggett_3cg_Table_147- 6_typo_errors.pdf <<<<	PROF Comn The d	POSED REJECT	ct. naling of the COMN	/IT and BE	ACON indicatio	ns, and presentation of	
C/ 147 SC 147.3.7.1 P 185 L 19 # [i-413	the sig	gnaling onto the	MII does not make	support of	PLCA mandato	ry.	
Kim, Yongbum NIO	would	cause the signa	ls to be ignored be	cause the s	state diagrams f	hev effect are not	
Comment Type ER Comment Status D PCS " a BEACON is received" the word "BEACON" is used without any x-reference, and the	implei See a	mented. Iso 215/51 ("148	.4.2 Reconciliation	Sublayer o	peration").		
nature of 'BEACON' (signal?, state?, interface?, etc) is found in other clauses.	C/ 147	SC 147.3.7.	I.1 /	[⊃] 185	L 51	# i-415	
SuggestedRemedy	Kim, Yong	gbum	NI	0			
Please insert x-ref to 'BEACON'.	Comment	Type TR	Comment Stat	us D		PCS	
Proposed Response Response Status W	WRT to "rx_cmd <= 'COMMIT' when a COMMIT indication is generated as specified". This statement makes support of PLCA RS in 10BASE-T1S PHY not optional. PLCA RS is advertised as optional RS. The use of COMMIT (in proposed changes to CL22) requires support of the optional RS, but this clause does not specify the optional RS bevior. This and two other shalls in this subclause makes it mandatoy implementation in all 10BASE- T1S PHYs.						
PROPOSED ACCEPT IN PRINCIPLE. Change this:							
When the PHY is not in multidrop mode and a BEACON is received either over the MII or from the line							
==== to this:	Suggestee	dRemedy					
====	Delete	e CL147.3.7.1.1	requirements.				
When the PHY is not in multidrop mode and a BEACON request is received from the MII	Proposed	Response	Response Statu	ıs W			
(See Table 22-2) or a BEACON code-group is received from the line (See Table 147-1)	PROF Comn The d the sig When would imple See a	POSED REJECT nenter is incorre- ecoding and sigu gnaling onto the the PLCA is not cause the signa mented. Iso 215/51 ("148	t. t. haling of the COMM MII does not make enabled or not sup Is to be ignored be .4.2 Reconciliation	AIT and BEA support of oported, RS cause the s Sublayer o	ACON indicatio PLCA mandato operation shal state diagrams to peration").	ns, and presentation of ry. I conform to C22, which hey effect are not	
TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/g	general			C/ 14	7	Page 11 of 23	

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SC 147.3.7.1.1 5/23/2019 6:37:58 PM SORT ORDER: Clause, Subclause, page, line

C/ 147	SC 147.4.2	P 191	L 11	# i-253	C/ 147 SC 1
Thompson	n, Geoffrey	Independent Co	onsultant		Graber, Steffen
Comment	Type ER	Comment Status D		PMA	Comment Type
In Fig. One fi	147-13 the two f gure shows the a	igures are confusing because to cual (idealized) signal transition	they are vastly ons and the ot	<pre>/ different time scales. her shows the LF</pre>	Test mode 3 - test, only a trai
Currenter					SuggestedRemedy
Suggested	renedy	the figure I auggest grov see	ling within the	transmission (Linkson	Test mode 3 -
what i wiggly	s being depicted vertical discontir	on the second fig. Is 2 time sca nuity break in the middle of T1)	ales, then thei	r should be a two	Proposed Respons PROPOSED A
Proposed	Response	Response Status W			C/ 147 SC 1
PROF Comm	OSED REJECT. nenter has not pro	ovided necessary and sufficien	t information f	or the problem and the	Thompson, Geoffre
fix it m	nay deserve.				Comment Type
C/ 147	SC 147.5.1	P 193	L 1	# i-254	"and sent to scope of this p
Thompson	n, Geoffrey	Independent Co	onsultant		reconciliation s
Comment	Type ER	Comment Status D		PMA Electrical	"Physical Laye
This c require	lause and its sub ements for 10BA	-clauses don't say anything us SE-T1S Physical Layer implem	eful to specify rentation. Say	the conformance ing something "may" be	to move to a m the twenty yea
confoi	mance requireme	ent.		an element of a	SuggestedRemedy
Suggested	dRemedy				Either define to
Repla	ce with: Applicat	ions for the specified device co	ommonly have	additional	intended in eac
requir	ements that limit	its conducted radio frequency	emission and	its susceptibility to	Proposed Respons
electro	omagnetic interfe	rence coupling to the cabling s is standard.	ystem. Such	requirements are	PROPOSED R
Proposed	Response	Response Status W			draft. The MII i
PROF	OSED REJECT.				Physical Layer
The pi can ur	roposed change inderstand the spe	n the comment does not conta ecific changes that satisfy the c	in sufficient de commenter.	etail so that the CRG	when PLCA fu
It is ur Repla applic	nclear exactly whi cing the entire su ation of the physi	ch part of the text the commer bclause and its subclauses wo cal layer devices specified in th	iter wishes to ould remove us nis clause.	replace. seful information for the	

C/ 147	SC	147.5.2	P 193	L 33	# i-136			
Graber, Ste	ffen		Pepperl+Fuc	hs GmbH				
Comment T	ype	т	Comment Status D		Test Mode			
Test mode 3 - Transmitter distortion test and PSD mask (there is no transmitter distortion test, only a transmit PSD mask specification within Clause 147)								
SuggestedF	Reme	dy						

Transmitter PSD mask

Proposed Response	Response Status	w	
PROPOSED ACCEPT.			

Cl 147	SC 147.5.5.1	P 19	96 L	40	#	i-255
Thompson, G	eoffrey	Indepe	endent Consult	tant		
Comment Tvp	e ER	Comment Status	D			PMA Electrical

the MII during normal..." Because of the inclusion PLCA as being within the project the term MII is ambiguous in the context of this draft as there are two sublayers. This a result of the further confusion between the "PHY" and the er". Originally the RS was supposed to a functionally transparent block which interfere with access at all and (b) allowed the old physical interface (AUI) nore logical division point (MII) in line with the evolution of technology over rs from 1973 to 1993.

wo terms, one for each RS (e.g. DMII, AMII) or clearly state which RS is ch use of MII in this project's draft.

se Response Status W

REJECT.

incorrect pointing out that the term MII is ambiguous in the context of this is the interface between the PHY and the RS. which both belong to the r. In the context of C147 the MII is supposed to work with either C22 RS or S (PLCA) seamlessly. C148 RS is specified to behave exactly as C22 RS inction is disabled.

C/ 147 SC 147.5.5.1 Page 12 of 23 5/23/2019 6:37:58 PM

C/ 147	SC 147.5.6	P 197	L 18	# i-256	C/ 147	SC 147.5.6	P 197	L 27	# <u>i-258</u>	
Thompsor	n, Geoffrey	Independer	nt Consultant		Thompson	Thompson, Geoffrey Independent Consultant				
Comment	Type TR	Comment Status D		PMA Electrical	Comment	Type TR	Comment Status D		PMA Electrical	
l don't option	understand how al"on a PMA v	the following text can be to where transmit is connected	ue: "The PMA loc d to receive.	al loopback function is	The participation forme	aragraph seem d to be to be de	s to assume that what is on the ecoded and converted to data.	ne receive PMA is Since it is the s	s sufficiently well- um of two or more	
Suggested	Remedy				Signai		alla assumption.			
Please	e clarify. I think y	ou mean "The PMA local I	oopback test func	tion is optional."	Suggester	aRemedy				
Proposed	Response	Response Status W			Add the following text: "During a collision (i.e. either a transmit collision or a receive collision) no assumptions whatsoever can be made about the validity or decodability of the waveform present at the input of the receiver."					
The C	RG disagrees wit	h the commenter - text is	clear - the PMA lo	cal loopback function is						
option	al.				Proposed	Response	Response Status W			
What this test mode does in - half-duplex mode, is overriding part of the condition on the single-ended arrow that point into WAIT_SYNC in "Figure 147-7-PCS Receive state diagram", allowing receiving back transmitting station's own data. - full-duplex mode, is suspending functionality that would prevent the transmitting station from receiving its own data.					PROF Comn detect Text h so on) The P	POSED REJEC nenter is incorre ion. here makes no a h in the presenc MA Loopback i	T. ect, as this subclause refers to assumption with regards to the e of collision. is used for diagnostic purpose	PMA loopback i e received signal s, and it is option	mode, not to collision (its shape, validity and nal, thus current text is	
C/ 147	SC 147.5.6	P 197	L 24	# i-257			.			
Thompsor	n, Geoffrey	Independer	nt Consultant		C/ 147	SC 147.5.6	P 197	<i>L</i> 31	# i-259	
Comment	Type TR	Comment Status D		PMA Electrical	Thompson	n, Geoffrey	Independent	t Consultant		
The w segme	ord "unterminate ent and other MA	d" here implies that loopba U connected but there is a	ck only works if th requirement of so	ere is no compliant link ome sort for some circuit	<i>Comment</i> Parag	<i>Type</i> TR raph 4 is not tru	Comment Status D ue. Add conditional text to mal	ke it true.	PMA Electrical	
charad	cteristics at the M	DI to guarantee the echo.			Suggested	dRemedy				
Suggested	lRemedy				Prece	de the current t	text with: "In the absence of co	ollision"		
Clarify	and specify				Proposed	Response	Response Status W			
Proposed	Response	Response Status W			PROF	POSED REJEC	T.			

PROPOSED REJECT.

In full-duplex PMA loopback only works with unterminated link segment (If the line is terminated, then you don't get any signal back, the reflection coefficient is 0).

PMA loopback mode is meant for serving diagnostics purposes, used in a special mode, therefore collision is not a concern there.

This subclause is about "PMA Local Loopback", so it is a means for the MAC client to verify underlying circuitry. In case of collision, COL is raised and MAC client can act accordingly.

CL 147	SC 4	47.6	P 107	1 20	# : 260	CI 117	50	147 0 2	P 200	1 52	# : 402
C/ 14/	301	47.0	F 197		# 1-260		30	147.0.2	F 200	L 3 Z	# [-403
Inompsor	n, Geoffr	ey	Independent	Consultant		Kim, Yong	bum		NIO		
Comment	Туре	TR	Comment Status D		Management	Comment	Туре	TR	Comment Status D		Mixing Segmen
I don't implen configu to test Suggested	underst nentatio uration) their ex	and how n. I think implemei istence a /	the last sentence of this para a compliant (as opposed to ntation is required to have co nd function. I don't see how	agraph works in interoperable in ntrol bits. Ifso, you get there fr	an actual some fixed there has to be a way om the present text.	The mi in 147. betwee based this sta	xing se 7.2 en any on cab	egment sl two MDI ling that s t, this spe	hall meet the return loss chara attachment points. And from supports up to at least 8 nodes ecification is requiring 28 (com	acteristics spec 147.8 "A mixir s and 25 m in r bination of any	fied for link segments ng segment is specified each". From both of r two) measurement
Put in a testable requirement to access the configurable aspects.						assura	taken. And any added nodes requires all combinations to be measured again, and with no assurances that the prior conformant MDI may fall out of range.				
Proposed	Respon	se	Response Status W			Suggested	Remed	ly .		-	
PROPOSED REJECT. The identical language is used in several 802.3 clauses with respect to control, in particular					Provide better medium specifcation and cable design considerations that can be followed assured scaleable MDI and medium construction.						
in clau	ise 45 w	hich gove	erns the registers. While the implementation of	f the MDIO inte	rface is optional and	Proposed Response Response Status W PROPOSED REJECT. The proposed change in the comment does not contain sufficient detail so that the CRG can understand the specific changes that satisfy the commenter.					
an equ	uivalent	mechanis	m is recommended. the imp	lementation is N	NOT required to have						
the con encodi	ntrol bits	s. An equ ts, or with	ivalent means of control and strap pins) would be permit	configuration (e ed. The existing	e.g., with a different g text allows this.						etail so that the CRG
C/ 147	SC 1	47.8.1	P 199	L 52	# i-402	Furthe explan	r, the C atory te	ERG disaget with the set with the set of the	grees with the commenter, as ne specification ("is specified"	the commenter vs. "shall meet	r mistakes 147.8 .").
Kim, Yong	jbum		NIO			It is co	mmon	practice t	for cabling systems to be spec	ified to be com	pliant by design rather
Comment	Туре	TR	Comment Status D		Mixing Segment	been s	pecifie	d based o	on measurements indicating the	at they suppor	t the described
The m segme	ixing se ents in 1	gment sh 47.7.1	all meet the insertion loss ch	aracteristics sp	ecified for link	topolog	jies, ar	n existend	ce proof that design is feasible		
based this sta	on cabl	ing that s	upports up to at least 8 node cification is requiring 28 (con	s and 25 m in r	each". From both of two) measurement						

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Clause, Subclause, page, line

taken. And any added nodes requires all combinations to be measured again, and with no

Provide better medium specification and cable design considerations that can be followed

The proposed change in the comment does not contain sufficient detail so that the CRG

It is common practice for cabling systems to be specified to be compliant by design rather than necessarily measured for each instance. Further, the characteristics required have been specified based on measurements indicating that they support the described

Further, the CRG disagrees with the commenter, as the commenter mistakes 147.8

assurances that the prior conformant MDI may fall out of range.

can understand the specific changes that satisfy the commenter.

explanatory text with the specification ("is specified" vs. "shall meet.").

Response Status W

assured scaleable MDI and medium construction.

topologies, an existence proof that design is feasible.

SuggestedRemedv

Proposed Response

PROPOSED REJECT.

C/ 147 SC 147.8.2

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Mixing Segment

C/ 148	SC 148	P 214	L 1	# <u>i-393</u>
Kim, Yongb	um	NIO		
Comment T	vpe TR	Comment Status D		PLCA SCOPE

Comment Type TR Comment Status D

[CSD] CSD/Economic Feasibility statements in CSD document is not valid for CL148 PLCA operation.

The project CSD states that "

The cost factors for Ethernet components and systems are well known. The proposed project may introduce new cost factors which can be quantified.

-The reduction in the number of legacy networks requiring specialized components, expertise, and gateways in the targeted markets is anticipated to result in a significant drop in both installation and operational costs."

While the cost factors for Ethernet is well known, this project introduces the new requirements that has not been a part of Ethernet. This project requires each node to be assigned a unique and sequential (as in little to no gaps in number sequence) node identifier to be assigned to each PHY, and allocate and assigna a special node identifier value of zero to a 'master node' that is responsible for sending special 'beacon' frame. This project requires that the configuration is assured (outside of this draft standard) that node identifier of zero is present, and only one of such node identifier is present. This operation described in this project cannot reasonably assume that this new behavior requirement could inherit "well known Ethernet cost factors". Also this project cannot reasonably assert assert "drop in both installation and operational costs" when additional configuration of node assignment and behaviors are required and without any specification on how they are done.

CSD/Economic Feasibility with regard to other clauses, other than CL148, are not in question.

SuggestedRemedy

CSD/Economic Feasibility with regard to CL148 PLCA operation is no longer valid and grossly incorrect. Appropriate changes to the CSD/Economic Feasibility to be made and to be approved.

Proposed Response Response Status W

PROPOSED REJECT.

Commenter improperly refers to CSDs which are not in scope for a Standards Association Ballot.

Additionally, commenter is incorrect. A number of individuals with a broad spectrum of affiliations agreed on an objective for this. The Criteria for Standards Development (e.g., broad market potential) apply to the entire standard:

====

Each proposed IEEE 802 LMSC standard shall have broad market potential. At a minimum, address the following areas: a) Broad sets of applicability.

B) Multiple vendors and numerous users. ____

The existing 802.3cg broad market potential speaks to 10 Mb/s single-pair Ethernet in industrial, automotive, and intra-system applications, and the number and breadth of

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Clause, Subclause, page, line

individuals and companies which have expressed interest in the standard.

Furthermore the commenter is technically incorrect in his assertions: [1] PLCA node IDs do not need to be sequential

[2] There is no such description of master node in the draft

[3] The BEACON is not a frame, it is a 20 bit long signal on the line which carries no information apart from its own presence. It isconceptually not different from IDLE signals which most physical layers use to retrieve clocking informatio from.

> C/ 148 SC 148

Page 15 of 23 5/23/2019 6:37:58 PM

C/ 148	SC 148	P 214	L 1	# i-390
Kim, Yongbu	ım	NIO		
Comment Ty	pe TR	Comment Status D		PLCA SCOPE

[PAR] PLCA Reconsciliation Sublayer (RS) contain specifications that handles contention avoidance and collision handling as well as access control. Media Access Control (MAC) specification is not a part of this Physical Layer project, as stated in this PAR scope: "5.2.b. Changes in scope of the project: Specify additions to and appropriate

modifications of IEEE Std 802.3 to add 10 Mb/s Physical Layer (PHY) specifications and management parameters for operation, and associated optional provision of power, using a single balanced pair of conductors.", whereas the MAC definition is in CL 4.1.1 of IEEE 802.3-2018 states:

"...The MAC sublayer defines a medium-independent facility...b) Media Access Management

1) Medium allocation (collision avoidance)

2) Contention resolution (collision handling).."

Furthermore, Reconsiliation Sublayer, as defined in the same parent document IEEE 802.3-2018, in 1.4.425 states "1.4.425 Reconciliation Sublayer (RS): A mapping function that reconciles the signals at the Media Independent Interface (MII) to the Media Access Control (MAC)-Physical Signaling Sublayer (PLS) service definitions. (See IEEE Std802.3, Clause 22.)". PLCA RS claims to be an RS, but does NOT simply map PLS to MII, but performs 1) Medium allocation (collision avoidance) -- as the title says ("physical layer Collision Avoidance), 2) Contention resolution (collision handling). PLCA performs Medium Access control function (MAC).

SuggestedRemedy

Align this draft to the approved PAR (14-May-2018)by deleting CL148 in its entirety (pages 214 through 234, inclusive) and any changes associated with such deletion. Alternatively, submit a new PAR that substantialy reflect this project content, including a MAC specification in the scope, and provide approved PAR with such revised scope. If a new PAR is submitted with MAC specification in scope, then re-open and seek technical contributions with regards to the new scope.

Proposed Response Response Status W

PROPOSED REJECT.

Commenter incorrectly posits that the Clause 148 PLCA RS is a new MAC. It does not meet the requirements for a MAC, and, leaves the MAC functionality with Clause 4, which, in fact, it could not work without. Commenter incompletely quotes IEEE Std 802-2014 4.1, paragraph 6 leading to incorrect conclusions.

See www.ieee802.org/3/cg/public/Jan2019/Tutorial_cg_0119_final.pdf.

See also http://www.ieee802.org/3/cg/public/adhoc/brandt_020619_3cg_01a_adhoc.pdf

C/ 148 S	SC 148	P 214	L 1	# i-48
Grow, Robert		RMG Con		- <u></u>
Comment Type	9 GR	Comment Status D		PLCA_SCOPE

This clause specifies functionality that is outside the scope of the PAR. The result of out of scope content is that all interested parties may not have been aware of actual content and as a result enticed to join the ballot group.

SuggestedRemedy

Either delete the clause and related content, or revise the PAR, reform the ballot group, and restart Standards Association ballot.

Proposed Response Response Status W

PROPOSED REJECT.

The commenter does not state the reasons that led him to this conclusion. Looking at a similar comment from the same commenter (i-47), the editor assumes he is referring to the incorrect assumption that PLCA is a new MAC. Response to comment i-47 is: PROPOSED REJECT. The CRG disagrees with the commenter that PLCA is a MAC protocol.

Several evidence has been provided, and a tutorial has been given, to prove that PLCA is in fact a normal physical layer function.

See http://www.ieee802.org/3/cg/public/Jan2019/Tutorial_cg_0119_final.pdf

The fundamental reason for PLCA to be a physical layer function is that it only works in conjunction with the CSMA/CD MAC specified in Clause 4 (without any modification to Clause 4 itself).

PLCA provides carrierSense and collision detection information to the MAC by the means of the existing PLS_CARRIER.indication and PLS_SIGNAL.indication primitives which is what the Reconciliation Sublayer (which is part of the physical layer) is supposed to accomplish.

The commenter's statement "it doesn't change the fact that the functions are medium access control" righfully deserves an appropriate answer, which is more conceptual rather than purely technical.

The PLCA working principle is to detect collisions (concurrent transmission of multiple stations on a shared network segment) in a logical sense. As an example, 10BASE-2 and 10BASE-5 detect concurrent transmissions by checking the DC voltage level on the shared media, that is detecting the superposition of multiple (not decodable) signals on the line. PLCA detects the very same concurrent transmissions by aligning the data conveyed by the local MAC to the unique transmit opportunity of the node and checking for concurrent reception of a packet. In such a way the collision does not result in "corrupting" the signal on the media. That is, the packet currently being transmitted is not interrupted, thus yielding the advertised network performance enhancement.

This is also in line with the ISO/OSI principle by which a layer may enhance the service it provides to the upper layer.

TYPE: TR/technical required ER/editorial required GR/generation	al required T/technical E/editorial G/general	C/ 148	Page 16 of 23
COMMENT STATUS: D/dispatched A/accepted R/rejected	RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn	SC 148	5/23/2019 6:37:58 PM
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See http://www.ieee802.org/3/cg/public/adhoc/brandt 020619 3cg 01a adhoc.pdf

Moreover the commenter is unclear as PLCA + CSMA/CD is obviously not identical to 802.4 Token Bus, and it is unclear what specification the commenter is referring to. For example. PLCA does not define any handshake protocol between nodes, it does not generate packets and there is no concept of arbitration of the media. Additionally, CSMA/CD nodes with PLCA enabled interoperate properly with non-PLCA enabled nodes on the same network segment (without yielding the advertised gain in performance in this case). That would not be possible if nodes with PLCA enabled were not, in fact, using the CSMA/CD MAC protocol. See

http://www.ieee802.org/3/cg/public/Sept2018/beruto_3cg_mixing_PLCA_with_non_PLCA_e nabled nodes r1.2.pdf.

C/ 148	SC 148	P 214	L 1	# i-47
Grow, Rober	t	RMG Con		- <u></u>
Comment Tv	pe TR	Comment Status D		PLCA SCOPE

Comment Type TR

The PLCA protocol is a MAC protocol. It is virtually identical to a token bus protocol (shared medium) I specified years ago. This clause violates 802.3 layering, and though considerable effort has been made to place this in the Reconciliation Sublayer, it doesn't change the fact that the functions are medium access control.

SuggestedRemedy

Delete Clause 148 and related text.

Proposed Response Response Status W

PROPOSED REJECT.

The CRG disagrees with the commenter that PLCA is a MAC protocol.

Several evidence has been provided, and a tutorial has been given, to prove that PLCA is in fact a normal physical laver function. See http://www.ieee802.org/3/cg/public/Jan2019/Tutorial cg 0119 final.pdf

The fundamental reason for PLCA to be a physical laver function is that it only works in conjunction with the CSMA/CD MAC specified in Clause 4 (without any modification to Clause 4 itself).

PLCA provides carrierSense and collision detection information to the MAC by the means of the existing PLS_CARRIER.indication and PLS_SIGNAL.indication primitives which is what the Reconciliation Sublayer (which is part of the physical layer) is supposed to accomplish.

The commenter's statement "it doesn't change the fact that the functions are medium access control" righfully deserves an appropriate answer, which is more conceptual rather than purely technical.

The PLCA working principle is to detect collisions (concurrent transmission of multiple stations on a shared network segment) in a logical sense. As an example, 10BASE-2 and 10BASE-5 detect concurrent transmissions by checking the DC voltage level on the shared media, that is detecting the superposition of multiple (not decodable) signals on the line. PLCA detects the very same concurrent transmissions by aligning the data conveyed by the local MAC to the unique transmit opportunity of the node and checking for concurrent reception of a packet. In such a way the collision does not result in "corrupting" the signal on the media. That is, the packet currently being transmitted is not interrupted, thus yielding the advertised network performance enhancement.

This is also in line with the ISO/OSI principle by which a layer may enhance the service it provides to the upper layer.

See http://www.ieee802.org/3/cg/public/adhoc/brandt 020619 3cg 01a adhoc.pdf

Moreover the commenter is unclear as PLCA + CSMA/CD is obviously not identical to 802.4 Token Bus, and it is unclear what specification the commenter is referring to. For example. PLCA does not define any handshake protocol between nodes, it does not

TYPE: TR/technical required ER/editorial required GR/gene	ral required T/technical E/editorial G/general	C/ 148	Page 17 of 23
COMMENT STATUS: D/dispatched A/accepted R/rejected	RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn	SC 148	5/23/2019 6:37:58 PM

SORT ORDER: Clause, Subclause, page, line

generate packets and there is no concept of arbitration of the media. Additionally,

CSMA/CD nodes with PLCA enabled interoperate properly with non-PLCA enabled nodes on the same network segment (without yielding the advertised gain in performance in this case). That would not be possible if nodes with PLCA enabled were not, in fact, using the CSMA/CD MAC protocol. See

http://www.ieee802.org/3/cg/public/Sept2018/beruto_3cg_mixing_PLCA_with_non_PLCA_e nabled_nodes_r1.2.pdf.

C/ 148 SC 148.1 P 214

L 12

Thompson, Geoffrey Comment Type ER Independent Consultant

PLCA SCOPE

i-265

The first sentence refers to PLCA as though it is already a familiar, well understood and well specified protocol that is familiar to the reader by the time he gets to clause 148 of IEEE Std. 802.3. Such is hardly the case.

SuggestedRemedy

Add the following text to the last paragraph: "PLCA modifies the CSMA/CD shared media access method so that assured access is provided via the collision free round robin protocol specified in this clause." This is a necessary but not sufficient addition. We'll leave further detail requirements to later in the clause..

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Change "This clause specifies the optional Physical Layer Collision Avoidance (PLCA) capabilities.

PLCA is defined for half-duplex mode of operation only. The PLCA RS is specified for operation with the

PHY defined in Clause 147 (10BASE-T1S).

 $\ensuremath{\mathsf{PLCA}}$ is designed to work in conjunction with CSMA/CD and can be dynamically enabled or disabled via

management interface."

to

"This clause specifies a reconciliation sublayer to provide optional Physical Layer Collision Avoidance (PLCA) capabilities. The PLCA RS is specified for operation with Clause 147 (10BASE-T1S) PHYs operating in half-duplex multidrop mode. When used as a reconcilation sublayer, it aligns data from the MAC with transmission opportunities of the physical layer and maps the physical layer signals to PLS primitives towards the MAC. The use of PLCA-enabled physical layers in CSMA/CD half-duplex shared-medium networks provides enhanced performance relative to CSMA/CD without PLCA by avoiding corruption of signals on the media itself. PLCA-enabled nodes can coexist with nodes without PLCA enabled on the same mixing segment, all using 802.3 CSMA/CD."

C/ 148	SC 148.2	P 214	L 38	# i-419
Seaman, Mic	hael	MICK SEAMA	AN .	- <u></u>
Comment Ty	be G	Comment Status D		PLCA_PRIORITIES

The utility of PLCA would be considerably improved, and emerging application areas (e.g. industrial, automotive) if the BEACON mechanisms provided simple support for priority. Two priority levels would be sufficient to support a deterministic (known bounded latency) service in addition to best effort. Four priority levels may be desirable, though I would not advocate more without detail uses case analysis.

SuggestedRemedy

Specify the BEACON to allow inclusion of a priority indication as a follow on project if not part of the present effort.

Proposed Response Response Status W

PROPOSED REJECT. Big Ticket Item - PLCA_PRIORITIES.

Communication of 802.1 priorities to the physical layer in an 802.3 PHY would require modification of the 802.3 MAC Service Access Point definition, and hence the MAC layer. While potentially desirable, this would be outside the scope of a physical layer project and the approved PAR.

C/ 148	SC 148.2	P 214	L 42	# i-269
Thompson, (Geoffrey	Independent (Consultant	
Comment Ty	rpe ER	Comment Status D		PLCA_ID

There needs to be a little more discussion of local_ID assignment, how it doesn't appear externally and that it is fully contained within the segment.

SuggestedRemedy

Add the following text at the end of the first paragraph: The local_ID assignment value doesn't appear externally or in the payload packet format. The local_ID assignment value is fully contained within the local bussed segment.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Add the following text at the end of the first paragraph at line 43: The node ID assignment value does not appear externally or in the payload packet format. The node ID assignment value is fully contained within the local collision domain.

C/ 148 SC 148.2

C/ 148 SC	348.2	P 214	L 42	# i-268
Thompson, Geo	ffrey	Independent (Consultant	
Comment Type	TR	Comment Status D		PLCA ID

This lack of a complete specification for full functionality is completely unprecedented for 10 Mb/s Ethernet and a major shortcoming. Plug and work, historically, has been a major factor in the success of Ethernet in face of the competition (which usually required a bunch of configuration before it would go on-line). Two examples of this in the history of Ethernet come to mind: (1) In the early days of 10 Mb/s full duplex and 100BASE-T early implementations of AutoNegotiation did not work very well. The failure of the promised plug 'n' play was a major marketing issue. (2) In the very first (3 Mb/s) version of Ethernet, DTEs only had 8 bit addresses. They had to have their addresses manually configured with push-on test leads as part of their installation process. This made the customer (most of whom were EEs or Computer Scientists) installation not possible and a technician had to be involved. Major network management problem.

SuggestedRemedy

Come up with and require availability of an automatic configuration app. No reason one shouldn't be able to use the CSMA/CD capability to (1) identify the stations on the local segment and (2) hand out the unique assigned node ID to each DTE.

Proposed Response Response Status W

PROPOSED REJECT.

Commenter provides insufficient information for a sufficient response. Defining an "automatic configuration app" may be a desirable feature, but appears to involve higher layer protocols and algorithms for configuration of the specified management parameters, which the CRG believes would be outside the scope of the

C/ 148	SC 148.4.5.1	P 221	L 9	# i-373
Law, David		Hewlett Packa	ard Enterprise	
Comment Ty	be TR	Comment Status D		State Diagram

There appears to be a conflict, or at least a lack of clarity, between the Figure 148-3 'PLCA Control state diagram' and the Figure 148-4 'PLCA DATA state diagram' in respect to which controls the encoding being placed on the MII transmit signals TXD, TX_EN and TX_ER by the PLCA RS.

As an example, when the PLCA Control state diagram is in the SEND_BEACON state, one of the actions is tx_cmd <= BEACON, which based on subclause 148.4.5.2 should result on the BEACON encoding defined in Table 22-1 being placed on TXD, TX_EN and TX_ER. At the same time that the PLCA Control state diagram is in the SEND_BEACON state, it would appear that the PLCA DATA state diagram is in the IDLE state, and the actions within the IDLE state include TXD <= 0000 and TX_EN <= FALSE. Hence we have the two different state diagrams requiring different values to be placed on TXD and TX_EN at the same time resulting in a conflict.

Perhaps the intent is to have both state diagrams assign values to TXD and TX_EN, but that isn't clear to me as one state diagram uses tx_cmd and the other TXD and TX_EN .

In addition, the states within the PLCA Control state diagram that have actions assigning values to tx_cmd, and therefore potentially changing the values of TXD and TX_EN, are not synchronised to TX_CLK through the MCD variable in that way that actions that assign values to TXD and TX_EN are in the 'PLCA DATA state diagram'. Not synchronising state changes in the PLCA Control state diagram change the value of tx_cmd could result in transitions in TXD and TX_EN that do not meet the timing requirements of IEEE Std 802.3-2018 subclause 22.3.1 'Signals that are synchronous to TX_CLK'.

Finally, it isn't clear to me why TX_ER would be an input to Figure 148-4 'PLCA DATA state diagram'. I was wondering if it was meant to be a plca_txer variable derived from the MAC service interface, similar to the plca_txen, but the MAC service interface doesn't provide the ability for the MAC to pass transmit errors to the RS.

One, I assume unintended, consequence of the use of TX_ER is that when the PLCA RS with local_nodeID=0 is transmitting a BEACON, and therefore TX_ER = 1 (see Table 22-1), and then a transmission from the local MAC is started, it would appear that this transmission is discarded. This is due to the PLCA DATA state diagram transitioning from the IDLE state to the HOLD state due to plca_txen, then to the ABORT state, which sets packetPending to FALSE discarding the packet, as a result of the transition condition (recv_timer_not_done * MCD * !committed * TX_ER * !receiving) being true.

SuggestedRemedy

[1] Clarify the source of TXD and TX_EN as either the Figure 148-3 'PLCA Control state diagram' or the Figure 148-4 'PLCA DATA state diagram'. If the intent is that both should source TXD and TX_EN, suggest that tx_cmd should be replaced with TXD, TX_EN and TX_ER in the respective PLCA Control state diagram states.

[2] Ensure that MCD is used in any condition that results in a change of value in TXD,

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general C/	/ 148	Page 19 of 23
COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SC	C 148.4.5.1	5/23/2019 6:37:58 PM

SORT ORDER: Clause, Subclause, page, line

TX_EN or TX_ER in the PLCA Control state and PLCA DATA state diagrams.

[3] Clarify the use of TX_ER in the transition condition from the HOLD to the ABORT state in the PLCA DATA state diagram.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

At page 223, line 23 replace

"tx_cmd Command to be conveyed to the PHY via MII. When set to NONE, no special signaling shall be conveyed. When set to BEACON or COMMIT, respective commands shall be conveyed to MII as specified in 148.4.4.1.1 and 148.4.4.1.2. Values: NONE, BEACON or COMMIT"

with:

"tx_cmd Command for the PLCA DATA State Diagram to convey to the PHY via the MII. Values: NONE, BEACON or COMMIT"

At page 225, line 36, replace "TX_ER" with "plca_txer".

Apply the following changes, in this order exactly:

1. In figure 148-4 replace all occurrences of "TX_ER" with "plca_txer".

2. In figure 148-4, in the NORMAL state, add "TX_ER <= plca_txer"

3. In figure 148-4, in the IDLE state, add "TX_ER <= ENCODE_TXER(tx_cmd). Replace "TXD <= 0000" with "TXD <= ENCODE_TXD(tx_cmd)"

4. In figure 148-4, in the RECEIVE, PENDING and WAIT_MAC states, add "TX_ER <= ENCODE TXER(tx cmd). Add "TXD <= ENCODE TXD(tx cmd)"

5. In figure 148-4, in the HOLD, ABORT, TRANSMIT and FLUSH states, add "TX ER <=

plca_txer".

6. In figure 148-4, in the HOLD and ABORT states, add "TXD <= 0000".

At page 228, line 10, add:

"plca_txer the conditions for generating plca_txer are the same as defined in 22.2.1.6 and 22.2.2.5 for the TX_ER MII signal. Values: TRUE or FALSE"

Replace content of subclause 148.4.6.3 with the following text: "ENCODE_TXER This function takes as its argument the tx_cmd variable defined in 148.4.5.2.

It returns TRUE if tx_cmd is BEACON or COMMIT. Otherwise it returns the value of the plca_txer variable, defined in 148.4.6.2

ENCODE_TXD

This function takes as its argument the tx_cmd variable defined in 148.4.5.2.

If tx_cmd is BEACON, the return value is the TXD encoding defined in Table 22-1 for the BEACON request.

If tx_cmd is COMMIT, the return value is the TXD encoding defined in Table 22-1 for the COMMIT request.

Otherwise, the return value is 0000.

Replace content of subclause 148.4.3.6 with the following text: "Generation of TX_ER shall comply with the PLCA Data State Diagram specified in 148.4.6.1"

Apply the following modifications to the PICS:

At page 232, line 39, replace "Specified in 22.2.1.6" with "Specified in "148.4.6.1" At page 233, line 44, delete the CON3 line.

C/ 148	SC 148.4.5.4	P 224	L 32	# <u>i-376</u>
Law, David	d	Hewlett Packa	ard Enterprise	
Comment	Type TR	Comment Status D		Timers
This s 2018 s For ex	ubclause specifies subclause 1.4.160 ample, for 100BA	the duration of the beacon_ 'bit time' states that 'The bit SE-T the bit time is 10-8 s o	_timer as 20 bit time is the rec r 10 ns.'. As a i	times. IEEE Std 802.3- iprocal of the bit rate. results in a duration of

For example, for 100BASE-T the bit time is 10-8 s or 10 ns.'. As a results in a duration of beacon_timer is exactly 20 x reciprocal(10 Mb/s) = 2000 ns. This would seem to result in a requirement for infinite precision and make a beacon_timer of 2000 + 10-15 ns non-conformant.

SuggestedRemedy

Provide a tolerance for the beacon_timer, burst_timer, commit_timer (subclause 148.4.6.4), hb_send_timer (subclause 147.3.7.1.2), hb_timer (subclause 147.3.7.1.2) and link_hold_timer (subclause 147.3.7.2.3)

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

At page 224, line 32, append: "Tolerance: +/- 1/2 bit time"

At page 224, line 38, append: "Tolerance: +/- 1/2 bit time"

At page 224, line 52, append: "Tolerance: +/- 1/4 bit time"

At page 228, line 55, append: "Tolerance: +/- 1/2 bit time"

At page 186, line 16, append: "Tolerance: +/- 1/2 bit time"

At page 186, line 20, append: "Tolerance: +/- 100 us"

At page 189, line 35, append: "Tolerance: +/- 100 us"

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Clause, Subclause, page, line C/ 148 SC 148.4.5.4 Page 20 of 23 5/23/2019 6:37:58 PM

Timers

C/ 148	SC 148.4.5.4	P 224	L 45	# i-320	
Baggett,	Tim	Microchip Teo	chnology, Inc.		

Comment Type E Comment Status D

*** Comment submitted with the file 100633500003-baggett_3cg_plca_timing_01_0519.pdf attached ***

More specific guidance may be provided to the system integrator in selecting a proper value for the PLCA to_timer when implementing a mixing segment that exceeds the "up to at least 25m" length or medium with different velocity of propagation. The following text change describes in additional detail the effects the medium propagation and PHY delays have in determining the transmit opportunity time.

See baggett_3cg_plca_timing_01_0519.pdf

SuggestedRemedy

Change the description of to_timer in lines 45-52 to read as follows:

The transmit opportunity timer maps to aPLCATransmitOpportunityTimer. The timer value should meet Equation (148-2). to_timer shall be set equal across the mixing segment for PLCA to work properly.

Duration: integer number between 1 and 255, expressed in bit times.

to_timer > max(2 * t_propdelay) + max(TX_EN sampled to MDI output) + max(MDI input to CRS asserted) + max(MDI input to CRS deasserted) - min(MDI input to CRS deasserted) (148-2)

where:

t_propdelay is the propagation delay between any two nodes on the mixing segment, and the delay specifications are the maxima and minima for the PHY type on the mixing segment (for 10BASE-T1S, see 147.11).

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Change the description of to_timer in lines 45-52 to read as follows:

The transmit opportunity timer maps to aPLCATransmitOpportunityTimer. The timer value needs to meet Equation (148-2). to_timer shall be set equal across the mixing segment for PLCA to work properly.

Duration: integer number between 1 and 255, expressed in bit times.

to_timer > max(2 * t_propdelay) + max(TX_EN sampled to MDI output) + max(MDI input to CRS asserted) + max(MDI input to CRS deasserted) - min(MDI input to CRS deasserted) (148-2)

where:

t_propdelay is the propagation delay between any two nodes on the mixing segment, and the delay specifications are the maxima and minima for the PHY type on the mixing segment (for 10BASE-T1S, see 147.11).

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Clause, Subclause, page, line C/ 148 SC 148.4.5.4 Page 21 of 23 5/23/2019 6:37:58 PM

With respect to the suggested remedy the "should" statement at the beginning of the sentence has been replaced with a "needs to be" to be coherent with proposed resolution of comment i-272.

C/ 148	SC 148.4.6	P 2 ′	14	L 22	# i-418
Kim, Yongb	um	NIO			
Comment T	ype TR	Comment Status	D		PLCA_SCOPE

[CSD/Compatibility] [Installed base compatibility] [PAR -- scope did not include MAC function in the project scope]

In PLCA data state diagram, COLLIDE state and related functional behaviors create a condition where in half-duplex, CSMA/CD, MAC transmits a packet, into a substantially busy network, but the collision condition does not result in a collision on the shared media. The collision signal is asserted only for the local node for the TX to collide-&-retry, while the simultaneous received signal that caused the collision is expected to be received as if there is no collision. The remote transmiter is not notified of contention on the network. This is a new behavior for an half-duplex MAC.

Legacy and installed base of Ethernet MACs expect to operate in 'architecturally' separate TX and RX. i.e. full-duplex datapath, while in half-duplex mode. Explicit allowance for implementations to optimize the datapath resources to only support simplex datapath operation is found in 4.1.2 where only obvious externally testable condition was inserted into the CL4 spec:

"4.1.2 CSMA/CD operation. Transmit frame operations are independent from the receive frame operations. A transmitted frame addressed to the originating station will be received and passed to the MAC client at that station. This characteristic of the MAC sublayer may be implemented by functionality within the MAC sublayer or full duplex characteristics of portions of the lower layers."

And the clear architectural model vs implementations here in 1.1.3.1: "...The architectural model is based on a set of interfaces that may be different from those emphasized in implementations. One critical aspect of the design, however, shall be addressed largely in terms of the implementation interfaces: compatibility."

This new behavior specified in CL148 PLCA data state diagram is not compatible with many installed bases of 802.3 nodes with appropriate explosed MII interoperability test point that is also a phyical interface with specified connectors. Also as forementioned, the contention management and collision handling are MAC functions, not a part of Physical Layer that Reconsilation Sub-layer belongs to.

Additional info could be found here : (slides 14~18 of): http://www.ieee802.org/3/cg/public/Nov2018/Kim 3cg 01a 1118.pdf

SuggestedRemedy

This clause CL148 PLCA RS should be deleted. Alternatively re-architected to avoid introducing new normative behaviors to the installed base with exposed interoperability interfaces.

Proposed Response Response Status W

PROPOSED REJECT.

Commenter fails to show compatibility issues with conformant implementations and incorrectly posits PLCA is a new MAC.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Clause, Subclause, page, line

C/ 148 SC 148.4.6.1 Page 22 of 23 5/23/2019 6:37:58 PM

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SOO htt	$n \cdot / / n n n n n n n n n n n n n n n n $	a/2/ca/public/ lop/2010/	UITORIAL COLUMNIA TIDAL P	a
See IIII		0/3/00/00000/3/40/01/9/		
				~ ~

See h	ittp://www.ieee802	.org/3/cg/public/Jan2	2019/	I utorial_cg_0119_f	inal.pdf
C/ 148	SC 148.4.6.1	P 2 :	25	L 9	# i-274
Thompso	n, Geoffrey	Indep	ende	nt Consultant	
Comment Clarify	Type E Y	Comment Status	D		Editorial
Suggeste	dRemedy				
Chang	ge to:transmit op	portunity on the me	dia is	detected.	
Proposed PROF	Response POSED REJECT.	Response Status	w		
Defer					
The C	RG disagrees witl	n the commenter. Th	ie cu	rrent text does not n	eed clarification.
The R signal	S does not detect Is from the PMA/P	activity on the medi CS to MAC/PLS prir	a, bu nitive	t maps detected act s.	ivity conveyed in MII
C/ 148	SC 148.4.6.1	P 2 :	26	L 26	# i-426
Brandt, D	avid	Rock	well A	utomation	
Comment	Туре Т	Comment Status	D		State Diagram
The e would	xit condition on the return to the NOF	e left side of the IDL RMAL state.	E sta	te is incorrect. If !plo	ca_en occurred, we
Suggeste	dRemedy				

From: receiving * !plca en * tx cmd = NONE

To:

receiving * !plca txen * tx cmd = NONE

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE. Accommodated by resolution of comment i-193

Proposed Resolution of comment i-193 is: PROPOSED ACCEPT IN PRINCIPLE. Replace "receiving * !plca en * tx cmd = NONE" with "receiving * (!plca txen)) * (tx cmd = NONE)"

C/ 148	SC 148.4.7.4	P 230	L 13	# i-277
Thompso	n, Geoffrey	Independen	t Consultant	
Comment	Type TR	Comment Status D		Timers
Also F timer, enterin from F timer noise	Figure 148-5. The not whether it is r ng another state of HYSTERESIS the degrades should t events are.	timer is very weakly define eset by a plca_reset nor w or anything else. Further, w re is no modification to the here be noise on the !plca	ed. It only specific hether it is reset by when the state is r timer setting so t _active input no n	es the duration of the by being "done and returned to ACTIVE he operation of the natter how far apart the
Suggestee	dRemedy			
Fully s	specify the operat	ion pf the timer.		
Proposed PROF delete	Response POSED ACCEPT "stop plca_status	Response Status W IN PRINCIPLE. s_timer" from "ACTIVE" sta	ate in Figure 148-	5.
The b descri "stop	ehaviour of the tin bed in 40.4.5.2. T timer_xxx" has no	ners is specified in 148.1.1 his means that "start timer effect on an already "done	.2. They operate i _xxx" implies a re " timer.	in the manner sset of the timer, while
The ti same is in a	mer status is only state. This means ny state other tha	checked in the HYSTERE s that its status has no effe n HYSTERESIS.	SIS state, and it is ct when the PLCA	s reset on entry of the A Status State Diagram
In oth	er words, this diag	gram represents an hold-or	n filter over the plo	ca_active variable.

C/ 148 SC 148.4.7.4