

Wienckowski 3ch D3p0 Comment 51 Response

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IEEE P802.3ch

Comment i-51 Submitted

- 149.1.3.1 P79 L41
- Comment – tx_group50x65B is used in several places but it loosely defined and never formally defined. There can be misinterpretation of the bit ordering.
- SuggestedRemedy – (Editorial Note. I cannot show subscripts in the spreadsheet so I will enclose anything that needs to be subscripted with **. For example A*n* is An with n subscripted. I'm not sure if the carriage return will show up in the file so a <cr> means carriage return.) <Begin proposed Change> In line 47 insert the following: <cr> tx_group50x65B<3249:0> is defined as: <cr> tx_group50x65B<65 * i + j> = tx_coded*i*<j> <cr> where i = 0 to 49 and j = 0 to 64 and tx_coded*i*<64:0> is the ith 64B/65B block where tx_coded*0*<64:0> is the first one transmitted.

Comment i-51 Response

- Response – PROPOSED ACCEPT IN PRINCIPLE.
- The text description of what to do is hard to understand and the usage of "*" to indicate both subscripts and multiplication is confusing.
- Implement the changes shown on slide 4 of [wienckowski_3ch_D3p0_comment51_response.pdf](#).

Comment i-51 Proposed Implementation

- Add text in yellow highlight as shown below:

superframe is $L \times 320 / S$ ns.) Finally these bits are exclusive OR'd with a degree 33 scrambler to create the MultiGBASE-T1 payload. PCS transmit functions are described in 149.3.2.2.

tx_group50x65B<3249:0> is defined as:

tx_group50x65B< $65 \times i + j$ > = tx_coded_i<j>

where $i = 0$ to 49, $j = 0$ to 64, and tx_coded_i<64:0> is the i^{th} 64B/65B block where tx_coded₀<64:0> is the first block transmitted.

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THANK YOU