

IEEE802.3ch Timing and Next Steps

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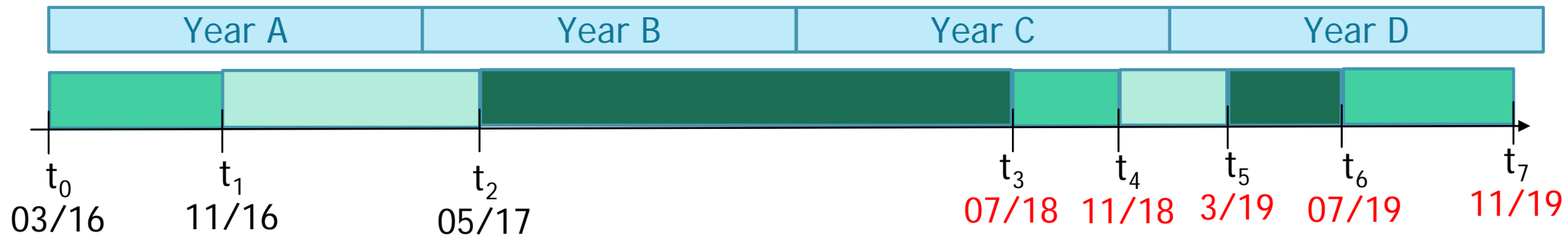
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IEEE 802.3ch

Typical Automotive Ethernet PHY IEEE Timing

Timeline from CFI with dates added.

- ▶ t_0 - Idea for CFI.
- ▶ t_1 - CFI approved. Start to work on PAR Components.
- ▶ t_2 - PAR approved. Start TF meetings and select technology components.
- ▶ t_3 - D1.0 complete. Refine specification.
- ▶ t_4 - D2.0 complete. WG ballot begins.
- ▶ t_5 - D3.0 complete. Sponsor ballot begins.
- ▶ t_6 - Sponsor ballot complete.
- ▶ t_7 - Completed specification available.



IEEE P802.3 P802.3ch NGAUTO Ethernet PHY DRAFT Timeline

- September 2017 - Review baseline proposals (Draft 0.1 skeleton)
- March 2018 – Draft 0.2 for TF review (link segment)
- May 2018 – baselines selected, D1.0 for TF review
 - Possible OOS joint interim with P802.3cg
- July 2018 – last technical feature
 - D1.1 (July), D1.2 (Sept), possible D1.3 (OOS October interim)
- November 2018 – D2.0, start WG ballot
 - 2 recircls, January 2019 (D2.1), March 2019 (D.2.2) (OOS interim to get to sponsor ballot in March 2019 instead)
- May/July 2019 – D3.0, begin Sponsor ballot (OOS interim June 2019 to complete sponsor ballot in July 2019 instead)
 - D3.1 (July/Sept), D3.2 (Sept/Nov) recirculations
- January/March 2020 SASB approval (September/December 2019 SASB approval)

Timing Questions

- Can we be ready for TF ballot in 6 months?
- Final silicon is currently projected to be needed mid 2021. What does this mean for the spec timing?

Goals to TF Ballot

- March 2018

- May 2018

- July 2018