

Minutes IEEE P802.3ch Multigig Automotive Ethernet PHY TF AdHoc meeting August 23, 2017

Prepared by George Zimmerman

Proposed Agenda:

1. Agenda/Admin: George Zimmerman, agenda_3chah_01_082317.pdf
2. SG Chair's comments: Steve Carlson, no presentation
3. Presentations:
 - a. MultiGig Auto-PHY Block Code Considerations – Brett McClellan, Marvell
4. Discussion & Next steps – All

Presentations were posted to the adhoc webpage the evening before

Agenda/Admin George Zimmerman acting as ad hoc chair:

Meeting began at 7:05am PT.

Introductions & Affiliations.

Presented file: [agenda 3chah 01 082317.pdf](#)

1. Reviewed the Attendance information related to the ad hoc.
2. Displayed the Participation slide and reviewed it.
3. Displayed patent slide deck, and reviewed it.
Call for Patents was made at 7:14AM Pacific Time, none responded
4. Reminded participants to indicate full names and employer/affiliation for the meeting minutes.

The reflector and website are now up, and we are now using the NGAUTO reflector. Instructions for subscribing to the reflector may be found at <http://www.ieee802.org/3/ch/reflector.html>. If you cannot subscribe to the reflector for some reason, and need additional assistance please contact the Task Force chair.

Files will be posted under the new Task Force ad hoc area.

Presentations/Discussion:

Chair's Comments & Discussion Steve Carlson, Chair, IEEE P802.3ch Task Force: Steve welcomed the group and discussion moved to the cabling data needed and formats requested for 802.3ch PHY studies.

Presentation: Brett McClellan, MultiGig Auto-PHY Block Code Considerations (McClellan_3ch_01_0817.pdf)

The presenter discussed issues of block coding used in the PCS for the MultiGig PHY, reviewing 1000BASE-T1 and other BASE-T PHYs. The presenter recommended that the 80/81B blocking used in 1000BASE-T1 would be undesirable because of its linkage to GMII mapping, whereas the multigig PHYs would likely use an XGMII interface, and therefore, basing on 64/65B or 64/66B would be preferable, perhaps with transcoding to a more efficient mapping if a large block FEC were used.

Additional Item:

George Zimmerman discussed that there appears to be a typo in the single-pair auto-negotiation state diagram (Figure 98-8) of IEEE 802.3bp-2016. On 4 transition arc's, logical "AND"s (*) had been replaced with logical "OR"s in the figure. This appears to have been introduced during a redrafting of the figure going from D2.0 to D2.1 in the 802.3bp working group ballot, and is without a comment. It was found by Mehmet Tazebay, and a description of the issue, its history, and the proposed fix were sent to the NGAUTO and 10SPE reflectors for review. There was no dissent on the call, and some agreement that the error was, in fact, a typo.

Closing Business

Next meeting scheduled for 9/6 – subject to contributions.

Meeting closed –7:58 am PT

Attendees (from Webex + emails)

First	Last	Affiliation
Dale	Amason	Nxp
Tobias	Belitz	Renesas
Rich	Boyer	Delphi
David	Brandt	Rockwell Automation
Clark	Carty	Cisco Systems
Eric	DiBiaso	TE
Dominik	Dorner	Leoni
Ramin	Farjad	aquantia
Frank	Flens	Finisar
Matthias	Fritsche	Harting
Olaf	Grau	Bosch
Yasuhiro	Hyakutake	Adamant
Matthias	Jaenecke	yazaki-europe
Dave	Jeskey	Sentinel Connector
Haysam	Kadry	Ford
Phil	Kelly	Delphi
Dongok	Kim	Hyundai
David	Law	HPE

Tzahi	Madgar	Valens
David	Malicoat	Senko
Kirsten	Matheus	BMW
Brett	McClellan	Marvell
Wes	Mir	Delphi
Henry	Muyshondt	Microchip
Douglas	Oliver	Ford
Sujan	Pandey	Nxp
Phong	Pham	Usconec
Vimalli	Raman	yazaki-europe
Hossein	Sedarat	aquantia
Masood	Shariff	Commscope
Yves	Stricot	Delphi
Alexander	Umnov	Corning
Lisa	Ward	Rohde-Schwarz
Natalie	Wienckowski	GM
Peter	Wu	Marvell
Kent	Younglove	Yazaki-US
John	Yurtin	Delphi
George	Zimmerman	CME Consulting
Helge	Zinner	Continental
Damien	Quenson	Acome
TOTAL	40	Attendees