

Table 162-C - Improved MDI Connector Mapping

Connector signal name	SFP112	SFP-DD112	DSFP	QSFP112	QSFP-DD800	OSFP	High Speed Signals Description
DL0p	13	13	13	17	17	29	Receiver Non-Inverted Data Output
DL0n	12	12	12	18	18	28	Receiver Inverted Data Output
SL0p	18	18	18	36	36	59	Transmitter Non-Inverted Data Input
SL0n	19	19	19	37	37	58	Transmitter Inverted Data Input
DL1p		33	8	22	22	32	Receiver Non-Inverted Data Output
DL1n		32	9	21	21	33	Receiver Inverted Data Output
SL1p		38	2	3	3	2	Transmitter Non-Inverted Data Input
SL1n		39	1	2	2	3	Transmitter Inverted Data Input
DL2p				14	14	26	Receiver Non-Inverted Data Output
DL2n				15	15	25	Receiver Inverted Data Output
SL2p				33	33	56	Transmitter Non-Inverted Data Input
SL2n				34	34	55	Transmitter Inverted Data Input
DL3p				25	25	35	Receiver Non-Inverted Data Output
DL3n				24	24	36	Receiver Inverted Data Output
SL3p				6	6	5	Transmitter Non-Inverted Data Input
SL3n				5	5	6	Transmitter Inverted Data Input
DL4p					55	23	Receiver Non-Inverted Data Output
DL4n					56	22	Receiver Inverted Data Output
SL4p					74	53	Transmitter Non-Inverted Data Input
SL4n					75	52	Transmitter Inverted Data Input
DL5p					60	38	Receiver Non-Inverted Data Output
DL5n					59	39	Receiver Inverted Data Output
SL5p					41	8	Transmitter Non-Inverted Data Input
SL5n					40	9	Transmitter Inverted Data Input
DL6p					52	20	Receiver Non-Inverted Data Output
DL6n					53	19	Receiver Inverted Data Output
SL6p					71	50	Transmitter Non-Inverted Data Input
SL6n					72	49	Transmitter Inverted Data Input
DL7p					63	41	Receiver Non-Inverted Data Output
DL7n					62	42	Receiver Inverted Data Output
SL7p					44	11	Transmitter Non-Inverted Data Input
SL7n					43	12	Transmitter Inverted Data Input