## IEEE P802.3ck Ad Hoc meeting -

January 2, 2019
Prepared by Kent Lusted

## Proposed Agenda:

- Approval of the Agenda
- Approve 19 December ad hoc minutes
- IEEE Patent Policy reminder:
- http://www.ieee802.org/3/patent.html
- IEEE Participation Requirements reminder
- Logistics for January Interim meeting
- .3ck Ad Hoc -
- "Exploration on the worst case DFE weight of DFE error propagation effect" - Louis Lu
- "Further study on RS(544,514) - symbol interleaving and bit muxing" - Xiang He

Presentations posted at: http://www.ieee802.org/3/ck/public/adhoc/index.html
Meeting began at $\sim 7: 05$ a.m. Pacific by Beth Kochuparambil.
Meeting began with the agenda presentation:
http://www.ieee802.org/3/ck/public/adhoc/jan02_19/agenda_010219_3ck_adhoc.pdf
The ad hoc chair reminded participants to indicate full names and employer/affiliation correctly for the meeting minutes. Reminded participants to mute lines when not speaking and reviewed the steps to unmute.

Showed the links to the IEEE P802.3ck Task Force ad hoc page and the email reflector.
Presented the proposed agenda and asked if there was objection as written. There was no objection. The agenda was approved by the ad hoc.

Asked if there were comments regarding the posted minutes for the December 19 ad hoc meeting. No one responded. The minutes were approved by the ad hoc.

Reminded participants of the IEEE patent policy. She asked if anyone was unfamiliar with the IEEE patent policy. No one responded.

Reminded participants of the IEEE Participation Requirements and showed the slide with the Participation requirements. She asked if anyone was unfamiliar with the IEEE Participation Requirements. No one responded.

## Agenda Items

## P802.3ck Update, Beth Kochuparambil

See: http://www.ieee802.org/3/ck/public/adhoc/jan02_19/agenda_010219_3ck_adhoc.pdf

- TF meets the week of January 14, 2019 in Long Beach, CA, USA. TF meets Tuesday afternoon thru end of Thursday.
- Notable dates: January 4th, 2019 (Fri) AOE - Presentation Requests due. January 9th (Wed) 5 pm PST (error in deck) - Presentations due


## Presentation \#1:

"Exploration on the worst case DFE weight of DFE error propagation effect" - Louis Lu
See: http://www.ieee802.org/3/ck/public/adhoc/jan02_19/lu_3ck_adhoc_01_010219.pdf

- Reviewed the differences between the data on slide 12 vs. 13. The difference is due to b1(max) values.
- There was a recommendation to develop a set of DFE tap weight rules and apply them to the submitted channels.
- Discussed the need to clarify the proposal with the specific changes requested.
- An update '01a' was requested to include a key of the channels used on slides $12 \& 13$


## Presentation \#2:

"Further study on RS(544,514) - symbol interleaving and bit muxing " - Xiang He
See: http://www.ieee802.org/3/ck/public/adhoc/jan02 19/he 3ck adhoc 01a 010219.pdf

- Updated version '01a' with editorial changes. No one objected.
- On slide 5, the ICN amount is manipulated directly in simulation. All 8 aggressors were used for the ICN manipulation.
- On slide 5 and 9, opinions stated that analysis needs to be extended to look at the post-FEC BER.
- It was noted that the analysis used smaller tap weights than what has been shown by other participants. The analysis intends to show that burst errors can come from ICN.

There are no ad hoc meetings scheduled before the January interim meeting. Chair noted that presentation requests are due Friday, 4 January AOE.

The ad hoc meeting ended at $\sim 8: 30 \mathrm{a}$.m. Pacific.

List of attendees (captured from Webex tool)

| Name | Affiliation | Employed by |
| :---: | :---: | :---: |
| Adam Healey | Broadcom | Broadcom |
| Adee Ran | Intel | Intel |
| Adrian Butter | Global Foundries | Global Foundries |
| Ali Ghiasi | GhiasiQuantum LLC | GhiasiQuantum LLC |
| Arthur Marris | Cadence | Cadence |
| Arturo Pachon | TE Connectivity | TE Connectivity |
| Beth Kochuparambil | Cisco | Cisco |
| Bill Kirkland | Semtech | Semtech |
| Biman Chattopadhyay | Synopsys | Synopsys |
| Brandon Gore | Samtec | Samtec |
| Brian Holden | Kandou | Kandou |
| David Law | HPE | HPE |
| David Malicoat | Senko | Malicoat Networking Solutions |
| David Rennie | Synopsys | Synopsys |
| Dean Wallace | Marvell | Marvell |
| Frank Lambrecht | Gigamon | Gigamon |
| Gary Nicholl | Cisco | Cisco |
| Geoff Zhang | Xilinx | Xilinx |
| Howard Heck | Intel | Intel |


| Hsinho Wu | Intel | Intel |
| :---: | :---: | :---: |
| Jane Lim | Cisco | Cisco |
| Jayen Desai | Intel | Intel |
| Jeffery Maki | Juniper | Juniper |
| Jeremy Stephens | Intel | Intel |
| John D'Ambrosia | FutureWei (Subsidiary of Huawei) | FutureWei (Subsidiary of Huawei) |
| John Ewen | Globalfoundries | Globalfoundries |
| Kent Lusted | Intel | Intel |
| Mark Gustlin | Xilinx | Xilinx |
| Mark Kimber | Samtech | Samtech |
| Masashi Shimanouchi | Intel | Intel |
| Matt Brown | Macom | Macom |
| Mau-Lin Wu | Mediatek | Mediatek |
| Mike Dudek | Cavium | Cavium |
| Mike Li | Intel | Intel |
| Nathan Tracy | TE Connectivity | TE Connectivity |
| Pete Anslow | Ciena | Ciena |
| Phil Sun | Credo | Credo |
| Phong Pham | US Conec | US Conec |
| Piers Dawe | Mellanox | Mellanox |
| Pirooz Tooyserkani | Cisco | Cisco |
| Rich Mellitz | Samtec | Samtec |


| Rick Rabinovich | Keysight | IXIA |
| :---: | :---: | :---: |
| Rita Horner | Synopsys | Synopsys |
| Rob Stone | Broadcom | Broadcom |
| Sam Kocsis | Amphenol | Amphenol |
| Scott Irwin | Mosys | Mosys |
| Shawn Nicholl | Xilinx | Xilinx |
| Shimon Muller | Axalume | Axalume |
| Steve Baumgartner | Global Foundries | Global Foundries |
| Steve Sekel | Keysight | Keysight |
| Steve Trowbridge | Nokia | Nokia |
| Tom Mitcheltree | US Conec | US Conec |
| Tom Palkert | Molex/Macom | Molex/Macom |
| Upen Kareti | Cisco | Cisco |
| Vittal Balasubramanian | Innovium | Innovium |
| Xiang He | Huawei | Huawei |
| Xinyuan Wang | Huawei | Huawei |
| Yan Zhuang | Huawei | Huawei |
| Yang Zhiwei | ZTE | ZTE |
| Yasuo Hidaka | Credo | Credo |
| Yuchun(Louis) Lu | Huawei | Huawei |
| Zhilei Huang | Huawei | Huawei |
| Zvi Rechtman | Mellanox | Mellanox |

