# **COM** Discussions

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IEEE 802.3 100 Gb/s, 200 Gb/s, and 400 Gb/s Electrical Interfaces Task Force

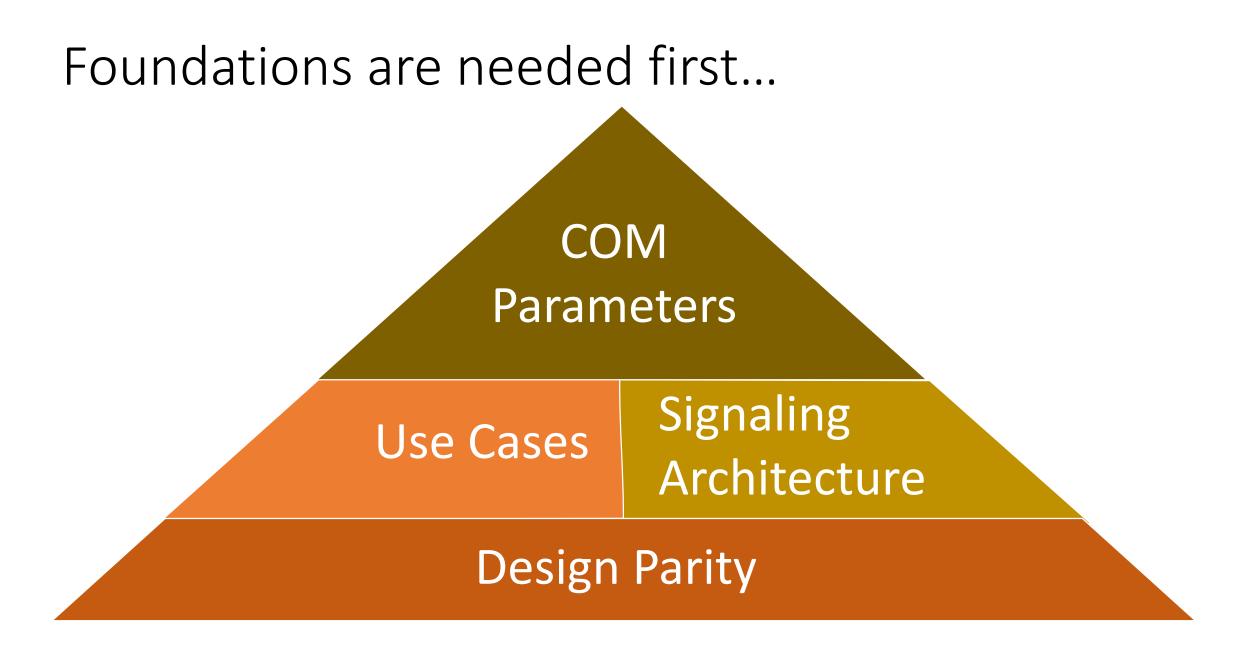
#### Motivation

- □ Reflector response was 50+ ... !
- □ Feedback/topics from many
- □ Bringing to the large group
  - Study may be more effective in small groups
  - Focus groups
    - Half-dozen-or-so people
    - Connect "offline" to discuss and study
    - Bring tradeoffs or study results to the larger group (F2F) or Ad Hoc

# Lexicon of feed back

- COM Ref Rx and actual design parity
- □ COM signaling architecture: include a long FFE.
- □ COM signaling architecture: Quantize DFE
- □ COM signaling architecture: balance between Tx FFE vs Rx FFE
- Power vs loss
- Power implications vs COM signaling architecture choice
- □ Use C2M/CR host to drive COM parameters
- Asymmetric channel and package loss
- Go back to a reasonable worst case or not
- COM parameters and speed

#### □ Let's organize this a bit



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# Attempt at organizing COM issues

Design Parity				
Reference Rx relation to Actual Rx designs	Use Cases Channel priority for PHY specification: CR/KR/C2M/C2C End User needs: channel/ package material and construction	Ref Signal Arch		
		TX/RX Balance	COM Parameters	
		Power consideration of various architectures	Package, Tx, Rx, Noise, etc	
	End User needs: power tradeoff and symmetry Should COM be used for C2M?	Relationship between architecture and channel impairments How much reference FFE, DFE, and CTF is needed Computational efficiency	Pass/Fail criteria – "Reasonable worst case"	

# Overlaying the Standards Process

Design Parity				
Reference Rx relation to Actual Rx designs	Use Cases Channel priority for PHY specification: Ref Signal Arch			BASELINES
	CR/KR/C2M/C2C End User needs: channel/ package material and construction	TX/RX Balance	COM Parameters	BA
		Power consideration of various architectures	Package, Tx, Rx, Noise, etc Pass/Fail criteria – "Reasonable worst case"	
	End User needs: power tradeoff and symmetry	Relationship between architecture and channel impairments How much reference FFE,		
	Should COM be used for C2M?			
		DFE, and CTF is needed		
TODAY??	(MAY), JULY	Computational efficiency	SEPTEMBER	NOVEMBER
	& AD HOCS (7/25, 8/8, 8/22)		& AD HOCS	

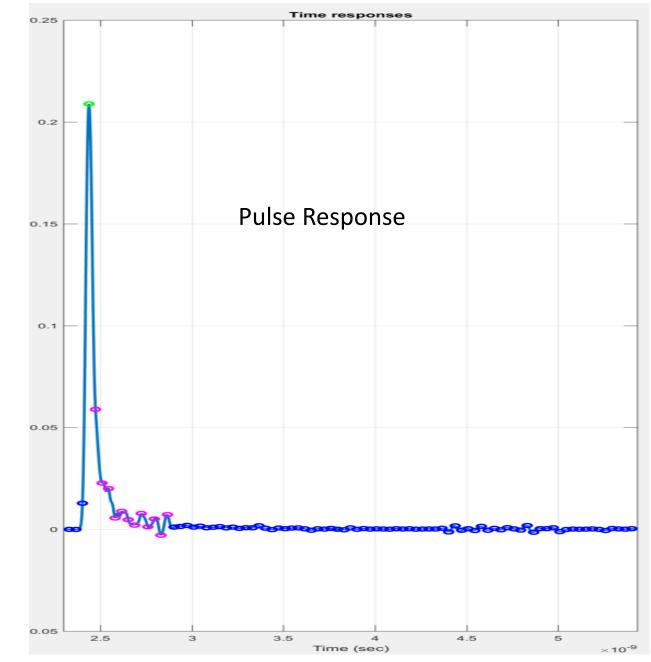
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6

#### Channel ....

#### Operating Margin: Interconnect Designer Expectations

What needs to be fixed and what can be ignored?



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# **Design Parity Discussion**

□ What is the purpose for the reference receiver?

- Is it: Minimum performance?
- Is it: A template for minimum performance design?
- Is it: A reference for receiver compliance testing?
- Is it: A way to qualify a channel?
- Consider that actual receivers are complex and there may be as may designs as receiver designers
  - How does that effect the answers above?
- Consider COM for a channel needs to be the same regardless of algorithm implementation