Comment : Figure 145-15 pages 144 and 145.

This comment is about Primary Alternative dual-signature semi-independent
PSE state diagram and the proposal discussed over mails for the need to allow LCE after class probe as we did for SSPD.

The following proposal is almost similar to Chad's proposal with the following difference:

It allows to keep the current state machine but if user which to allow the above objective, he needs to set a variable to be true to set this option.

Proposed Remedy for page 144

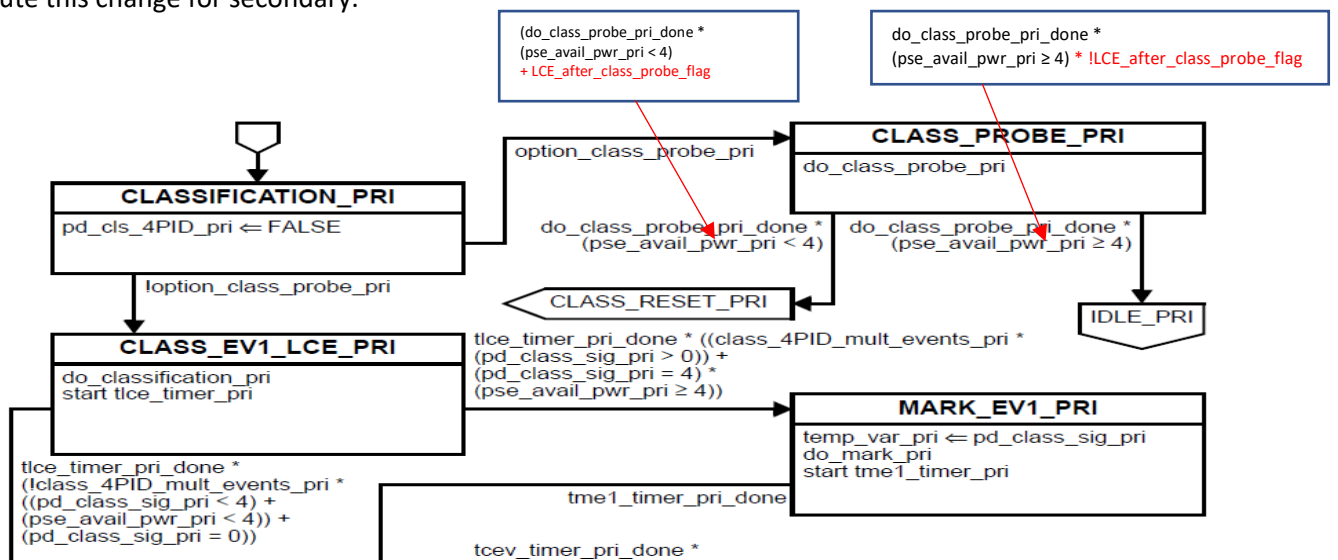1. Add variable and make the following changes in the state machine

LCE_after_class_probe_flag
An optional variable to allow CLASS_EV1_LCE_PRI after CLASS_RESET_PRI.
TRUE=
FALSE=

2. Repute this change for secondary.

(do_class_probe_pri_done *
(pse_avail_pwr_pri < 4)
+ LCE_after_class_probe_flag

do_class_probe_pri_done *
(pse_avail_pwr_pri ≥ 4) * !LCE_after_class_probe_flag

CLASSIFICATION_PRI
pd_cls_4PID_pri ⇐ FALSE

option_class_probe_pri

CLASS_PROBE_PRI
do_class_probe_pri

do_class_probe_pri_done *
(pse_avail_pwr_pri < 4)

do_class_probe_pri_done *
(pse_avail_pwr_pri ≥ 4)

!option_class_probe_pri

CLASS_RESET_PRI

IDLE_PRI

CLASS_EV1_LCE_PRI
do_classification_pri
start tlce_timer_pri

tlce_timer_pri_done * ((class_4PID_mult_events_pri *
(pd_class_sig_pri > 0)) +
(pd_class_sig_pri = 4) *
(pse_avail_pwr_pri ≥ 4))

MARK_EV1_PRI
temp_var_pri ⇐ pd_class_sig_pri
do_mark_pri
start tme1_timer_pri

tlce_timer_pri_done *
(!class_4PID_mult_events_pri *
((pd_class_sig_pri < 4) +
(pse_avail_pwr_pri < 4)) +
(pd_class_sig_pri = 0))

tme1_timer_pri_done

tcev_timer_pri_done *

Proposed Remedy for page 145

tclass_reset_timer_pri_done *(! LCE_after_class_probe_flag +
LCE_after_class_probe_flag * (pse_avail_pwr_pri<4))

CLASS_EV4_PRI
do_classification_pri
start tcev_timer_pri

(pse_avail_pwr_pri ≥ 4) +
(temp_var_pri ≠ 4)

4PID3_PRI
pd_cls_4PID_pri ⇐ TRUE

tcev_timer_pri_done *
(pd_class_sig_pri ≠ 3)

(pse_avail_pwr_pri < 4) *
(temp_var_pri = 4)

tcev_timer_pri_done *
(pd_class_sig_pri = 3)

CLASS_RESET_PRI
do_class_reset_pri
start tclass_reset_timer_pri

tclass_reset_timer_pri_done *
LCE_after_class_probe_flag *
(pse_avail_pwr_pri>=4)

4PID4_PRI
pd_cls_4PID_pri ⇐ TRUE

tclass_reset_timer_pri_done

UCT

CLASS_EV1_LCE_4PID_PRI
do_classification_pri
start tlce_timer_pri

MARK_EV_LAST_PRI
do_mark_pri
start tme2_timer_pri

tlce_timer_pri_done

CLASS_EV1_LCE_PRI

tme2_timer_pri_done

CLASS_EVAL_PRI