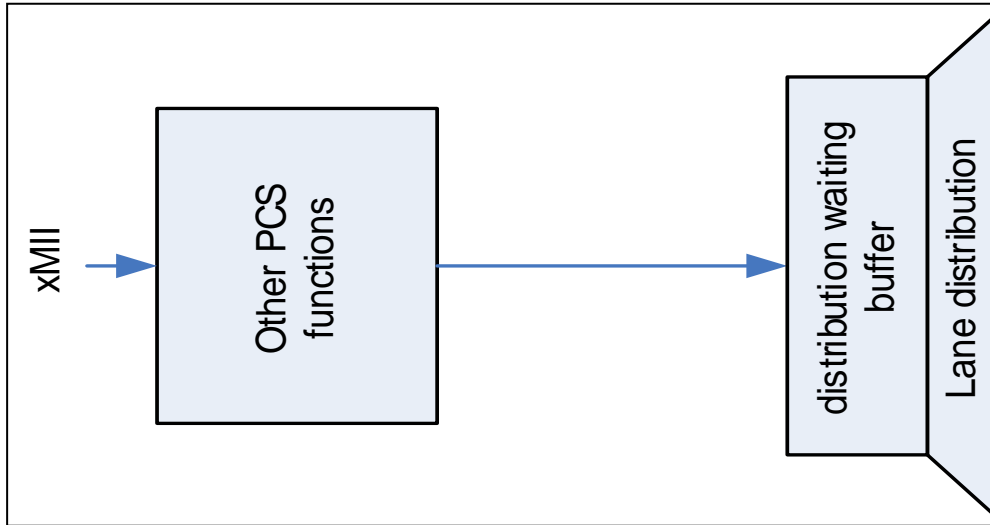


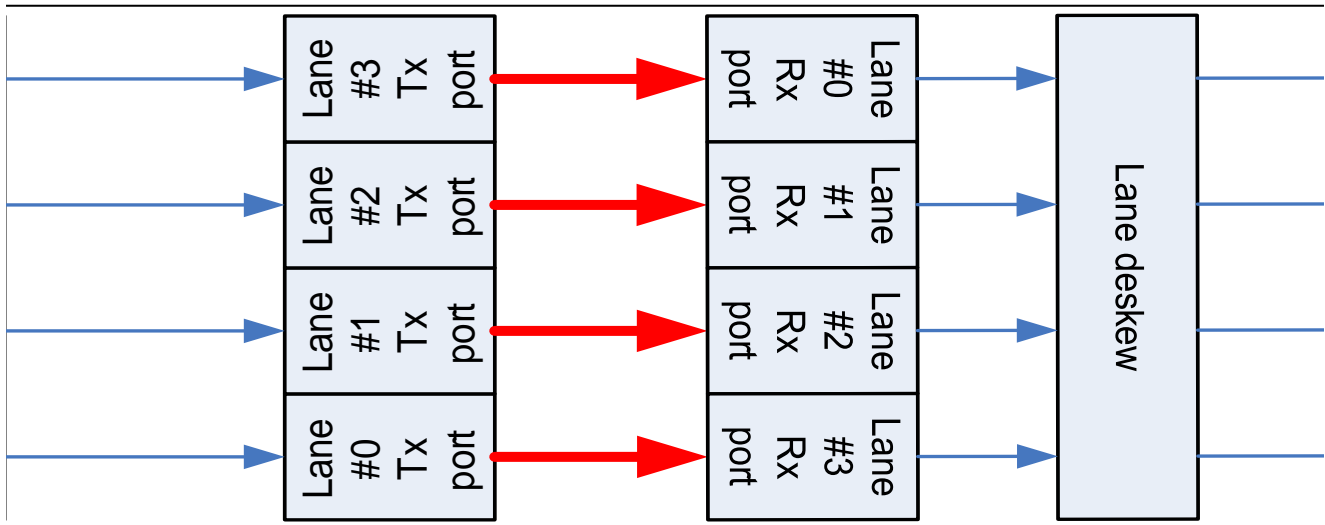
Block Time	640	ps	time of one block's worth of data (i.e. 64
Tx Option	A		Blocks on NxPCS lane Tx MDI are not alig
Rx Option	1		Each Rx lane's MDI to xMII delay is differ
Link delay	0	ps	assumes the same delay for all lanes
Number of lanes	20		number of PCS lanes (max = 20)
Resulting time error	0	ps	



Lane	0	actual arvl time	0	actual distrib dly	0
	1		640		0
	2		1280		0
	3		1920		0
	4		2560		0
	5		3200		0
	6		3840		0
	7		4480		0
	8		5120		0
	9		5760		0
	10		6400		0
	11		7040		0
	12		7680		0
	13		8320		0
	14		8960		0
	15		9600		0
	16		10240		0
	17		10880		0
	18		11520		0
	19		12160		0
Lane	0			modelled PHY dly	0
	1				0
	2				0
	3				0

bits of uncoded data)

ned, xMII to MDI delay is constant for every Tx PCS lane, blocks on every Tx PCS lane have different timestamp and is accounted for as such



actual dept time	0	actual arrival time	0	actual deskew dly	12160
	640		640		11520
	1280		1280		10880
	1920		1920		10240
	2560		2560		9600
	3200		3200		8960
	3840		3840		8320
	4480		4480		7680
	5120		5120		7040
	5760		5760		6400
	6400		6400		5760
	7040		7040		5120
	7680		7680		4480
	8320		8320		3840
	8960		8960		3200
	9600		9600		2560
	10240		10240		1920
	10880		10880		1280
	11520		11520		640
	12160		12160		0
dept timestamp	0				
	640				
	1280				
	1920				

2560

3200

3840

4480

5120

5760

6400

7040

7680

8320

8960

9600

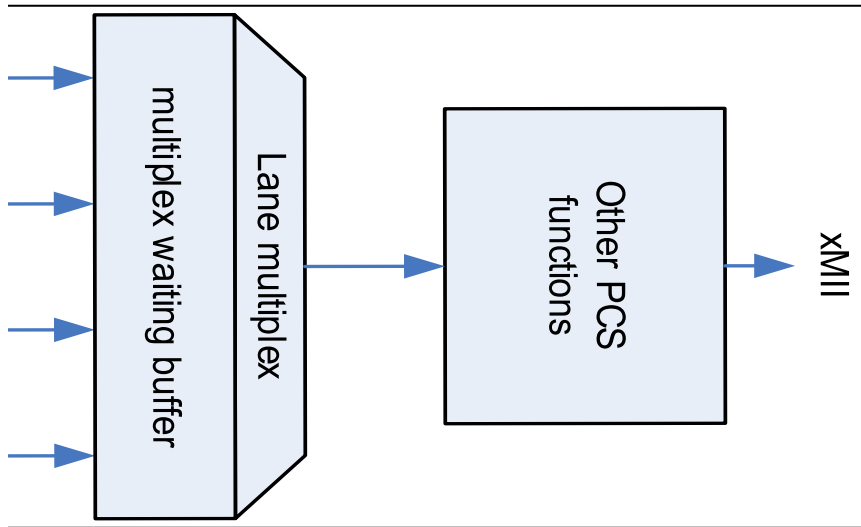
10240

10880

11520

12160

ns, differing by the block arrival delay on xMII



actual merge dly

0

640
1280
1920
2560
3200
3840
4480
5120
5760
6400
7040
7680
8320
8960
9600
10240
10880
11520
12160

actual arrival time

12160

12800
13440
14080
14720
15360
16000
16640
17280
17920
18560
19200
19840
20480
21120
21760
22400
23040
23680
24320

modelled PHY dly

12160
12160
12160
12160

arrival timestamp

0
640
1280
1920

measured link dly

0
0
0
0

12160	2560	0
12160	3200	0
12160	3840	0
12160	4480	0
12160	5120	0
12160	5760	0
12160	6400	0
12160	7040	0
12160	7680	0
12160	8320	0
12160	8960	0
12160	9600	0
12160	10240	0
12160	10880	0
12160	11520	0
12160	12160	0