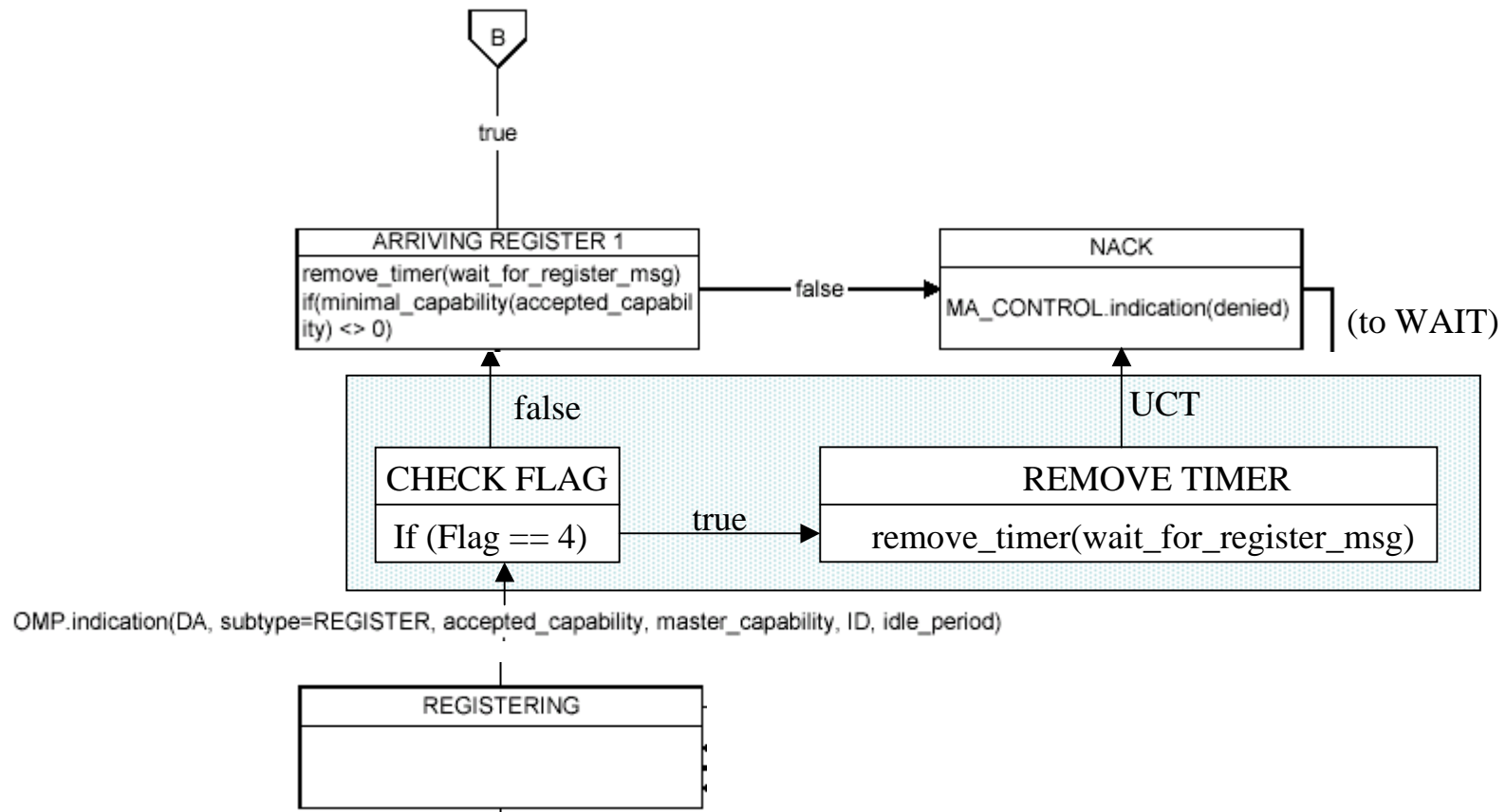


Current definition

- Discovery process (P134~136 Figure 56-16~56-18)
 - The following processes are not clear in D1.1.
 - ✓ RX of REGISTER indicating Nack
 - ✓ TX of REGISTER_ACK indicating Failure
 - ✓ RX of REGISTER_ACK indicating Failure

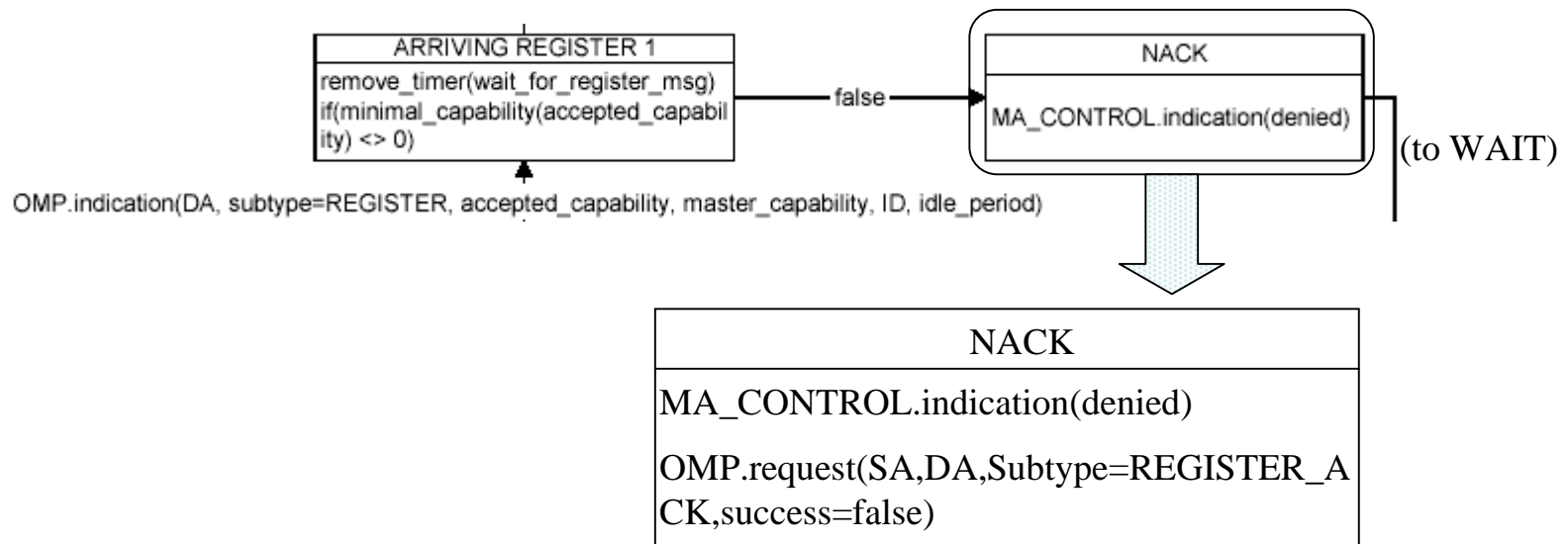
Proposal (1)

- RX of REGISTER indicating Nack
 - After TX of REGISTER indicating Nack, the OLT transits to INSIDE REGISTER WINDOW as shown in Figure 56-16. After the timeout of register_window_size, the OLT transits to IDLE via INDICATION.
 - Addition to D1.1 (P135 Figure 56-17)



Proposal (2)

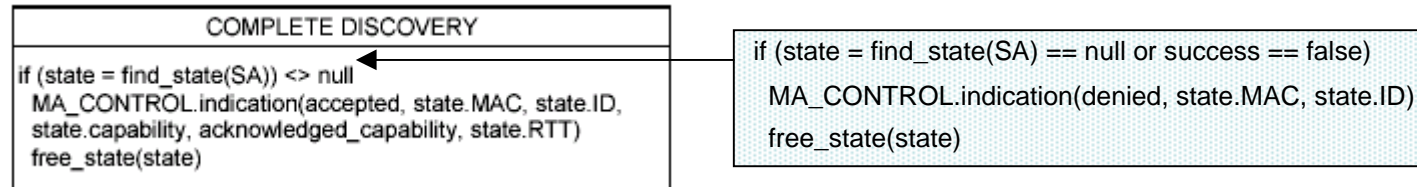
- TX of REGISTER_ACK indicating Failure
 - Even if $(\text{minimal_capability}(\text{accepted_capability}) \neq 0)$ in ARRIVING REGISTER 1 is false, the ONU does not send REGISTER_ACK indicating failure.
 - ✓ On the OLT side, no failure indication of registration process is issued.
 - Modification of D1.1 (P135 Figure 56-17)



Proposal (3)

■ RX of REGISTER_ACK indicating Failure

- No failure indication of registration process is defined.
- Addition to D1.1 (P134 Figure 56-16)



- Addition to D1.1 (P133 Section 56.3.4.1.5)
 - ✓ The definition of `MA_CONTROL.indication(denied, SA, ID_list)` should be added.

`MA_CONTROL.indication(deniedaccepted, SA, ID_list, capability, acknowledged_capability, RTT)`

The service indication issued by the Discovery Process to notify the client and Layer Management that the registration process has ~~completed~~. ^{been denied}

The MAC address of the recipricating MAC (ONU address at the OLT, and OLT address at the ONU) is passed in the parameter *SA*. The list of LLIDs allocated to the ONU are passed in the parameter *ID_list*.