

# Multi-PHY Operation

B. Rezvani: Ikanos Communications

## » Operation

- Loop bonding and data rate aggregation
- Differentiated level of services on multi-pair
  - Different BER can be allocated to each copper pair
- Protection Switching
  - Allow switch over operation when one link fails
- Transmission coordination
  - Allows cross talk cancellation

## » VDSL modem sublayers:

- Transport Protocol Specific Transmission Convergence (TPS-TC) sublayer
- Physical Media Specific Transmission Convergence (PMS-TC) sublayer
- Physical Media Dependent (PMD) sublayer

## » TPS-TC sublayer being defined for Ethernet Transport

- HDLC encapsulation with byte stuffing for rate matching

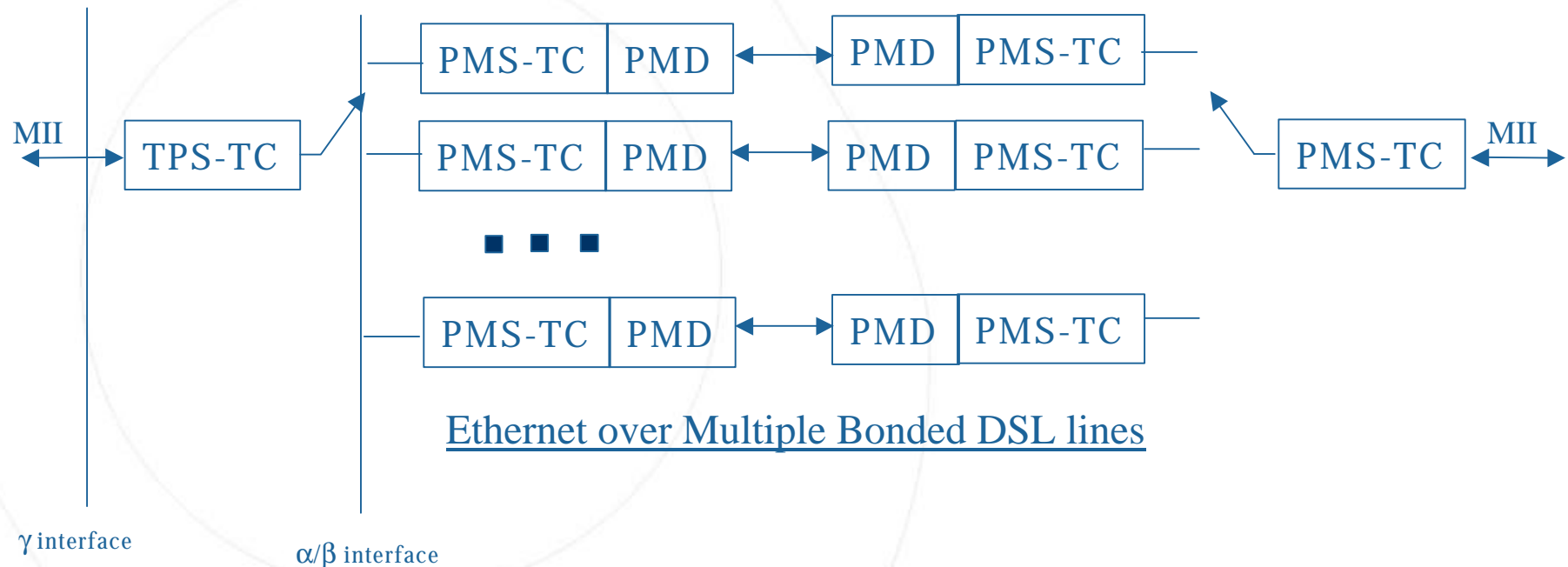


Ethernet over Single VDSL/ADSL line

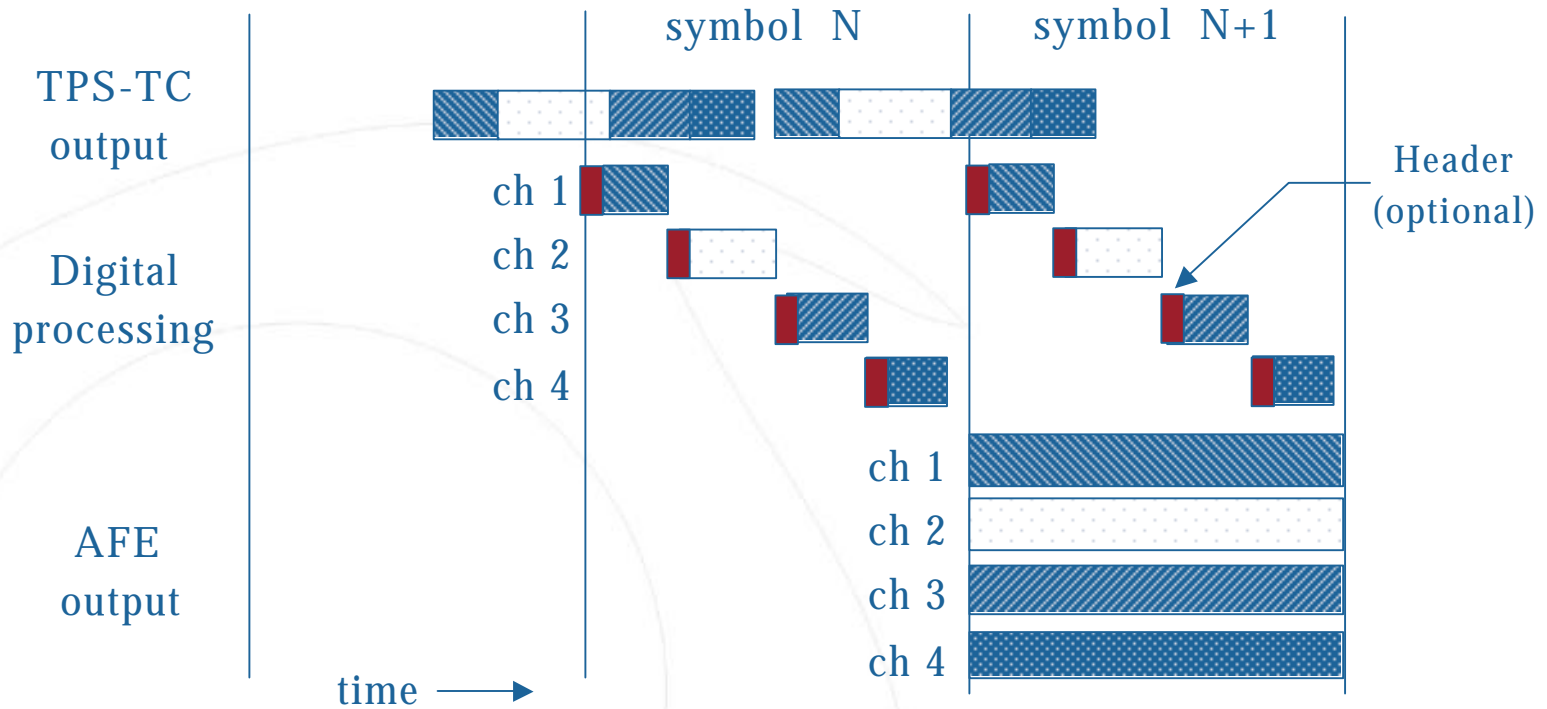
# Multi-PHY, Loop aggregation

## » TPS-TC sublayer output muxed onto multiple lines:

- TPS-TC output is rate matched stream of bytes
- Send TPS-TC output on each line, once every symbol, in sequence
- Each line takes the number of bytes which will be sent per symbol on that line
- Receive side puts them back together in same sequence



# Data Flow in Bonded System

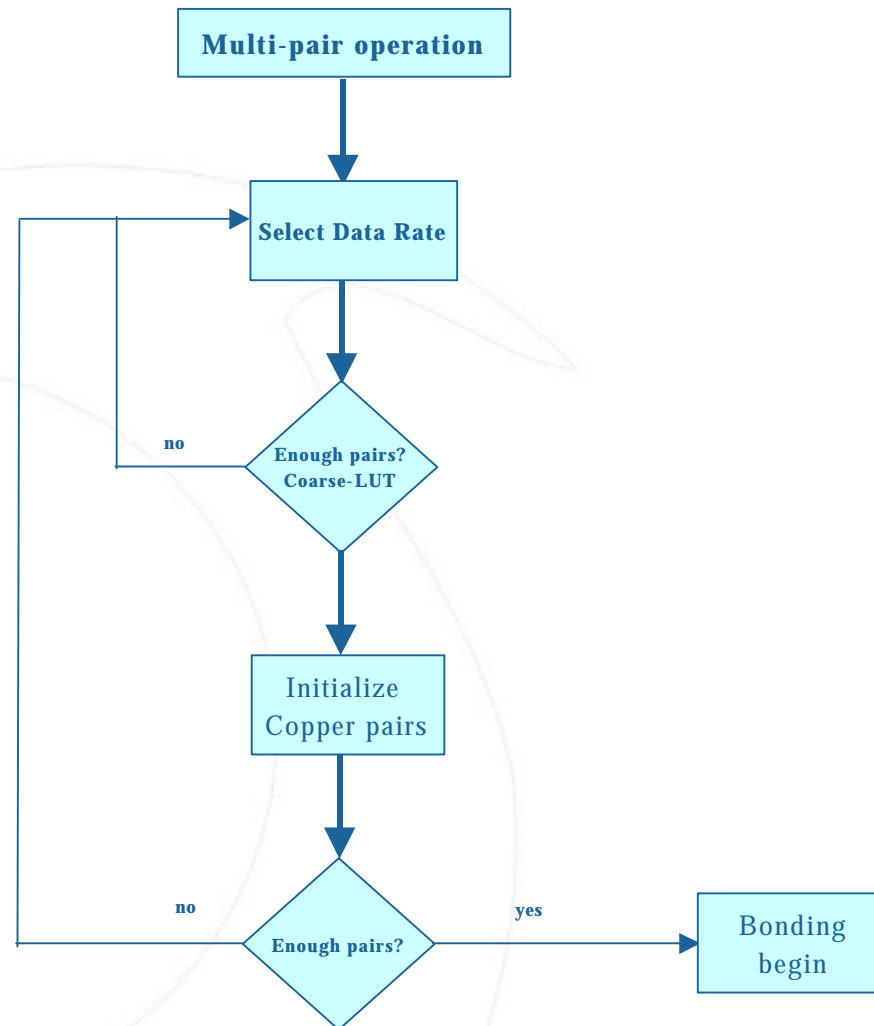


Data flow thru 4 channel Bonded DSL system

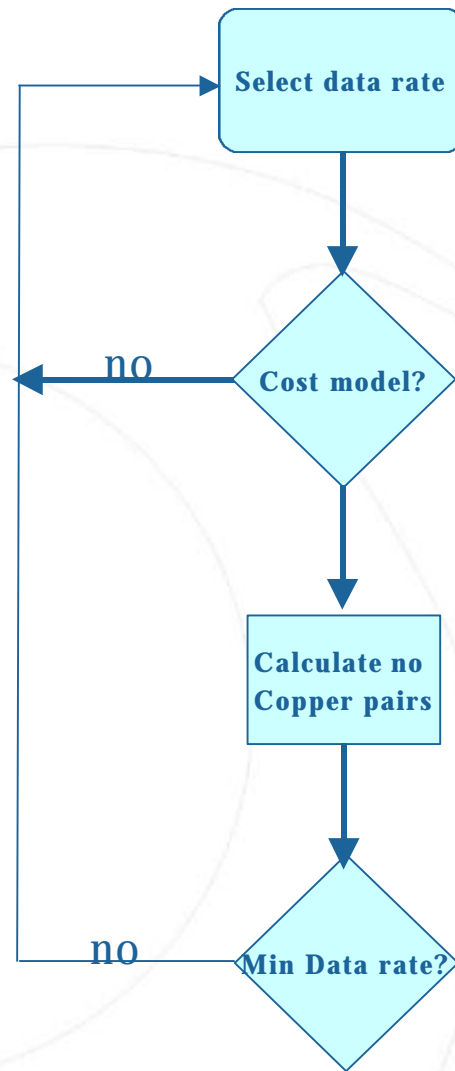
# Multi-PHY, Loop aggregation Channel Initialization

- » **Single Channel Initialization, SCI**
  - Determine channel capacity
  - Determine noise margin
  - Identify presence of narrowband and wideband disturbers
  - Determine bit rate
- » **Multi PHY Channel Initialization, MCI**
  - Repeat n times SCI
- » **Bonding**
  - Coordinate across n channels
- » **Advanced features**
  - Optimize PSD by sharing channel information

# Multi-PHY operation state diagram



# Multi-PHY operation state diagram

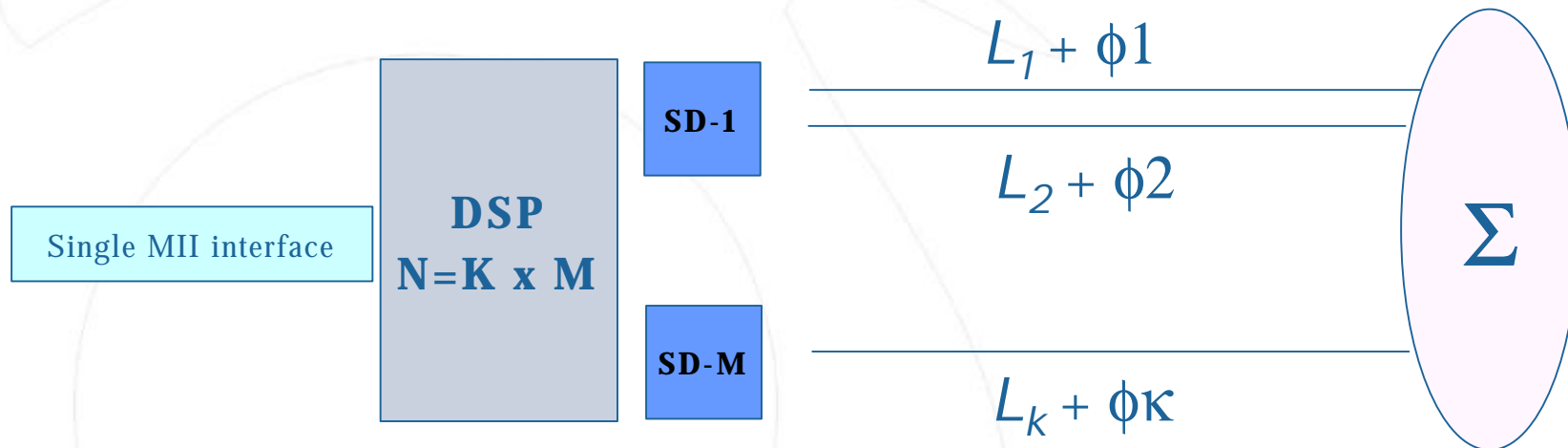




- **Optional header has sequence number:**
  - Sequence number used for segmenting and reassembly
- **Can work without header:**
  - When a line comes up, it is assigned an ID by CO side and a position in the line sequence used for segmenting and re-assembly
  - When a line goes down, EOC messages can be sent on other lines to drop the line from the line sequence
- **CRC check of segments not needed:**
  - Segments sent on each line already have CRC & RS encoder (DSL framing)
  - HDLC encapsulation adds its own CRC
  - Ethernet frames have their own 32-bit CRC

## » Bit/Byte synchronous operation, single xMII interface

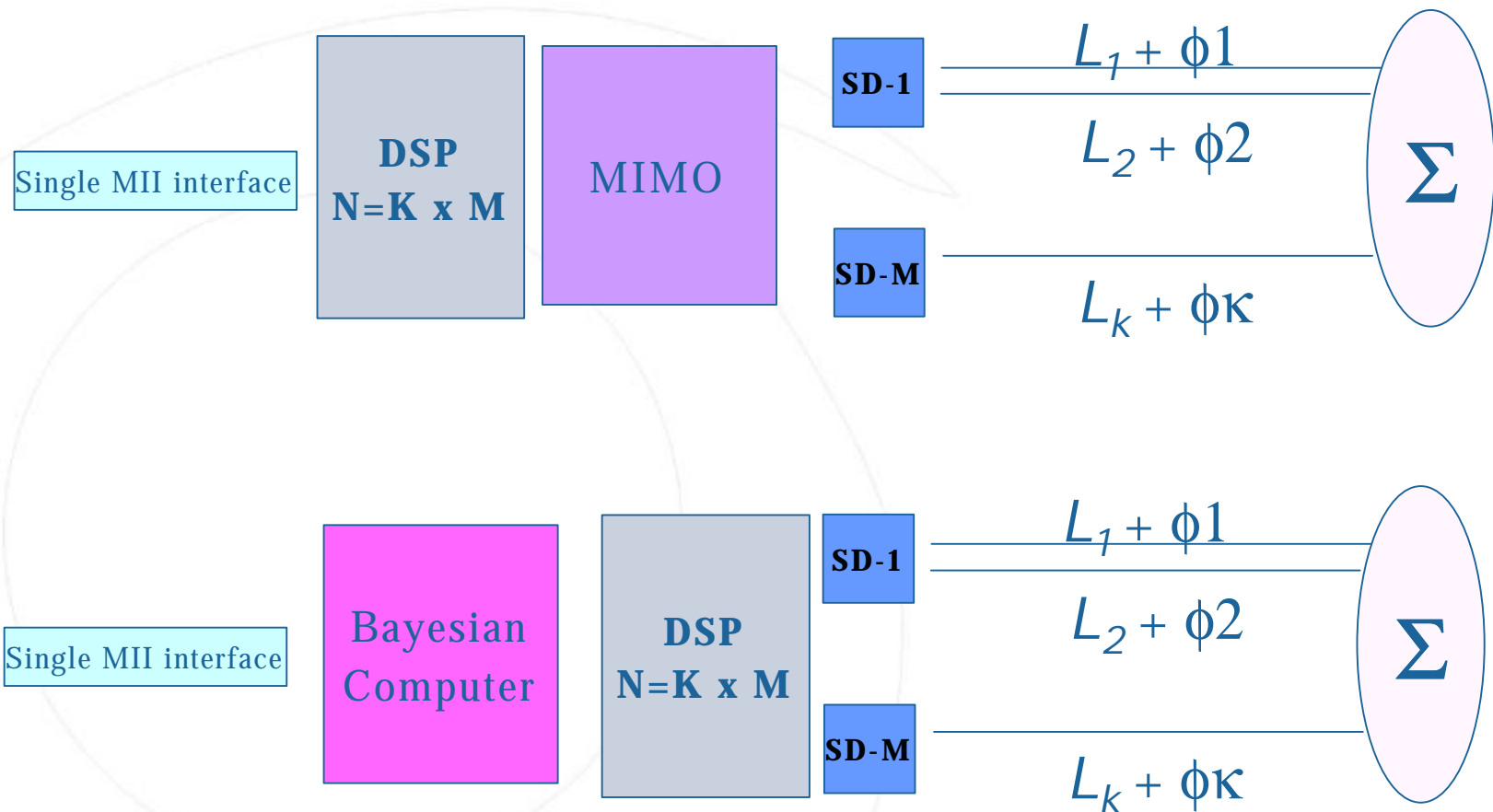
- Single N-port device operating on M sampling j-port devices, serving K lines of unequal channel capacity



# Operation of multi-PHY

## Modulation interfaces below $\alpha/\beta$ interface

» Modulation independent, line interface



# Operation of multi-PHY Modulation interfaces below $\alpha/\beta$ interface

## » Modulation Dependant operation

