**TDD sub-Task Force – Minutes December 12, 2013**

**Provided the IEEE-SA Patent Policy link. Everyone on the call was familiar with the patent policy.**

* https://development.standards.ieee.org/myproject/Public/mytools/mob/slideset.pdf

Everyone on the call was familiar with the IEEE patent policy.

**HINOC PHY: Probe Frame Introduction (Zhao Hui, Peking University)**

Q: How frequently are they sent? Is it a fixed duration or is it programmable?

A: The Pd frame is transmits periodically every 64ms. In the middle is the Pu frame.

Q: What level of time synchronization do you expect?

A: Very precise. We can transmit even 2048 QAM.

C: When you have a value maybe you can clarify this in the future.

A: In MAC layer we define (1/128) us. Approximately 7ns

Channel Allocation (Zhang Yu, Zhang Bing, Yan Kezhou, Xidian University)

Q: It says that the Pd cycle is 65536us. Is that TDD cycles?

A: No, it contains many MAP cycles.

Q: How long is a MAP cycle?

A: 2.5 ms

**Attendance**

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| **Person** | **Affiliation** |
| Zhang Cheng | Peking University |
| Lixia Deng | Peking University |
| Li Dou | Peking University |
| Hesham ElBakoury | Huawei |
| Zhao Hui | Peking University |
| Yan Kezhou | Xidian University |
| Mark Laubach | Broadcom |
| Bill Powell | Alcatel Lucent |
| Saif Rahman | Comcast |
| Duane Remein | Huawei |
| Steve Shellhammer | Qualcomm  |
| Yanbin Sun | Huawei |
| Guangsheng Wu | Huawei |
| Zhang Yu | Xidian University |
| Jin Zhang | Marvell |
| Yuping Zhao | Peking University |