# Unconfirmed Minutes - Multiple MCS IEEE 802.3bn EPoC Ad Hoc - 011513

Agenda

* Roll call
* Patent Policy
* Confirm Minutes
* Review presentation: Multiple Profiles for EPOC 11Jan2013

No response to the IEEE call for patents.

## Attendance

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| **Attendee** | **Present** |
| Andrea Garavaglia – Qualcomm | X |
| Tom Staniec - Cohere | x |
| Jim Farmer – Aurora |  |
| Avi Kliger – Broadcom | X |
| Ramdane Krikeb – Videotron | X |
| Yitshak Ohana - Broadcom | X |
| Volker Leisse - CEL | X |
| Tom Williams –Cablelabs | X |
| Alan Brown – Aurora | X |
| Satish Mudugere – Intel | X |
| Joe Solomon – Comcast |  |
| Jorge Salinger – Comcast |  |
| Bill Powell – ALU | X |
| Charaf Hanna – ST Micro | X |
| Christian Pietsch – Qualcomm | X |
| Curtis Knittle – CableLabs | X |
| Dave Urban – Comcast | X |
| David Law – HP |  |
| Duane Remein – Huawei | X |
| Dylan Ko – Qualcomm |  |
| Ed Boyd – Broadcom |  |
| Eugene Dai – Cox |  |
| George Hart – Rogers |  |
| Hesham ElBakoury – Huawei | X |
| John Dickinson – Brighthouse  |  |
| John Ulm – Motorola |  |
| Juan Montojo – Qualcomm | X |
| Leo Montreuil – Broadcom | X |
| Mark Laubach – Broadcom | X |
| Marek Hajduczenia – ZTE | X |
| Matt Schmitt – CableLabs |  |
| Michel Allard – Cogeco | X |
| Mike Darling – Shaw |  |
| Mike Emmendorfer – Arris | X |
| Nicola Varanese – Qualcomm | X |
| Patrick Stupar – Qualcomm |  |
| Peter Wolff – Titan Photonics |  |
| Saif Rahman – Comcast | X |
| Steve Shellhammer – Qualcomm |  |
| Thushara Hewavithana – Intel  | X |
| Tim Brophy – Cisco |  |
| Venkat Arunarthi – Cortina  |  |

## Review Multiple Profiles for EPOC 11Jan2013

Reviewed presentation from Andrea and Nicola.

Questions regarding interleaver and how it works on per profile. Refer to SA presentation for how interleaver works

Question regarding the funtions of “Map”; it is just a pointer to the MCS details

How are the packets sorted into profiles?

* See slide 5 for how “selected frame” is used to sort.

Is there latency added to select frames?

* No the state machine executes in zero time. Time and frequency mapping is done at PHY layer.

How many profiles do each CNU need to receive?

* Its highest and all lower profiles. It could just receive two; the common and its highest capable.

Is this proposal require a certain DBA implementation?

* No undue restriction beyond what is implied today

Discussed details of slide 8 and how grouping is done without any major impact to gate to gate cycle time

Reviewed questions from Ed Boyd.