**TDD sub-Task Force – Minutes August 26, 2013**

**Provided the IEEE-SA Patent Policy link. Everyone on the call was familiar with the patent policy.**

* https://development.standards.ieee.org/myproject/Public/mytools/mob/slideset.pdf

Everyone on the call was familiar with the IEEE patent policy.

**Data Detector for TDD Downstream**

Steve Shellhammer and Andrea Garavaglia (Qualcomm)

We discussed the possible jitter in the downstream time window

C: The presentation points out the jitter due to the different frequencies of the XGMII clock and the PHY clock.

C: In addition to that jitter there is also a jitter budget which in EPON is for the combination of the MAC and the PHY. This could result in some jitter in when the first non-Idle character is seen at the PHY interface.

C: It would be good if we could eliminate that jitter. We could make the PHY send when it expects the downstream time window to start as long as the first non-Idle character arrives within a small window around when the PHY expects the first non-Idle character, and if it does not fall within the small window then the PHY would reset the beginning of the downstream time window.

**Straw Poll #1**

Do you support this as a starting point for a baseline for the TDD downstream data detector?

Yes 9

No 0

Abstain 0

**Attendance**

|  |  |
| --- | --- |
| **Person** | **Affiliation** |
| Jim Farmer | Aurora Networks |
| Yan Kezhou | Xidian University |
| Michael Peters | Sumitomo Electric |
| Bill Powell | Alcatel Lucent |
| Saif Rahman | Comcast |
| Duane Remein | Huawei |
| Steve Shellhammer | Qualcomm |
| Joe Solomon | Comcast |
| Ron Wolfe | Aurora Networks |