

Interpretation Number: 3-07/05
Topic: PME Aggregation Restrictions
Relevant Clause: 61.2.2.5
Classification: Unambiguous

Interpretation Request

Standard: Std 802.3ah-2004
Section: 61.2.2.5: PME Aggregation Restrictions

Question:

The above section defines the term "differential latency" as the number of bits, N , that can be sent across the fast link in the time it takes one "maxFragmentSize" fragment to be sent across the slow link. In the next page it says that "maxFragmentSize"=512 octets, and that the maximum ratio between any two links is 4. Combine it with the above statement, you get the value of $4*512=2048$ octets = 16K bits for the maximal possible value of "differential latency". But in the next page, it says that the maximum value of "differential latency" should be 15000. How come?

Interpretation for IEEE Std 802.3ah-2004

There's no ambiguity in the text. The requirement for differential latency is 15000 bits