Clause 33 MPS Fix: A Simple State Diagram Approach

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The Simple Fix (1)

- Remove MPS portion of Figure 33-10 (current the Inrush and MPS state diagram)
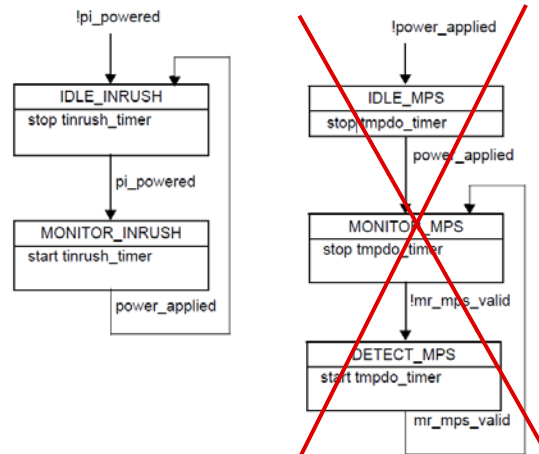 - Rename Figure 33-10 to "PSE monitor inrush state diagram"

Figure 33–10—PSE monitor inrush and monitor MPS state diagrams

- Change 33.2.9 as follows:

33.2.9 PSE power removal

Figure 33–10 shows the PSE monitor state diagrams. Thisese state diagrams monitors for inrush current and the absence of the Maintain Power Signature (MPS). If any of these this conditions exists for longer than its related time limit, the power is removed from the PI.

The Simple Fix (2)

- Replace tmpdo_timer_done in Figure 33-9 (main state diagram) with new variable mps_power_removal

Figure 33–9—PSE state diagram

The Simple Fix (3)

- Define new variable mps_power_removal in a way that references/matches the requirements in the text (Section 33.2.9).

mps_power_removal

A variable indicating that the MPS has been absent for longer than TMPDO. See 33.2.9 for MPS requirements.

Values: True: The MPS has been absent for longer than TMPDO.

False: The MPS has not been absent for longer than TMPDO.