

+-----+
| 8802-3/802.3 REVISION REQUEST 1105 |
+-----+

DATE: 14th Nov, 2002
NAME: Naresh Raman
COMPANY/AFFILIATION: LSI Logic
E-MAIL: naresh@lsil.com

REQUESTED REVISION:
STANDARD: IEEE 802.3ae-2002
CLAUSE NUMBER: 45
CLAUSE TITLE: 45.2.3.15.1

PROPOSED REVISION TEXT:

See below.

RATIONALE FOR REVISION:

The second and third occurrences of 3.32.2 in this paragraph need to be changed to 3.42.5 since it refers to this bit itself. The first occurrence of 3.32.2 is correct since it refers to the ability of the device in supporting PRBS31 pattern testing.

IMPACT ON EXISTING NETWORKS:

None. Cross reference errors.

+-----+
| Please attach supporting material, if any |
| Submit to:- Bob Grow, Chair IEEE 802.3 |
| E-Mail: Bob.Grow@intel.com |
| |
| +----- For official 802.3 use -----+ |
| REV REQ NUMBER: 1105 |
| DATE RECEIVED: 14th Nov, 2002 |
| EDITORIAL/~~TECHNICAL~~ |
| ACCEPTED/~~DENIED~~ |
| BALLOT REQ'D YES/~~NO~~ |
| COMMENTS: 08-Mar-06 Ver: D2.0 Status: P |
+-----+

+-----+
| For information about this Revision Request see - |
| http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1105 |
+-----+

Change to ANSI/IEEE Std 802.3ae-2002, Clause 45

EDITORIAL NOTES - This revision request is based on the current edition of IEEE Std 802.3ae-2002. The editing instructions define how to merge the material contained here into this base document.

Editing instructions are shown in ***bold italic***. Three editing instructions are used: change, delete, and insert. ***Change*** is used to make small corrections in existing text or tables. The editing instruction specifies the location of the change and describes what is being changed either by using ~~strikethrough~~ (to remove old material) or underscore (to add new material). ***Delete*** removes existing material. ***Insert*** adds new material without disturbing the existing material. Insertions may require renumbering. If so, renumbering instructions are given in the editing instruction. Editorial notes will not be carried over into future editions.

Editors' Notes: *To be removed prior to final publication.*

Revision History:

Draft 1.0, November 14, 2002

Initial draft.

45.2.3.15.1 PRBS31 receive test pattern enable (3.42.5)

Change subclause 45.2.3.15.1 as follows:

If the PCS supports the optional PRBS31 pattern testing advertised in bit 3.32.2 and the mandatory receive test pattern enable bit (3.42.2) is not one, setting bit ~~3.42.53.32.2~~ to a one shall set the receive path of the PCS into the PRBS31 test pattern mode. The number of errors received during a PRBS31 pattern test are recorded in register 3.43. Setting bit ~~3.42.53.32.2~~ to a zero shall disable the PRBS31 test pattern mode on the receive path of the PCS. The behavior of the PCS when in PRBS31 test pattern mode is specified in Clause 49.