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| 8802-3/802.3 REVISION REQUEST 1219 |
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DATE: 23 of February, 2010
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REQUESTED REVISION:
STANDARD: IEEE 802.3av-2009
CLAUSE NUMBER: 76.3.2.4.1
CLAUSE TITLE: FEC Algorithm (RS(255, 223))

PROPOSED REVISION TEXT:
IEEE 802.3av-2009 Figure 76-12 - 'Bit ordering in FEC codeword generation' contains 4 blocks of 66B each, marked as 'block 1'. That is incorrect and the blocks should be named 'block 1', 'block 2', 'block 3' and 'block 4' starting from the right-most block.

RATIONALE FOR REVISION:
This error was introduced in D2.2 of P802.3av by incorrect reproduction of part of resolution of comment #201960 per 3av_0811_comments_d2_1_accepted.pdf

IMPACT ON EXISTING NETWORKS:
None. Operation of the state diagrams is not affected. None of the existing technical features of 10G-EPON are affected.

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| Please attach supporting material, if any
| Submit to:- David Law, Chair IEEE 802.3
| and copy:- Wael William Diab, Vice-Chair IEEE 802.3
|
| At:- E-Mail: stds-802-3-maint-req@ieee.org
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| +----- For official 802.3 use -----+
	REV REQ NUMBER: 1219
	DATE RECEIVED: 23rd Feb, 2010
	EDITORIAL/TECHNICAL
	ACCEPTED/DENIED
	BALLOT REQ'D YES/NO
	COMMENTS: XX-Xxx-XX Ver: D1.0 Status: R
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| For information about this Revision Request see -
| http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1219
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