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2  |                8802-3/802.3 REVISION REQUEST                |
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4
5  DATE: 14-Nov-2016
6  NAME: David Law
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9
10 REQUESTED REVISION:
11   STANDARD: IEEE Std 802.3-2015
12   CLAUSE NUMBER: Figure 36-5
13   CLAUSE TITLE:  PCS transmit ordered set state diagram
14
15 PROPOSED REVISION TEXT:
16 Change the transition condition on the transition from XMIT_DATA back
17 to XMIT_DATA from 'TX_EN=FALSE * TX_OSET.indicate' to read 'TX_EN=FALSE
18 * TX_OSET.indicate * !assert_lpidle'.
19
20 RATIONALE FOR REVISION:
21 assert_lpidle is defined in subclause 36.2.5.1.3 as an alias for
22 (TX_EN=FALSE * TX_ER=TRUE * (TXD<7:0>=0x01)), hence when the condition
23 'assert_lpidle * TX_OSET.indicate' becomes true the condition
24 'TX_EN=FALSE * TX_OSET.indicate' also becomes true. This results in
25 two valid transitions out of the XMIT_DATA state which is not correct.
26 The transition to the XMIT_LPIDLE state is the only valid one.
27
28 IMPACT ON EXISTING NETWORKS:
29 None. If Low Power Idle is supported the transition to the XMIT_LPIDLE
30 state has to be implemented.
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33 |Please attach supporting material, if any                        |
34 |Submit to:-  David Law, Chair IEEE 802.3                      |
35 |              E-Mail: David_Law@ieee.org                     |
36 |
37 |              +----- For official 802.3 use -----+
38 |              | REV REQ NUMBER: 1304                       |
39 |              | DATE RECEIVED: 14th November, 2016       |
40 |              | EDITORIAL/TECHNICAL                       |
41 |              | ACCEPTED/DENIED                           |
42 |              | BALLOT REQ'D      YES/NO                   |
43 |              | COMMENTS:                                  |
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45 | For information about this Revision Request see -
46 | http://www.ieee802.org/3/maint/requests/revision\_history.html#REQ1304 |
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