+------8802-3/802.3 REVISION REQUEST 1322 -----11th September, 2018 DATE: NAME : Marek Hajduczenia; Pete Anslow COMPANY/AFFILIATION: Charter Communications; Ciena E-MAIL: marek.hajduczenia@charter.com; panslow@ciena.com REQUESTED REVISION: 802.3-2018 STANDARD: CLAUSE NUMBER: 30, 33 CLAUSE TITLE: 30.9.1.1.9 aPSEOverLoadCounter; 33.5.1.2.8 Overload (12.8); 33.8.3.7 Management function requirements; 30.9.1.1.10 aPSEShortCounter; 33.5.1.2.7 Short Circuit (12.9) PROPOSED REVISION TEXT: In 30.9.1.1.9, change: "This counter is incremented when the PSE state diagram (Figure 33-9) enters the state ERROR DELAY OVER." to: "This counter is incremented when the PSE state diagram (Figure 33-9) enters the state ERROR_DELAY due to the ovld_detected variable being TRUE." In 30.9.1.1.10, change: "This counter is incremented when the PSE state diagram (Figure 33-9) enters the state ERROR_DELAY_SHORT." to: "This counter is incremented when the PSE state diagram (Figure 33-9) enters the state ERROR_DELAY due to the short_detected variable being TRUE." In 33.5.1.2.7, change: "This bit shall be set to one when the PSE state diagram (Figure 33-9) enters the state 'ERROR DELAY.'" to: "This bit shall be set to one when the PSE state diagram (Figure 33-9) enters the state 'ERROR_DELAY' due to the short_detected variable being TRUE." In 33.5.1.2.8, change: "This bit shall be set to one when the PSE state diagram (Figure 33-9) enters the state 'ERROR DELAY OVER'." to: "This bit shall be set to one when the PSE state diagram (Figure 33-9) enters the state 'ERROR_DELAY' due to the ovld_detected variable being TRUE." In 33.8.3.7 Management function requirements, item MF28, change: "Bit indicates a short circuit condition has been detected. Set to one entering ERROR DELAY state." to: "Bit indicates a short circuit condition has been detected. Set to one entering ERROR_DELAY state due to the short_detected variable being TRUE." In 33.8.3.7 Management function requirements, item MF30, change: "Bit indicates an overload condition has been detected. Set to one when entering the ERROR_DELAY_OVER state" to: "Bit indicates an overload condition has been detected. Set to one when entering the ERROR_DELAY state due to the ovld_detected variable being TRUE"

RATIONALE FOR REVISION:

Figure 33-9-PSE state diagram does not contain state ERROR_DELAY_OVER or ERROR_DELAY_SHORT, but instead ERROR_DELAY only. The problem exists in 2018 2015, and 2012 editions of the standard. The state "ERROR_DELAY_OVER" and "ERROR_DELAY_SHORT" were present in 2008 edition but merged thereafter, but the associated text has never been updated accordingly.

+---------------+ | Please attach supporting material, if any Submit to: - David Law, Chair IEEE 802.3 and copy: - Adam Healey, Vice-Chair IEEE 802.3 At:-E-Mail: stds-802-3-maint-req@ieee.org +----- For official 802.3 use -----+ REV REQ NUMBER: 1322 DATE RECEIVED: 11th September, 2018 EDITORIAL/TECHNICAL ACCEPTED/DENIED | BALLOT REQ'D YES/NO COMMENTS: XX-Xxx-XX Ver: D1.0 Status: R +-----+ | For information about this Revision Request see http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1322