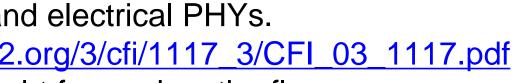


100 Gb/s per Lane for Electrical Interfaces and PHYs

## CFI Consensus Building

- 146 gathered on Tuesday night
- Panel and Contributors
  - John D'Ambrosia, Futurewei
  - David Ofelt, Juniper
  - Adam Healey, Broadcom
  - Kent Lusted, Intel
  - Beth Kochuparambil, Cisco
- Presentation given discussing market need, technical feasibility, and why now topics for 100Gb/s per lane for electrical interfaces and electrical PHYs.
  - http://www.ieee802.org/3/cfi/1117\_3/CFI\_03\_1117.pdf
  - No questions brought forward on the floor.



hank you!

## Supporters (individuals from 45 companies)

- Andy Zambell, Amphenol
- · Erdem Matoglu, Amphenol
- Amir Bar-Niv, Aquantia
- Ramin Farjad, Aquantia
- Adam Healey, Broadcom
- Henry Chen, Broadcom
- Raj Hegde, Broadcom
- Rob Stone, Broadcom
- · Arthur Marris, Cadence
- Mike Dudek, Cavium
- · Marek Hajduczenia, Charter
- Ryan Tucker, Charter
- · Gary Nicholl, Cisco Systems
- Jane Lim, Cisco Systems
- Joel Goergen, Cisco Systems
- · Mark Nowell, Cisco Systems
- Alex Umnov, Corning
- · Steve Swanson, Corning
- Haoli Qian, Credo Semiconductor\*
- · Phil Sun, Credo Semiconductor
- · Jeff Twombly, Credo

- Semiconductor
- David Piehler, Dell
- Jon Lewis, Dell EMC
- Martin Zielinski, Emerson
- · Vipul Bhatt, Finisar
- · Ali Ghiasi Ghiasi Quantum LLC
- · John Ewen, Global Foundries
- · Jacky Chang, HPE
- · Kenghua Chuang, HPE
- Andre Szczepanek, HSZ Consulting
- Yasuo Hidaka, Independent
- Ted Sprague, Infinera
- · Kapil Shrikhande, Innovium
- Adee Ran, Intel
- Howard Heck, Intel
- · Kent Lusted, Intel
- · Mike Li. Intel
- Tom Issenhuth, Issenhuth Consulting
- Jerry Pepper, IXIA

- · Rick Rabinovich, IXIA
- Thananya Baldwin, IXIA
- David Ofelt, Juniper
- · Jeff Maki, Juniper
- Steve Sekel, Keysight Technologies
- · Dale Murray, Lightcounting
- · Matt Brown, Macom
- David Malicoat, Malicoat Networking Solutions
- Jacov Brener, Marvell
- · Liav Ben-Artsi. Marvell
- · Venu Balasubramonian, Marvell
- Piers Dawe, Mellanox
- Brad Booth, Microsoft
- · Scott Sommers, Molex
- Tom Palkert, Molex/Macom
- · Mabud Choudhury, OFS
- · Rick Pimpinella, Panduit
- · Chris DiMinico, PHY-SI
- Bharat Tailor, Samtec

- Mark Kimber, Samtec
- Rich Mellitz, Samtec
- · Toshiaki Sakai, Socionext
- · David Estes, Spirent
- · Ed Nakamoto, Spirent
- Lokesh Kabra, Synopsys
- Michael Lynch, Synopsys
- Rita Horner, Synopsys
- · Pavel Zivny, Tektronix
- Megha Shanbhag, TTM Technologies
- Nathan Tracy, TTM Technologies
- · Paul Brooks, Viavi Solutions
- Mark Gustlin, Xilinx
- Takeshi Nishimura, Yamaichi Electronics USA
- Yang Zhiwei, ZTE

IEEE 802.3 November 7, 2017, Consensus Building

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## Straw Polls Taken

• Should a study group be formed for "100Gb/s per Lane for Electrical Interfaces and PHYs"?

**Yes: 137 No: 0 Abstain: 7** 

- I would participate in a "100Gb/s per lane for Electrical Interfaces and PHYs" study group in IEEE 802.3.
- My company would support participation in a "100Gb/s per lane for Electrical Interfaces and PHYs" study group.
  Tally: 45

Room Count: 146

## **Motion**

 Move that the IEEE 802.3 Working Group request the formation of a Study Group to develop a Project Authorization Request (PAR) and Criteria for Standards Development (CSD) responses for "100Gb/s per Lane for Electrical Interfaces and Electrical PHYs".

M: Beth Kochuparambil

S: Kent Lusted

>50%

Y: N: A