# TXI Access Protocol 100 Mbit/s 802.5 Changes

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# 1. Definitions

16 17	This document uses the following terms to indicate the type of IEEE 802.5 protocol being used.			
18 19 20	FDX	the 802.5 TXI	sed is used to indicate the "Full-Duplex" operation supported by Access Protocol. The TXI Access Protocol can be used to 16/4 Mbit/s "Dedicated Token Ring" Station.	
21 22 23	HDX	the 802.5 TKI	sed is used to indicate the "Half-Duplex" operation supported by P Access Protocol. The TKP Access Protocol can be used to 16/4 Mbit/s "Token Passing" Station.	
24 25	The following Standar	C	e used to describe the protocol defined by the ISO/IEC8802-5	
26 27 28	TKP A	ccess Protocol	This protocol is specified by the ISO/IEC8802-5:1995 Standard and its update: ISO/IEC 8802-5:1997 REV. These Standards specify the original "Token Passing Protocol".	
29 30 31	TXI Ac	ecess Protocol	This protocol is defined in the ISO/IEC 8802-5:1997 Standard Supplement titled "Dedicated Token Ring". This Supplement specifies the "Transmit Immediate Protocol".	

#### 32 1.1 TXI Access Protocol Network Configuration

- Figure 1-1 illustrates example TXI and TKP Access Protocol connections to 100/16/4
- 34 Mbit/s Switch Ports including:
- 1. a *new* **FDX** 100/16/4 Mbit/s Station using a 100 Mbit/s protocol based on a *modified* 16/4 Mbit/s 802.5 TXI Access Protocol,
- a FDX 100/16/4 Mbit/s Station using the *standard* 16/4 Mbit/s 802.5 TXI Access
   Protocol and
- 39 3. a **HDX** 16/4 Mbit/s Concentrator (and its attached Stations) using the *standard* 16/4 Mbit/s 802.5 TKP Access Protocol.

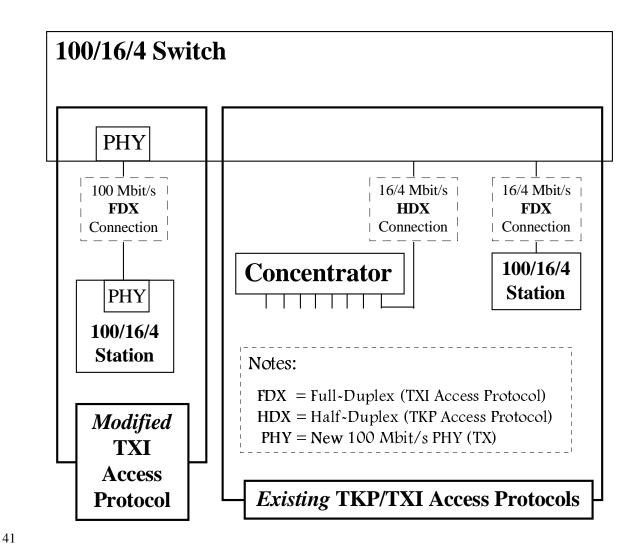


Figure 1-1 - Network Containing New 100/16/4 Mbit/s FDX Stations

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#### 2. IEEE 802.5 Architectural Changes

- This document identifies the changes needed in the IEEE 802.5 document to support 100
- Megabit per second (100 Mbit/s) Token Ring and addresses only *switched* Token Ring
- 47 support.

#### 48 2.1 TXI Access Protocol Changes

- Table 1 identifies the modifications to the IEEE 802.5 Standards document required to
- support the 100 Mbit/s TXI Access Protocol.

#### 51 **2.1.1 Design Philosophy**

- 52 The design philosophy for the 100 Mbit/s TXI Access Protocol is one of 'least change' to
- the 16/4 TXI Access Protocol defined by the ISO/IEC 8802-5:1997 Supplement defining
- the Dedicated Token Ring Port and Station.
- The TXI Access Protocol supports 100/16/4 Mbit/s operation.

#### **2.1.2 MAC Layer Assumptions**

- 57 The following MAC Layer assumptions were used in developing Table 1.
- 1. The TXI Access Protocol supports the 4, 16 and 100 Mbit/s link speeds.
- 59 2. No 802.5 timer values need to be changed.
- 3. No changes will be made to the 802.5 MAC Frame formats between the MAC and its *upper* layers.
- 62 4. The layout of the Frame and Abort Sequence as seen on the 100 Mbit/s Link are
- changed to agree with the requirements of the TX PCS/PMD framing requirements.
- This change will be made so few if any changes have to be made to the Port and
- 65 Station Operation Tables.
- 5. The Port and Station *Join* Machines will be changed either through words or minor
- Port and Station Operation Table changes to recognize the new (to 802.5) TX
- PCS/PMD Link Negotiation conditions. This includes the following.
- a) A new method of indicating a Lobe Test failure.
- 70 b) The failure of the Link Negotiation process.
- 71 c) The mapping of TX Error conditions to the 802.5 error conditions (e.g., burst error, code violation, etc.).
- d) Other TX Link Negotiation process conditions (still being defined).
- 74 These changes will be made so few, if any, changes have to be made to the Port and
- 75 Station Operation Tables.

- 76 6. The Station and Port *Transmit* Machines will be changed with minor Port and Station 77 Operation Table changes to recognize the maximum Frame size for 100 Mbit/s 78 (suggest the maximum be 4550 octets).
- 7. No changes are foreseen in the Station and Port *Monitor* Machines.
- 80 8. The Source Routing function remains unchanged.

#### 2.1.3 PHY Layer Assumptions

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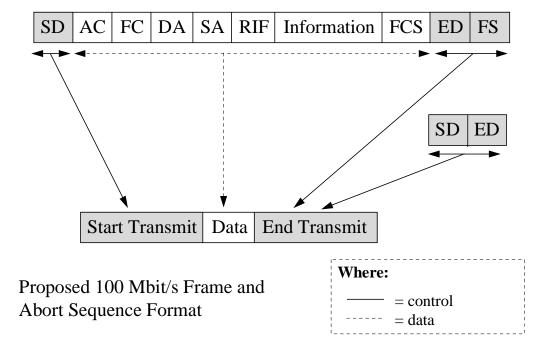
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- The following PHY Layer assumptions were used in developing Table 1.
- 1. The TXI Access Protocol supports the 4, 16 and 100 Mbit/s link speeds as follows.
- a) The Station attempts to open at the highest speed selected via Option Flags (a new Option Flag is required for 100 Mbit/s). If the Station or Port supports 100 Mbit/s, then they request the TX PHY to negotiate to bring up the link as follows.
  - i) If the TX Link Negotiation fails due to incompatibility, the Station closes (returns to Bypass).
  - ii) If the TX Link Negotiation fails due to no response (TX is not on the other end of the link), then the Station switches to the speed selected via the 16/4 Option flag (it is suggested an auto ring-speed process be initiated).
  - 2. The layout of the Frame, Token and Abort Sequence as seen on the 100 Mbit/s Link will be changed to agree with the requirements of the TX PCS/PMD framing procedure which requires *all* data to be within a single envelope.
- Figure 2-1 is an example of how the current 16/4 Mbit/s 802.5 Frame and Abort Sequence
- Formats (specified in clause 3 of ISO/IEC8802-5) can be mapped into a *single* 100 Mbit/s
- 97 TX transmission envelope that represents the 802.5 Frame and Abort Sequence Formats.
- NOTE: I have used the terms "Transmit Start" and "Transmit End" to avoid confusing them with the current "Starting Delimiter" and "Ending Delimiter" respectively already defined in ISO/IEC8802-5:1995.

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# Existing 16/4 Mbit/s Frame and Abort Sequence Formats



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Figure 2-1 - Proposed 100 Mbit/s Full-Duplex Frame and Abort Sequence Format

Figure 2-1 illustrates the following mapping of the 802.5 Frame and Abort Sequence into a single TX PHY transmission envelope as follows.

- 1. Control Mapping
  - a) The "Starting Delimiter" (SD) field of the 802.5 Frame is mapped into "Start Transmit" field.
  - b) The "Ending Delimiter" (ED) and "Frame Status" (FS) fields of the 802.5 Frame are mapped into the "End Transmit" field.
  - c) The "Starting Delimiter (SD) and Ending Delimiter (ED) fields" of the 802.5 Abort Sequence are mapped into the "End Transmit" field.
- 112 2. Data Mapping
- The fields containing the 802.5 Frame data ["Access Control (AC)"field through and including the "Frame Check Sequence (FCS)" field] are mapped into the "data" field.

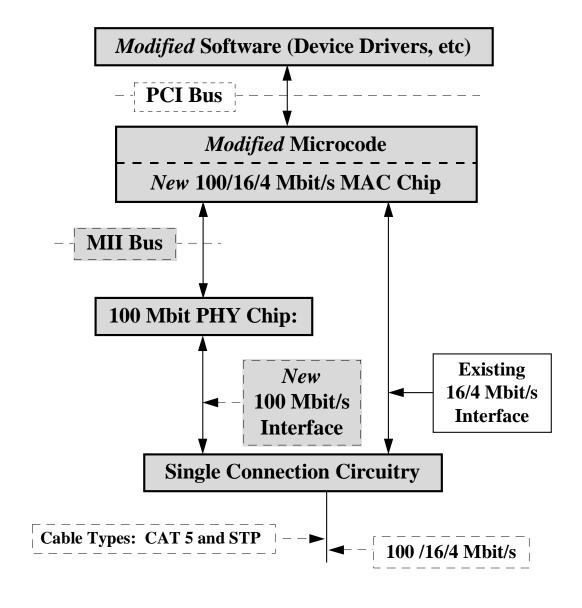
Table 1 - 100 Mbit/s TXI Access Protocol					
802.5 Change	Purpose of Change	Change Size/Risk			
Clause 1	Definitions unique to TXI Access Protocol at 100 Mbit/s.	small/low			
Clause 2	Definition of the configurations supported by the TXI Access Protocol at 100 Mbit/s.	small/low			
Clause 9.1	Define unique definitions used in 9.2 and 9.3 for the TXI Access Protocol at 100 Mbit/s.	small/low			
Clause 9.2	Change the Join Station Operation Table to support the TXI Access Protocol at 100 Mbit/s.	medium/low			
	Change the Transmit Station Operation Tables to support the TXI Access Protocol at 100 Mbit/s.	small/low			
Clause 9.3	Change the Join Port Operation Tables to support the TXI Access Protocol at 100 Mbit/s.	medium/low			
	Change the Transmit Port Operation Tables to support the TXI Access Protocol at 100 Mbit/s.	small/low			
Clause 9.7	Change the PHY requirements unique to the TXI Access Protocol at 100 Mbit/s for Clauses 9.2 and 9.3.	medium/low			
Clause 10	Change the definition of the Frame, Token and the Abort Sequence for the TXI Access Protocol at 100 Mbit/s for Clauses 9.1, 9.2 and 9.3.	medium/low			
Annex A	Change the normative requirement statements to support the TXI Access Protocol at 100 Mbit/s	small/low			
Annex L	nex L Change the Join and Transmit FSMs to support changes made to the Station Operation Tables in 9.2.				
Annex M	Change the Join and Transmit FSMs to support changes made to the Port Operation Tables in 9.3.	small/low			

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## 116 **3. Attachments**

- The following additional figures were used during the Presentation of this paper.
- Figure 3-1 is an overview of the adapter causing least change the the TXI Access
- 119 Protocol.
- Figure 3-2 is an overview to illustrate where the MII interface fits within the 802.5
- 121 Standard.
- Figure 3-3 is an overview of bit mapping into the Start Transmit and End Transmit
- functions identified in Figure 2-1.

# 100 Mbit/s Token Ring Adapter Overview



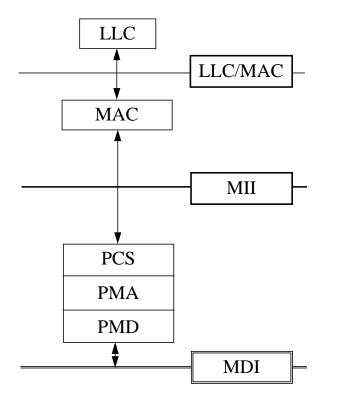
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125 Figure 3-1 - Adapter Overview

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## MAC relationship to PCS/PMA/PMD

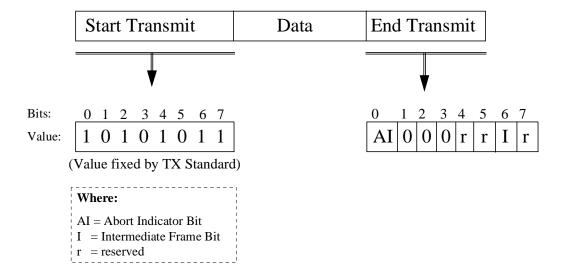


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Figure 3-2 - Importance of MII

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## Start and End Transmit Bit Mapping



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129 Figure 3-3 - Bit Mapping Overview

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