

Source: IEEE 802.3 Working Group¹
To: Mr Bernard Dumortier ,SC65C Secretary
CC: Paul Nikolich, Chair, IEEE 802 LMSC (p.nikolich@ieee.org)
Wael Diab, Secretary, IEEE 802.3 Ethernet Working Group ()
Michael McCormack, Chair, IEEE P802.3at Task Force ()
Subject: Liaison to IEC TC65/SC65C/JWG10 from IEEE 802.3
From: David Law, Chair, IEEE 802.3 Ethernet Working Group
(David_Law@3Com.com)
Approval: Agreed to at IEEE 802.3 Plenary meeting, San Francisco, CA, USA on
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Dear Mr. Dumortier and Members of the IEC TC65/SC65C/JWG10,

The IEEE 802.3 Working Group thanks the IEC TC65/SC65C/JWG10 for their kind liaison requesting performance information regarding IEEE Std802.3-2005 Clause 33 (PoE) and industrial environmental requirements.

In response to your specific inquiries:

Has IEEE had any similar reports?

The IEEE 802.3 Working Group has received no such reports. However, we do not typically receive end equipment reports.

Have IEEE members reported any testing or evaluation of expected performance of the PoE "probing & classification" procedure when used in noisy environments?

During the creation of IEEE STD 802.3af there were numerous contributions by individuals on the performance of detection and classification which are archived at http://www.ieee802.org/3/poep_study/public/index.html and <http://www.ieee802.org/3/af/public/index.html>. You may wish review these to see if they are applicable to your noise environment.

What are the IEEE recommendations or guidance to avoid damage to vulnerable PD's when used in noisy environments such as MICE E2 and E3?

The IEEE 802.3 family of standards specify electrical requirements for interoperability as well as a basic set of environmental requirements. These do not preclude or alleviate additional requirements imposed by the environment or jurisdiction of the actual installation site for the end equipment.

What are the expected bit error rates for normal Ethernet signals during such transients in PoE demand on the same or adjacent pairs?

What recommendations are proposed to mitigate any anticipated problems?

PDs transients are bounded by P802.3at Draft 4.2 section 33.3.7.5 Peak transient current and section 33.3.7.7 Ripple and noise. PDs which do not meet these requirements are non-compliant. PDs are required to include circuitry to mitigate their internal load requirement while conforming to the interface specification.

Bit error ratios and channel requirements are specified identically with PoE as without it.

Sincerely,
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