

DRAFT YANG FOR LLDP P802.1ABcu

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INTRODUCTION



- › Draft model follows IEEE Std 802.1AB-2016”
- › Configuration root
 - “/dot1q:bridges/dot1q:bridge”
- › Run-State root
 - “/dot1q:bridges-state/dot1q:bridge”
- › Grouping
- › Use of Port

LLDP CONFIG GROUPING



› grouping:

› lldp-cfg

- +---	message-fast-tx?	uint32
- +---	message-tx-hold-multiplier?	uint32
- +---	message-tx-interval?	uint32
- +---	reinit-delay?	uint32
- +---	tx-credit-max?	uint32
- +---	tx-fast-init?	uint32
- +---	notification-interval?	uint32

PORT



- › RW part: LLDP port-name refers to interface name with interface type restriction(MUST expression)
 - Different than Bridge port
- › LLDP should be located on ETH physical port or LAG
- › RO part: LLDP Port just refers to LLDP RW port instead of **interfaces-state/interface** or **interfaces/interface**, considering the same type restriction as RW.

- LLDP port type restriction

```
must "deref(..)/../type = 'ianaif:ethernetCsmacd'"
+ " or deref(..)/../type = 'ianaif:ieee8023adLag'" {
  error-message
    "The LLDP is only configured on Link Aggreagation
    and Ethernet Port.";
}
```

- Bridge port type restriction

```
when "if:type = 'ianaif:bridge' or
if:type = 'ianaif:ethernetCsmacd' or
if:type = 'ianaif:ieee8023adLag' or
if:type = 'ianaif:ilan' or
if:type = 'ianaif:pip'" {
  description
    "Applies when a Bridge interface.";
```

DISCUSSION



- › Model contains no TX or RX feature
- › TX and RX can be dynamically configured on a port
 - Shouldn't be statically defined using if-feature

MODEL



> Draft Model



ieee802-dot1ab-lldp.yang

> Draft Tree



ieee802-dot1ab-lldp.tree

NOTE: In Powerpoint double-click on the icon above

In PDF the files are attachments (View->Show/Hide->Navigation Panes->Attachments)



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