P802.1CQ-D0.8, Comment 3 - Supportive Slides -

Johannes Specht (Self)

Jens Bierschenk (Robert Bosch GmbH)

Introduction

Comment

The main focus in the document is on the dynamic protocol. The commenter believes that there is value to separately describe the forwarding lookup addressing scheme, and allow static configurable associated FDB entries (e.g., YANG) for potential use-cases in static engineered applications. By appropriate definitions of conformance requirements, this could allow conformant implementations that do not implement the dynamic protocol, just the static configuration means.

Contained Aspects

- 1. Static configuration (e.g., via YANG)
- 2. New FDB Format(s)
- 3. Conformance

Motivation for Static Configuration

- "Classic" Automotive Applications ...
 - <u>... do not require</u> on-the-fly configuration computation
 - "Classic" Automotive applications rely on configurations entirely engineered prior to car (mass-)production.
 - Engineering can take days, weeks, months, or even years however, once every configuration parameter in every station is determined, this configuration can be re-produced quickly per car.
 - ... do not like indeterministic address configurations
 - Different address ranges assigned per car during mass production: In some cars, the FDB may fall back to flooding → QoS guarantees gone → inacceptable
 - <u>Different address ranges assigned per power cycle:</u> Sometimes, on car startup (turn key), the FDB may fall back to flooding → QoS guarantees sometimes gone → inacceptable
 - ... do not like slow address negotiation processes on startup
 - After turning the key (startup), the network operations state must be reached quickly (e.g., < 100 ms)
 → Typically not enough time for address negotiation by a distributed protocol.
- Examples for achieving this
 - Storing engineered address configurations in NVM of each ECU ("classic")
 - Storing engineered address configurations in NVM of a car-central 802.1CQ Registrar, distribute to ECUs on startup, provided that feasibility for Automotive applications (startup time, determinism, etc.) can be proven (further analysis needed)

Motivation: New FDB Formats

<u>Current FDB Formats</u>

- The current FDB entry types (802.1Q) are essentially composed of two components (VLANs skipped):
 - 1. A MAC address, the entry with an **exact DA match** is selected (bit-by-bit comparison)
 - 2. A port map, one bit per port: $1 \rightarrow$ transmission port; $0 \rightarrow$ no transmission port
- MMRP (802.1Q) supports address blocks. But this is not reflected in the FDB, and not suited for fast startup.

<u>The Issue</u>

 FDB-size matters in cost sensitive markets - with one FDB entry per Stream, the FDB capacity requirement can become ... significant.

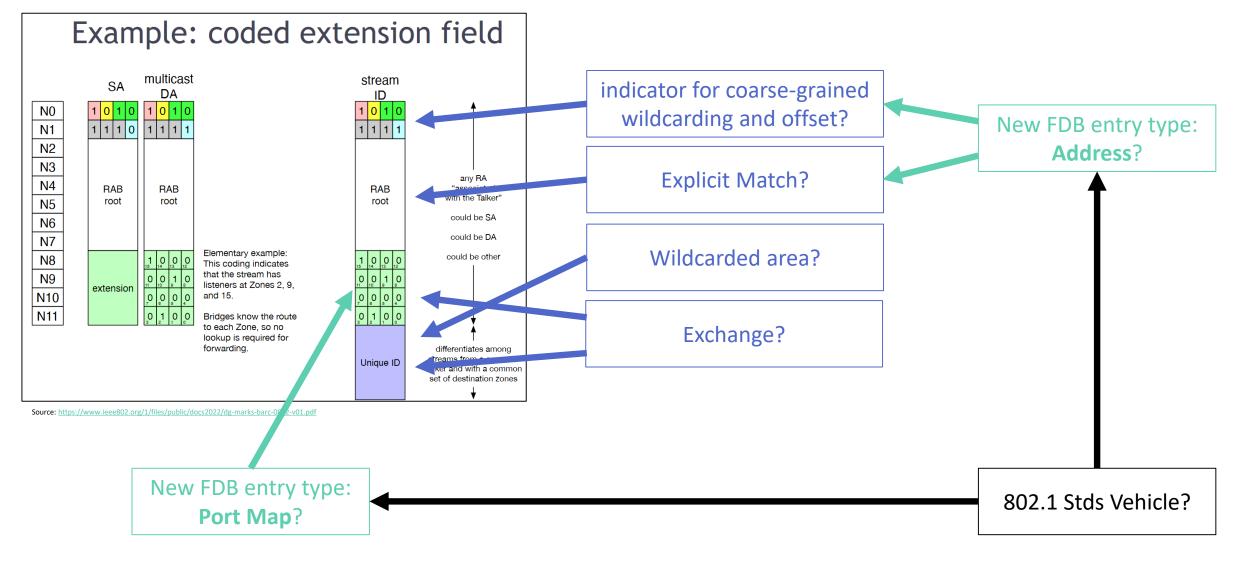
• <u>The Idea</u>

- Wildcarding of coarse-grained MAC address bit blocks in a single FDB entry appears promising ...
- A block of streams could be covered by a single FDB entry (relaxed FDB requirement)
- Nothing new in Bridges:
 - Several TCAM-based implementations should be already provide the capability (fine-grained), but
 - the capability is not standardized, and
 - would not be friendly to hash-based FDB implementations
 - P802.1CBdb introduced mask-and-match filter
 - hash-based unfriendly (fine-grained)
 - Unavailable for FDB forwarding port lookup
- Coarse-grained block-wise wildcarding may mitigate "unfriendliness" for hash-based implementations
 - Requires a indicator for coarse-grained wildcarding and offset

Discussion (1)

IEEE Std 802.1CBdb Stream Identification (DA only)				
	DA ₀	DA ₁₁	DA ₃₅	DA ₄₇
Mask Vector		0 1	110111111	1
Match Vector	0001101010	10	01010101010	01
TCAM-based Implementation	0 X 0 0 1 X X X 1 X	X 1	1 1 X 1 1 1 X 1 1 1 X	< 1
IEEE Std 802.1Q FDB	DA ₀	DA ₁₁	DA ₃₅	DA ₄₇
Entry	0001100010	01	DA ₃₅ 0 1 0 1 0 1 0 1 1 1 0	011010001
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	DA			Port Map (8 ports)
New FDB Entry				
Entry	1001100010	01	0101XXXXXX	X 1010001
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	wildcarding of		1 Wildcard	Port Map (8 ports)
		1301		

Discussion (2)



Thank You for Your Attention!

Questions, Comments, Opinions, Ideas?