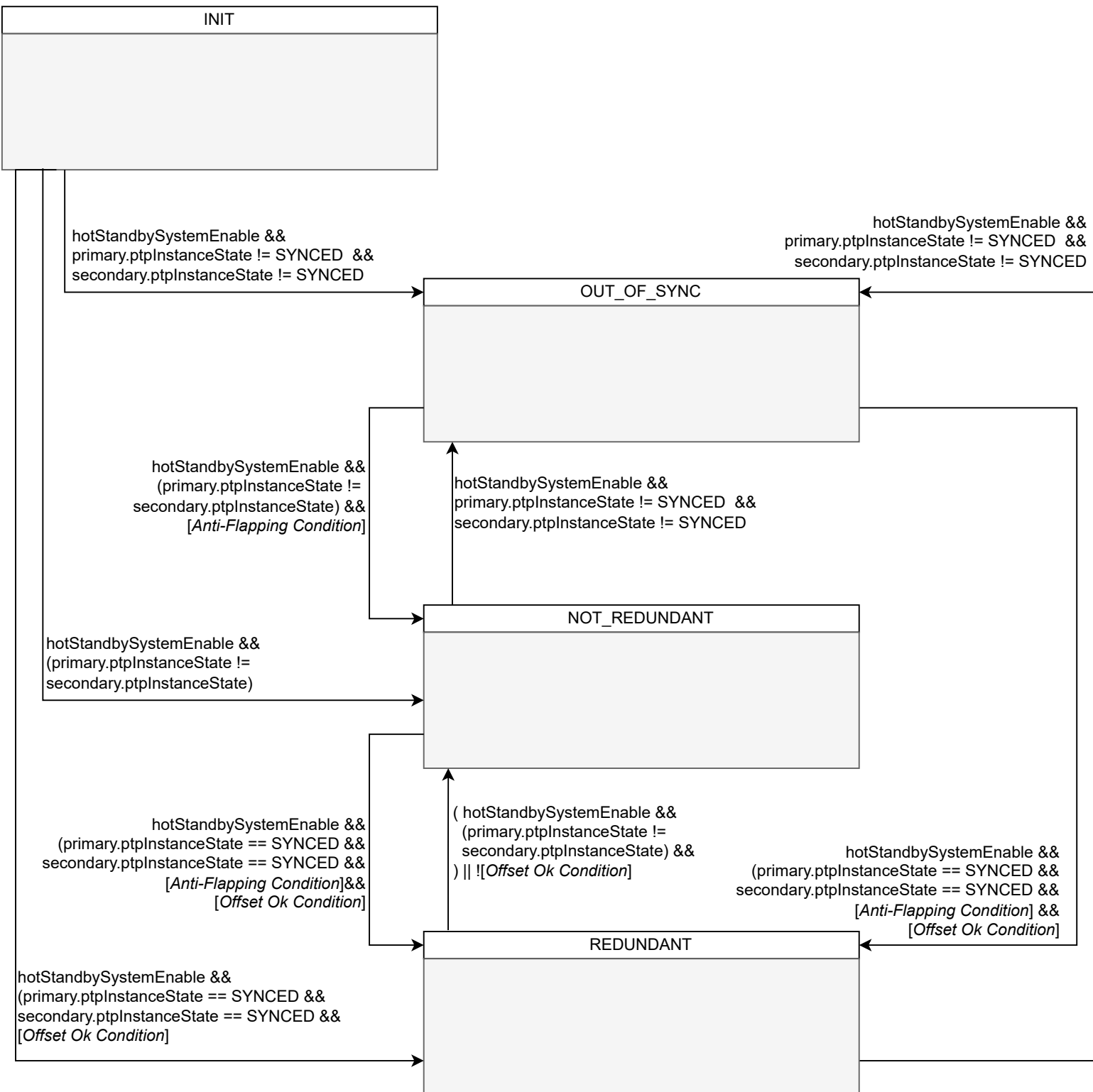


Johannes Specht (Self), Individual Contribution

This state diagram is an abstract proposal for structuring the next version of Figure 17-3 from IEEE P802.1ASdm/D1.0.

It is an early version and it is abstract in the sense that the state actions are not shown in detail (grey), and some conditions on transitions are partially given in prose (and in brackets). The authors intent is to illustrate a conservative approach for fault-tolerance in the given context, and simplify the layout of the diagram.



Abstract Conditions

[Offset Ok Condition]: The offset between primary and secondary time is low enough.

[Anti-Flapping Condition]: A local timeout expired. The timeout is used to prevent too fast transitions between two states ("toggling").