

Project	IEEE 802.16 Broadband Wireless Access Working Group < <a href="http://ieee802.org/16">http://ieee802.org/16</a> >	
Title	LDPC coding for OFDMA PHY	
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Re: IEEE P802.16-REVe/D5-2004, sponsor ballot

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Abstract This contribution contains additional text output from an informal LDPC group.

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Purpose Provide additional LDPC specification text.

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located at the top and the bottom of  $\mathbf{h}_b$  are assigned equal shift sizes, and the third 1 in the middle of  $\mathbf{h}_b$  is given an unpaired shift size. The unpaired shift size is 0.

## Method 1

Encoding is the process of determining the parity sequence  $\mathbf{p}$  given an information sequence  $\mathbf{s}$ . To encode, the information block  $\mathbf{s}$  is divided into  $k_b = n_b - m_b$  groups of  $z$  bits. Let this grouped  $\mathbf{s}$  be denoted  $\mathbf{u}$ ,

$$\mathbf{u} = [\mathbf{u}(0) \quad \mathbf{u}(1) \quad \cdots \quad \mathbf{u}(k_b - 1)],$$

where each element of  $\mathbf{u}$  is a column vector as follows

$$\mathbf{u}(i) = [s_{iz} \quad s_{iz+1} \quad \cdots \quad s_{(i+1)z-1}]^T$$

Using the model matrix  $\mathbf{H}_{bm}$ , the parity sequence  $\mathbf{p}$  is determined in groups of  $z$ . Let the grouped parity sequence  $\mathbf{p}$  be denoted  $\mathbf{v}$ ,

$$\mathbf{v} = [\mathbf{v}(0) \quad \mathbf{v}(1) \quad \cdots \quad \mathbf{v}(m_b - 1)],$$

where each element of  $\mathbf{v}$  is a column vector as follows

$$\mathbf{v}(i) = [p_{iz} \quad p_{iz+1} \quad \cdots \quad p_{(i+1)z-1}]^T$$

Encoding proceeds in two steps, (a) initialization, which determines  $\mathbf{v}(0)$ , and (b) recursion, which determines  $\mathbf{v}(i+1)$  from  $\mathbf{v}(i)$ ,  $0 \leq i \leq m_b - 2$ .

An expression for  $\mathbf{v}(0)$  can be derived by summing over the rows of  $\mathbf{H}_{bm}$  to obtain

$$\mathbf{P}_{p(x,k_b)} \mathbf{v}(0) = \sum_{j=0}^{k_b-1} \sum_{i=0}^{m_b-1} \mathbf{P}_{p(i,j)} \mathbf{u}(j) \quad (1)$$

where  $x$ ,  $1 \leq x \leq m_b - 2$ , is the row index of  $\mathbf{h}_{bm}$  where the entry is nonnegative and unpaired, and  $\mathbf{P}_i$  represents the  $z \times z$  identity matrix circularly right shifted by size  $i$ . Equation (1) is solved for  $\mathbf{v}(0)$  by multiplying by  $\mathbf{P}_{p(x,k_b)}^{-1}$ , and  $\mathbf{P}_{p(x,k_b)}^{-1} = \mathbf{P}_{z-p(x,k_b)}$  since  $p(x,k_b)$  represents a circular shift.

Considering the structure of  $\mathbf{H}_{b2}$ , the recursion can be derived as follows,

$$\mathbf{v}(1) = \sum_{j=0}^{k_b-1} \mathbf{P}_{p(i,j)} \mathbf{u}(j) + \mathbf{P}_{p(i,k_b)} \mathbf{v}(0), \quad i = 0, \quad (2)$$

$$\mathbf{v}(i+1) = \mathbf{v}(i) + \sum_{j=0}^{k_b-1} \mathbf{P}_{p(i,j)} \mathbf{u}(j) + \mathbf{P}_{p(i,k_b)} \mathbf{v}(0), \quad i = 1, \dots, m_b - 2 \quad (3)$$

where

$$\mathbf{P}_{-1} \equiv \mathbf{0}_{z \times z}.$$

Thus all parity bits not in  $\mathbf{v}(0)$  are determined by evaluating Equation (2) for  $0 \leq i \leq m_b - 2$ .

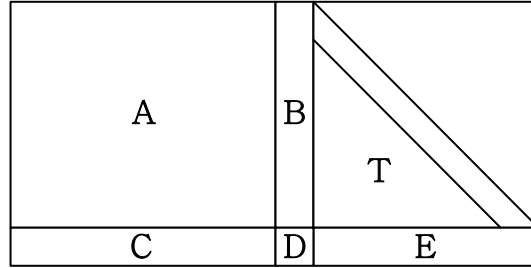
Equations (1) and (2) completely describe the encoding algorithm. These equations also have a straightforward interpretation in terms of standard digital logic architectures. Since the non-zero elements  $p(i,j)$  of  $\mathbf{H}_{bm}$  represent circular shift sizes of a vector, all products of the form  $\mathbf{P}_{p(i,j)} \mathbf{u}(j)$  can be implemented by a size- $z$  barrel shifter.

## Method 2

For efficient encoding of LDPC,  $\mathbf{H}$  are divided into the form

$$\mathbf{H} = \begin{pmatrix} \mathbf{A} & \mathbf{B} & \mathbf{T} \\ \mathbf{C} & \mathbf{D} & \mathbf{E} \end{pmatrix} \quad (1)$$

where  $\mathbf{A}$  is  $(N_p - g) \times N_k$ ,  $\mathbf{B}$  is  $(N_p - g) \times g$ ,  $\mathbf{T}$  is  $(N_p - g) \times (N_p - g)$ ,  $\mathbf{C}$  is  $g \times N_k$ ,  $\mathbf{D}$  is  $g \times g$ , and finally,  $\mathbf{E}$  is  $g \times (N_p - g)$ . The basic structure of the  $\mathbf{H}$  matrix is



Further, all these matrices are sparse and  $\mathbf{T}$  is lower triangular with ones along the diagonal.  $\mathbf{B}$  and  $\mathbf{D}$  part have the column degree 3 and  $\mathbf{D}$  has shift value of  $a$  ( $a$  is an integer,  $0 \leq a \leq z-1$ ).  $\mathbf{B}$  is with the shift value  $a$  of the first entry and shift value 0 in the middle of the column. This other entry is non-zero.

Let  $\mathbf{v} = (\mathbf{u}, \mathbf{p}_1, \mathbf{p}_2)$  that  $\mathbf{u}$  denotes the systematic part,  $\mathbf{p}_1$  and  $\mathbf{p}_2$  combined denote the parity part,  $\mathbf{p}_1$  has length  $g$ , and  $\mathbf{p}_2$  has length  $(N_p - g)$ . The definition equation  $\mathbf{H} \cdot \mathbf{v}^T = \mathbf{0}$  splits into two equations, as in equation 3 and 4 namely

$$\mathbf{A}\mathbf{u}^T + \mathbf{B}\mathbf{p}_1^T + \mathbf{T}\mathbf{p}_2^T = \mathbf{0} \quad (2)$$

and

$$(-\mathbf{E}\mathbf{T}^{-1}\mathbf{A} + \mathbf{C})\mathbf{u}^T + (-\mathbf{E}\mathbf{T}^{-1}\mathbf{B} + \mathbf{D})\mathbf{p}_1^T = \mathbf{0} \quad (3)$$

Define  $\phi := -\mathbf{E}\mathbf{T}^{-1}\mathbf{B} + \mathbf{D}$  and when we use the parity check matrix as indicated appendix we can get  $\phi = \mathbf{I}$ . Then from (4) we conclude that

$$\mathbf{p}_1^T = (-\mathbf{E}\mathbf{T}^{-1}\mathbf{A} + \mathbf{C})\mathbf{u}^T \quad (5)$$

and

$$\mathbf{p}_2^T = \mathbf{T}^{-1}(\mathbf{A}\mathbf{u}^T + \mathbf{B}\mathbf{p}_1^T). \quad (6)$$

As a result, the encoding procedures and the corresponding operations can be summarized below and illustrated in Fig. 1.

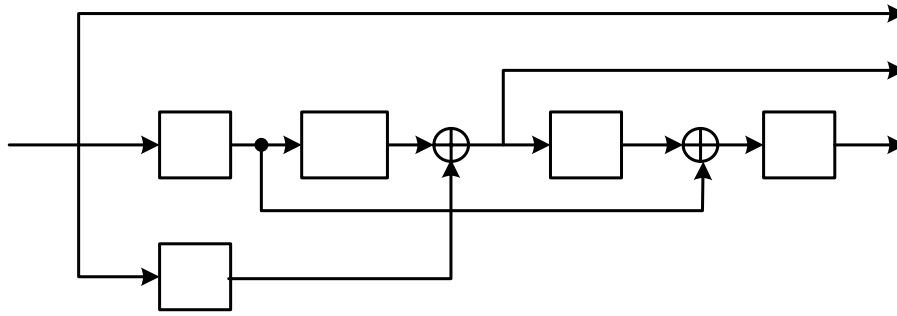
### Encoding procedure

**Step 1)** Compute  $\mathbf{A}\mathbf{u}^T$  and  $\mathbf{C}\mathbf{u}^T$ .

**Step 2)** Compute  $\mathbf{E}\mathbf{T}^{-1}(\mathbf{A}\mathbf{u}^T)$ .

**Step 3)** Compute  $\mathbf{p}_1^T$  by  $\mathbf{p}_1^T = \mathbf{E}\mathbf{T}^{-1}(\mathbf{A}\mathbf{u}^T) + \mathbf{C}\mathbf{u}^T$ .

**Step 4)** Compute  $\mathbf{p}_2^T$  by  $\mathbf{T}\mathbf{p}_2^T = \mathbf{A}\mathbf{u}^T + \mathbf{B}\mathbf{p}_1^T$ .



**Fig. 2** Block diagram of the encoder architecture for the block LDPC code.

*A*

*u*

*C*