Project	IEEE 802.16 Broadband Wireless Access Working Group <http: 16="" ieee802.org=""></http:>		
Title	Proposed Template for Simulation Assumptions for Draft IEEE 802.16m Evaluation Methodology Document		
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Re:	IEEE 802.16m-07/014r1– Call for Comments on Draft 802.16m Evaluation Methodology Document		
Abstract	This document contains proposed text for the draft evaluation methodology for IEEE 802.16m technical proposals.		
Purpose	For discussion and approval by TGm		
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1. Introduction and Background

The purpose of this contribution is to outline simulation assumptions that proponents will need to provide in order to facilitate independent assessment of their proposals. The Tables 2-1 and 2-2 in this contribution further provide updates to the Tables 2.3-1 and 2-3.2 respectively in Section 2.3 of http://ieee802.org/16/tgm/contrib/C80216m-07_080r1.zip. The blank entries of the following tables are expected to be filled by the proponents.

2. Simulation Assumptions

Topic	Baseline Assumptions Where Applicable	Additional Assumptions
Basic modulation		
Duplexing scheme	TDD and FDD	
Resource block definition		
Downlink pilot structure		
Receiver Structure	MMSE	
Data Channel coding	Convolutional Turbo Coding (CTC)	
Multiple antenna configuration		
Scheduling	As required by mandatory traffic mixes	
Link adaptation		
PHY Abstraction for Link to System Mapping	EESM	
H-ARQ	Asynchronous Chase Combining	
Power Control		
Interference Model		
Frequency Reuse	1/3/1 and 1/3/3	
Control signalling		

Table 2-1: System-level simulation assumptions for the downlink

Topic	Baseline Assumptions Where Applicable	Additional Assumptions
Basic modulation		
Duplexing scheme	TDD and FDD	
Resource block definition		
Data multiplexing		
Pilot structure		
Receiver Structure	MMSE	
Data channel coding	Convolutional Turbo Coding (CTC)	
Multiple antenna configuration		
Random Access		
Scheduling	As required by mandatory traffic mixes	
Link adaptation		
H-ARQ	Asynchronous Chase Combining	
Power Control		
Interference Model		
Frequency Reuse	1/3/1 and 1/3/3	
Control signalling		

Table 2-2: System-level simulation assumptions for the uplink