

# *i*PT Fairness

# **Controlled Access Protocol**

## **Simulation Report**

## Content

## Simulation Setup

- Reference Network
- Traffic source:
  - TCP: real world traffic profile
  - UDP: for steady state

## Simulation

- Performance metrics
- Result
- What's Next

## **Independent Control Messages on the Ring**

## Message only lives within the bound of a ring

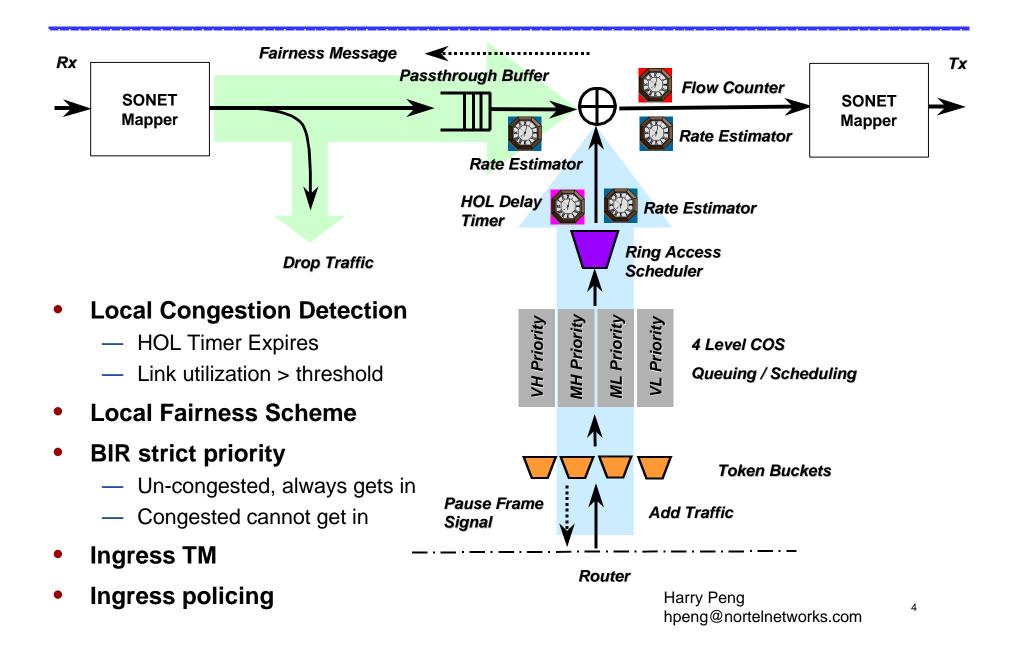
- Infrequent
  - On demand and soft state
- Scalable
  - OC-3 to OC-192 WAN rate
- Low overhead
- Easily Upgradeable
  - Support CoS
  - Support unfairness
  - Flexible

## **Performance Metrics**

## Single Ring Network Performance

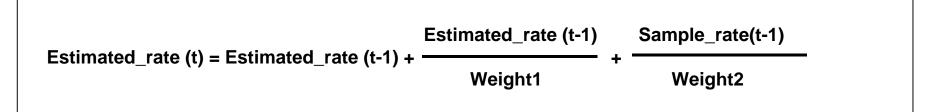
- Head of Line Delay: head of line delay at the queue output
- Access Delay: measured from packet accepted into the box
- Nodal Throughput: average throughput
- Link utilization: average ring utilization
- Fair performance: equal behavior for all participants
- Good Reaction time: ~1 ms response time
- Stable performance: small oscillation and convergence time time
- Scalable solution
- 10 us delay per link

## **iPT Node Model**



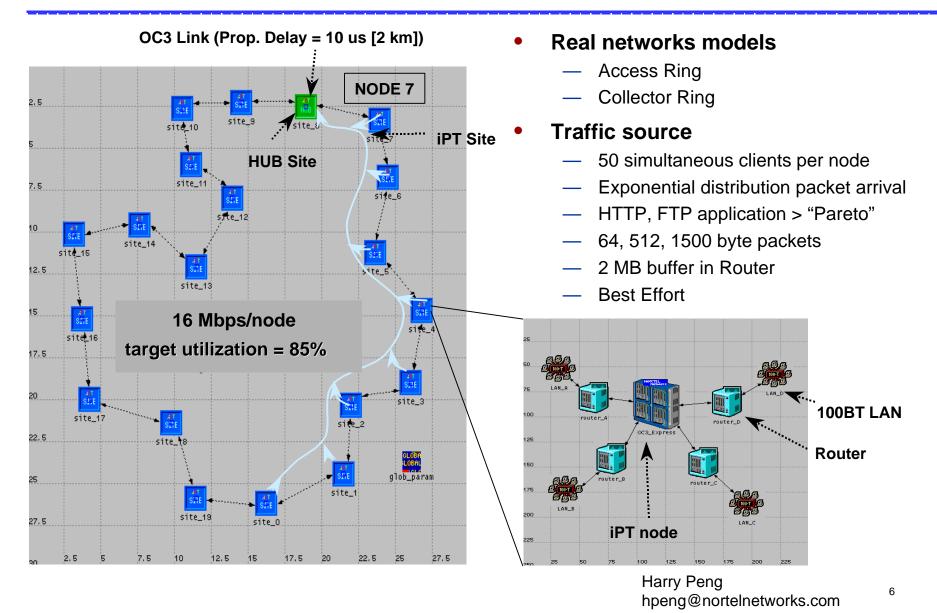
## **Nodal Components**

• Rate estimator<sup>10</sup>:

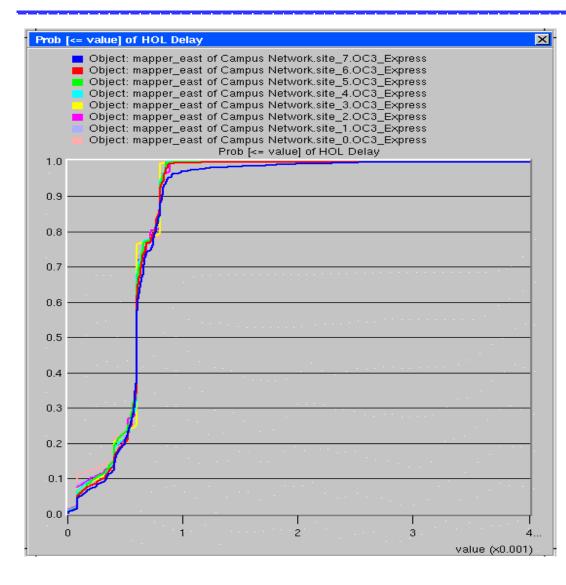


- Estimated\_rate(t) is the current estimated rate
- Estimated\_rate(t-1) is the previous estimated rate
- Sample\_rate(t) is the current sampled rate over T<sub>sample</sub>
- Weight1 and Weight2 can be independent
- Algorithm
  - Rate measuring and Delay driven
  - Round trip delay
  - Responsive on demand with stability

## Simulation Network OC-3: <sup>1</sup>/<sub>2</sub> Ring 8 Node Hub; Ingress Policed at 16 Mbps

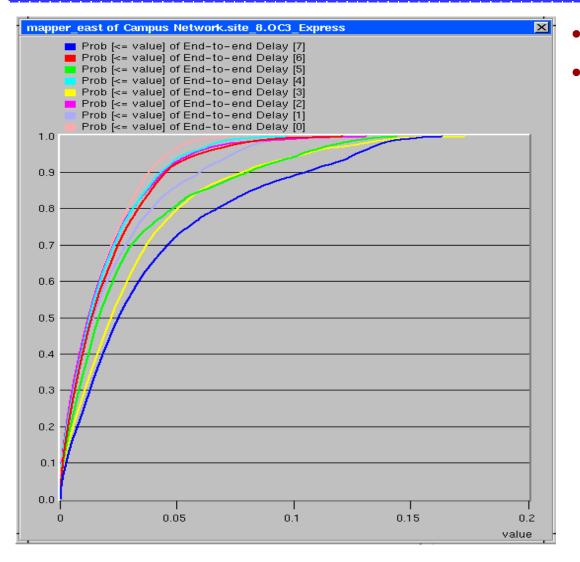


## OC-3 Ring Access: Head of Line Delay 85% Utilization Threshold



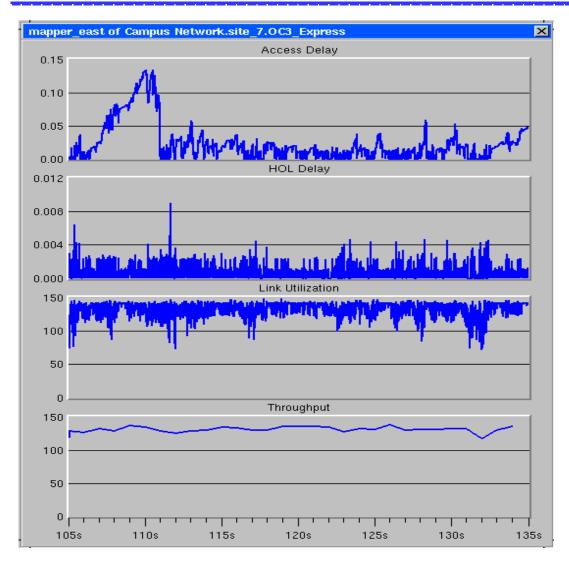
- Faster ring access in all ring operating conditions
- Head of Line Delay
  - Head of line delay timer
  - Cummulative Distribution
    Function
- 95% of all packets have less than 1 ms HOL delay
- Fair Access

## OC-3 End to End Delay 85% Utilization Threshold



- Sustained Congested state
- Fair performance to all nodes

## OC-3 Node 7 Internal Performance Characteristics 85% Utilization Threshold



1<sup>st</sup> 5 seconds of simulation

#### **Access Delay**

 Accumulative effect on access delay for burst

#### HOL Delay

Second

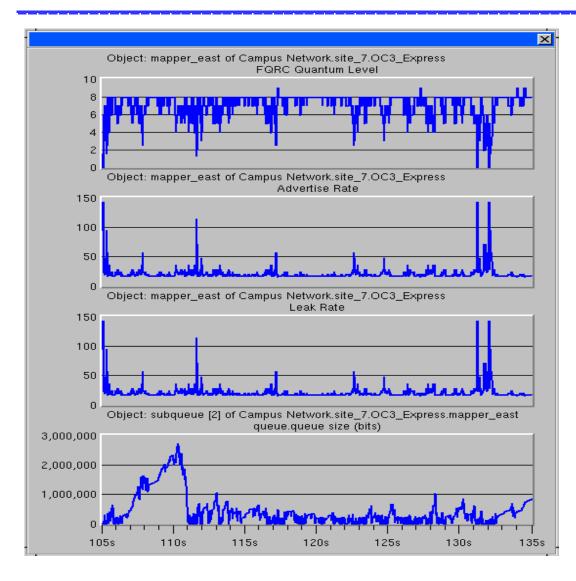
#### Link Utilization

• 150Mb/s = OC-3

#### **Throughput**

• 150Mb/s = OC-3

## OC-3 Node 7 Internal Performance Characteristics 85% Utilization Threshold (Cont'd)



1<sup>st</sup> 5 seconds of simulation

#### **Fairness State**

 Number of nodes in congestion span

#### **Advertised Rate**

• 150 Mb/s

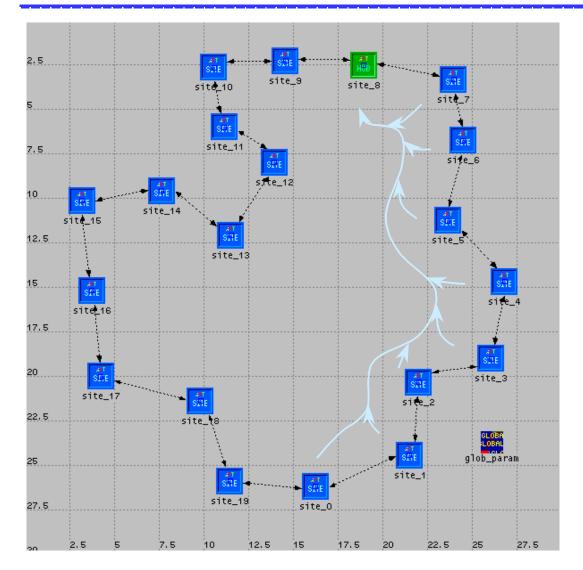
#### Leak/ Add Rate

• 150Mb/s = OC-3

#### **iPT Node Queue Occupancy**

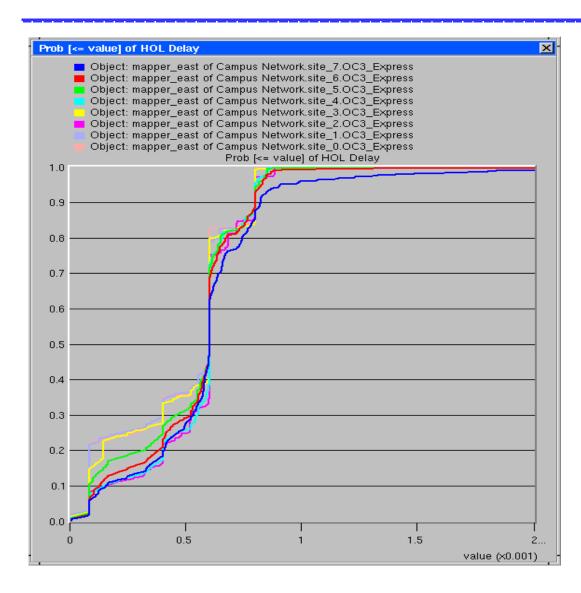
- 3 Mb
- Bursty input

## OC-3: <sup>1</sup>/<sub>2</sub> Ring 8 node Hub Ingress Policed at 18 Mbps

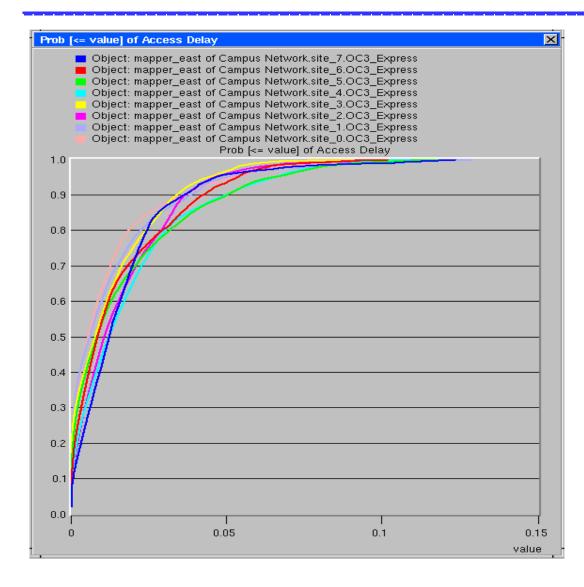


- Ingress policing set at 18 Mbps
- Traffic source still set at 16 Mbps
- Excess burst capability

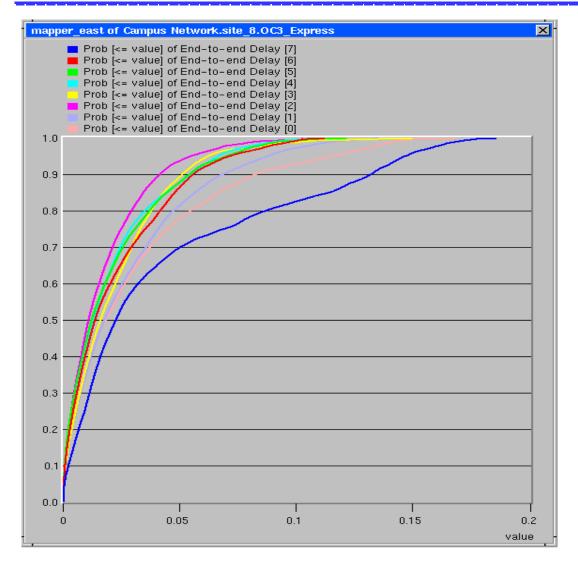
## OC-3: 8 node Hub



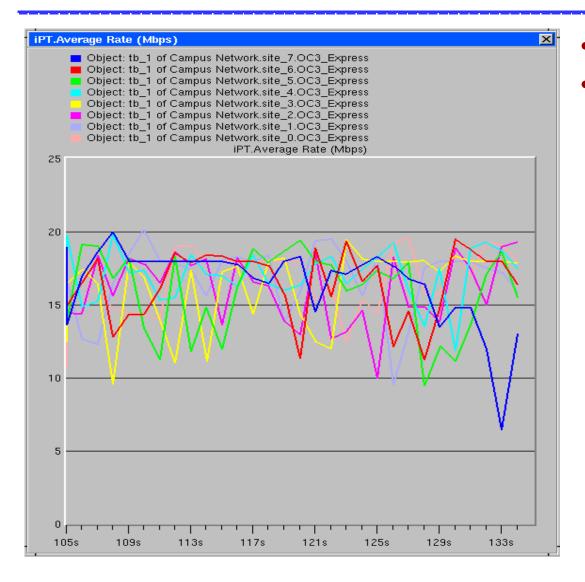
- Fair HOL Delay
- Slightly better than 16 Mbps ingress policing



- Fair Access Delay
- Much better than 16 Mbps ingress policing

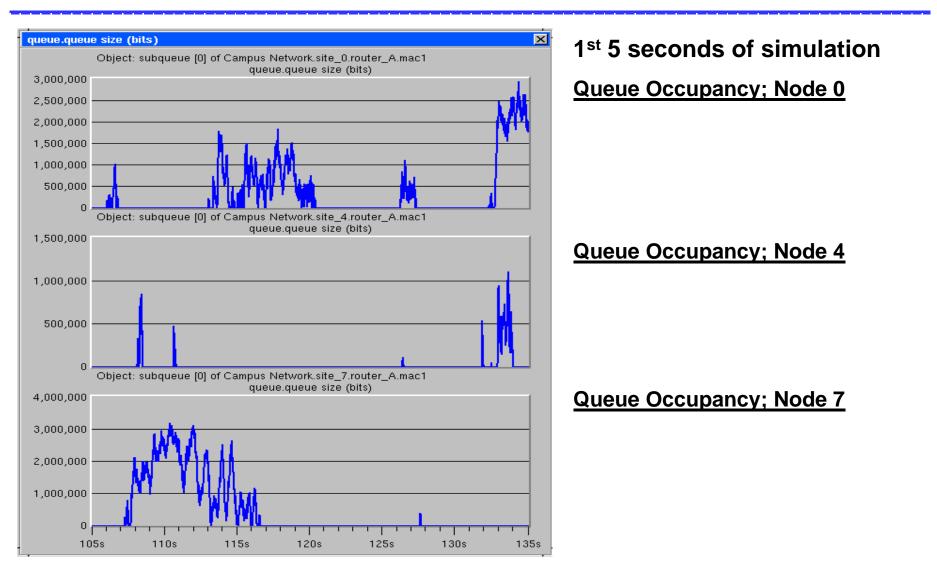


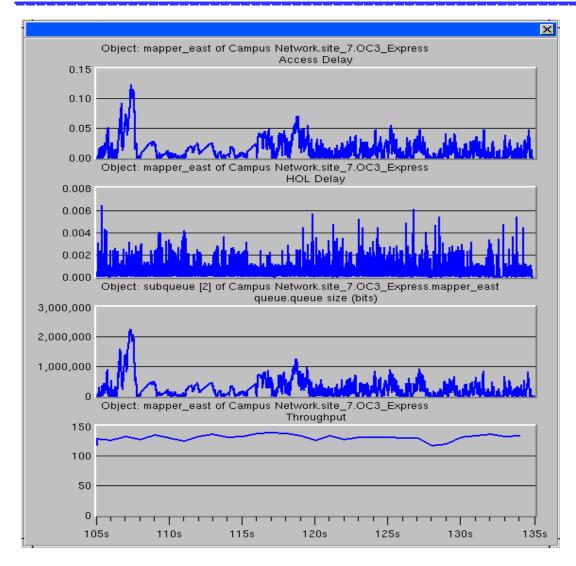
- Fair Access Delay
- Better than 16 Mbps ingress policing



- Ingress Average rate
- Target is 16 Mbps

## Insight into Traffic profile is bursty





#### 1<sup>st</sup> 5 seconds of simulation

#### **Access Delay**

 Accumulative effect on access delay for burst

#### HOL Delay

• Order of ms

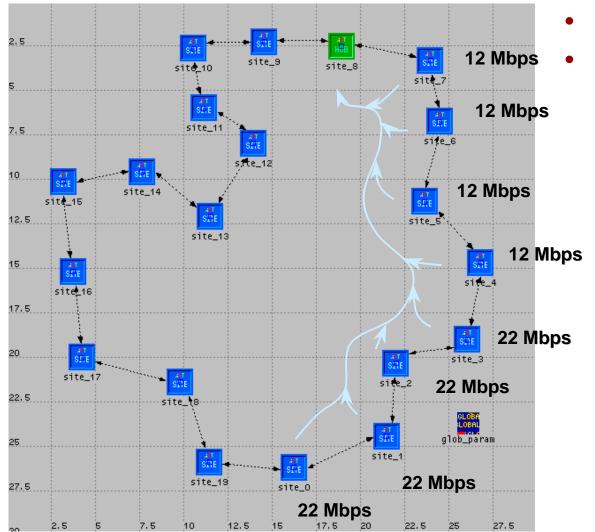
#### **Queue Occupancy Node 7**

• 3 Mb

#### <u>Throughput</u>

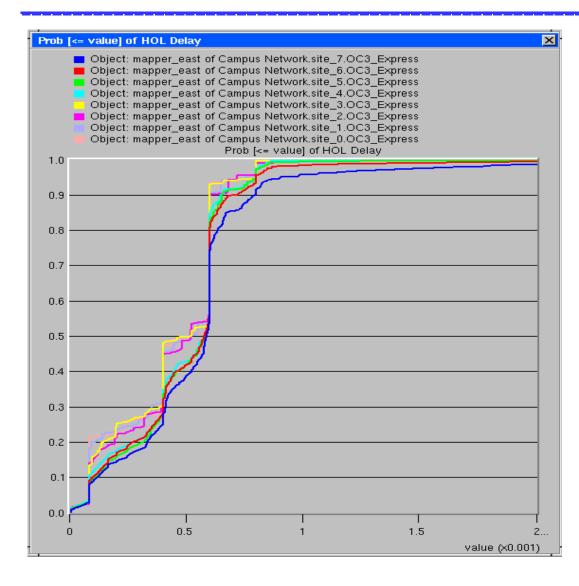
• 150Mb/s = OC-3

## OC-3 90% Utilization Threshold



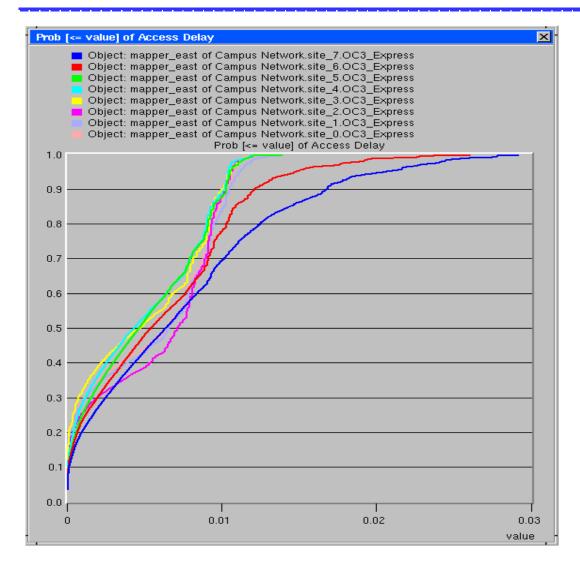
- Target Utilization 90%
- 8 nodes

## OC-3 Head of Line Delay 90% Utilization Threshold



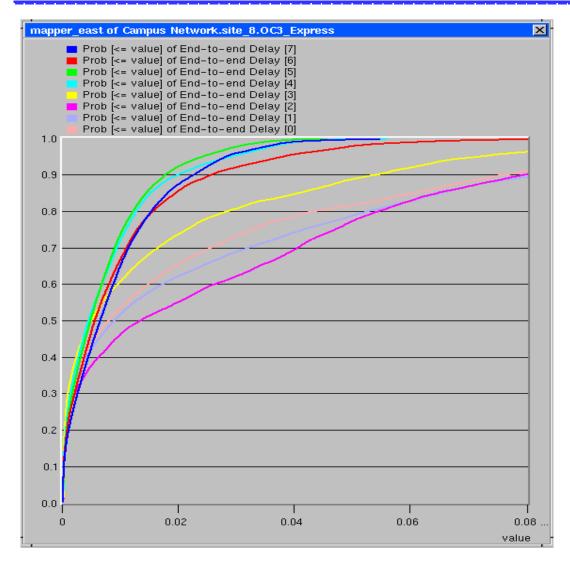
• Fair and equal performance

## OC-3 Access Delay 90% Utilization Threshold



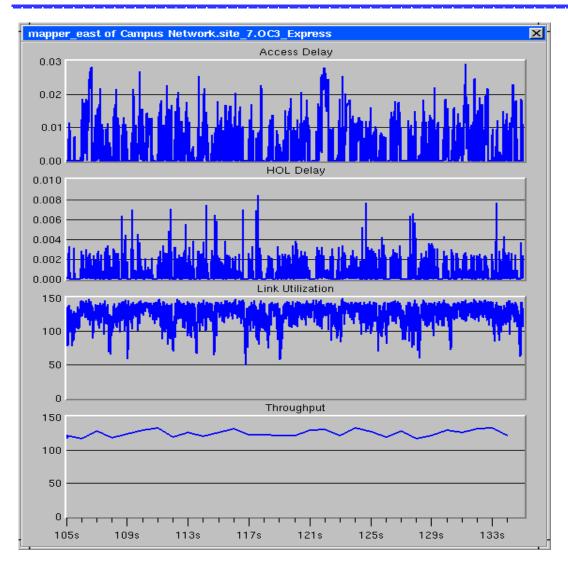
• Fair and equal performance

## OC-3 End to End Delay 90% Utilization Threshold



CDF plot for End to End Delay Node 0 to 3 suffers BIR effect

## **OC-3 Node 7 Internal Performance Characteristics 90%** Utilization Threshold



1<sup>st</sup> 5 seconds of simulation

#### **Access Delay**

 Accumulative effect on access delay for burst

#### HOL Delay

Second

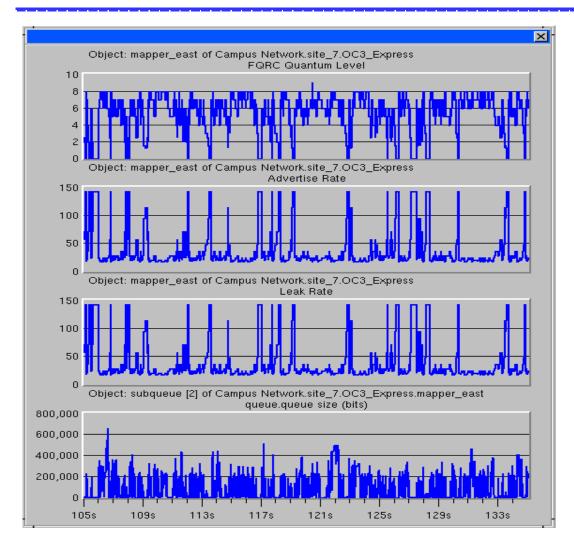
#### Link Utilization

- 150Mb/s = OC-3
- High utilization

#### <u>Throughput</u>

- 150Mb/s = OC-3
- Estimated

# OC-3 Node 7 Internal Performance Characteristics 90% Utilization Threshold (Cont'd)



1<sup>st</sup> 5 seconds of simulation

#### Fairness State

 Number of nodes in congestion span

#### **Advertised Rate**

• 150 Mb/s

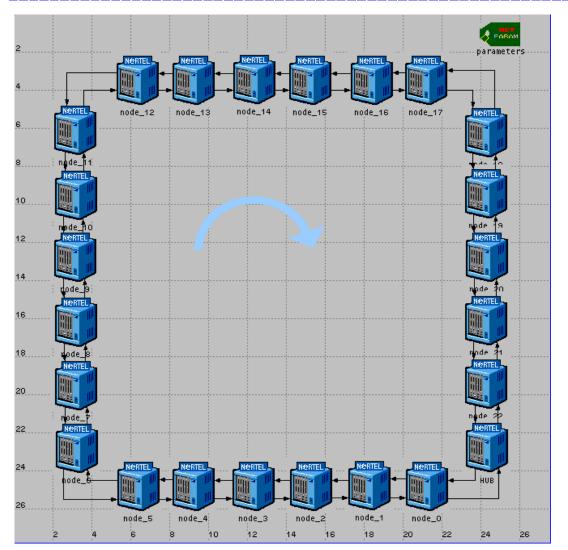
#### Leak/ Add Rate

• 150Mb/s = OC-3

#### **iPT Node Queue Occupancy**

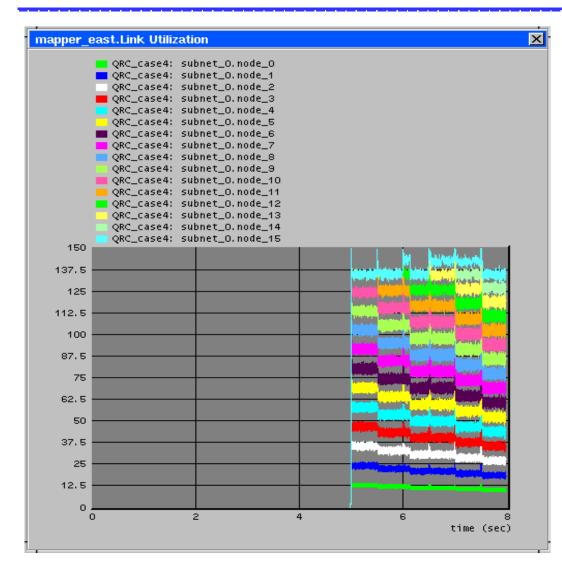
- 0.6 Mb
- Bursty input

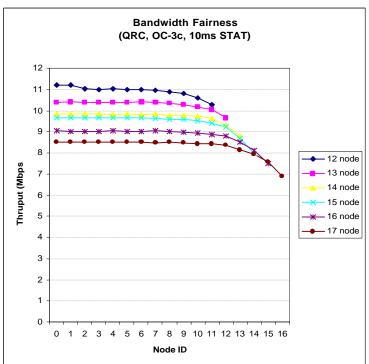
## **Stability Evaluation**



- A simple UDP model was developed and used for a higher simulation efficiency. (models a completely congested network).
- Simulates a single routing domain.
- 24 node OC3/12c hub ring
- Node\_0 starts at 5s. Each node (from node\_1 to node\_22) starts at every half second.
- Packet size distribution used for the UPD generator
  - 50% 64 bytes, 20% 536
    bytes, 30% 1500 byte
- Message Timer Interval = 100 μs

## **Stable for Large number of nodes and scalable**





- OC-3c link utilization
- Bursty leaky bucket
- Explicit rate control scales better (12 nodes) than the Adoptive rate.

## Summary

## • Performance effected by

- 1. Buffer Insertion Ring Effect.
  - Head node slightly worse performance
- 2. Bursty traffic pattern.
  - Heavy Tail Effect.
- Trade-off between stability, responsiveness, delay
  - HOL timer
  - Rate estimator
- Scalable with WAN BW, using explicit rate advertising
  - Better performance with fewer number of nodes
- Achieve Fairness on real network topology.

## What's Next

## Higher BW simulation

- OC-48
- OC-192

## QoS support

— 4 ingress priorities

— iPT CoS Fairness messages

## Meshed traffic flow

## References

- 1. Harry Peng, "iPT Fairness Design Specification", v0.1, July 1999.
- 2. Yang Lee, "Evaluation of Fairness Algorithms", v1.1, June 1999.
- 3. M. Mathis, Sally Floyd, "TCP Selective Acknowledgement Options", RFC 2018, October 1996.
- 4. B. Raahemi, L. Khan, P. Cottreau, "A Novel Algorithm For Local Fairness On Dual Ring LAN/WANs Using Adaptive Rate Control", Version 3.0, December 1998.
- 5. I. Cidon, Y. Ofek, "MetaRing A full duplex ring with fairness and spatial reuse", IEEE Transactions on Communications, Vol.41, No.1, January 1993, pp.110-120.
- 6. T. Saito, H. Aida, T. Aoki, Y. Kishi, and P. Setthawong, "QOS Guarantees for high-speed variablelength packet LANs", www.sail.t.u-tokyo.ac.jp/pisai/research
- 7. J.S.C.Chen, I. Cidon, and Y. Ofek, "A local fairness algorithm for Gigabit LANs/MANs with spatial reuse", *IEEE Journal on Selected Areas in Communications*, Vol.11, No.8, October 1993, pp. 1183-1192.
- 8. "Dynamic Packet Transport Technology and Applications Overview", White Paper, February 1999.
- 9. "Dynamic Packet Transport (DPT) Technology and Products, February 1999.
- 10. "On Estimating End-to-end Network Path Properties", Mark Allman and Vern Paxson, ACM;SIGCOMM 1999.