100Gb/s per Lane for Electrical Interfaces and Electrical PHYs Study Group: Status and Work

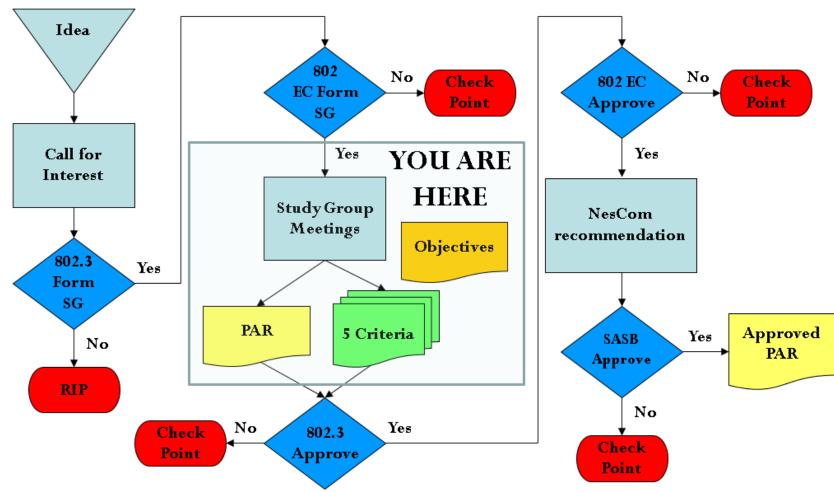
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Website: http://www.ieee802.org/3/100GEL/index.html

OUTLINE

- Study group goals
- Proposed text
- Call to action
- Straw Polls

Overview of IEEE 802.3 Standards Process (1/5)-Study Group Phase



TIMELINE

Option I

- Understand boundaries of Objectives to write CSD and PAR.
- May need further study and modification to objectives in Task Force,
- Allows us to move into baseline proposals when ready

PAR and CSD in January; objectives in March

May Interim Task Force

No Answer

Option 2

11/47 (23%)

- Do deeper study prior to Objectives/CSD/PAR
- Have more firm Objectives and head straight into baseline proposals once a Task Force.
- May need to wait a meeting cycle or two after consensus is formed due to scheduling/process

PAR, CSD, and Objectives in March/May

Nov Plenary Task Force

1.I would support A.Targeting TF in May B.Targeting TF in November C.Targeting TF later than November D.Need more information Straw Poll from Dec 20 16/47 (34%) 16/47 (23%) 11/47 (23%) 8/47 (17%)

FOUNDATIONAL OBJECTIVES

- Support a MAC data rates of 100, 200, and 400 Gb/s
- Support full-duplex operation only
- Preserve the Ethernet frame format utilizing the Ethernet MAC
- Preserve minimum and maximum Frame Size of current IEEE 802.3 standard
- Support a BER of better than or equal to 10-12 at the MAC/PLS service interface (or the frame loss ratio equivalent) for single-lane 100Gb/s operation
- Support a BER of better than or equal to 10-13 at the MAC/PLS service interface (or the frame loss ratio equivalent) for two-lane 200Gb/s or four-lane 400Gb/s operation
- Support optional Energy-Efficient Ethernet operation

PROPOSED FORM FOR 100G OBJECTIVES

- Define a single-lane 100 Gb/s Attachment User interface (AUI) for C2M electrical operation with a total channel insertion loss of <= "x" dB at 28GHz.</p>
- Define a single-lane 100 Gb/s Attachment User interface (AUI) for C2C electrical operation with a total channel insertion loss of <= "y" dB at 28GHz.
- Define a single-lane 100Gb/s PHY for operation over electrical backplanes with a total insertion loss of \leq "z" dB at 28GHz.
- Define a single-lane 100Gb/s PHY for operation over twin-axial copper cable with lengths up to at least "w"m.

ASSUMPTIONS FOR CREATING 200G & 400G OBJECTIVE LANGUAGE

- Modify the above objectives with appropriate language for "two-lane" or "four-lane" interfaces.
- Any interface we define for single lane would have similar BMP for multiple lanes.
- Desire to keep same targets for loss/reach for single-lane, two-lane, four-lane

CSD TEXT – CALL TO ACTION

- BROAD MARKET POTENTIAL
- TECHNICAL FEASIBILITY
- ECONOMIC FEASIBILITY
- MANAGED OBJECTS
- CO-EXISTENCE
- COMPATIBILITY
- DISTINCT IDENTITY

CALL TO ACTION

- Define a single-lane 100 Gb/s Attachment User interface (AUI) for C2M
 electrical operation with a total channel insertion loss of <= "x" dB at 28GHz.
 - Seen range from x=10.5dB to 20dB →
 Does removing loss target harm CSD responses?
 - Is there consensus on re-use of FEC and PCS?
 - Need evidence of economic feasibility
 - Is this 1.5m or 2m for an appropriate budget?
 - Need evidence of broad market potential
- Define a single-lane 100Gb/s PHY for operation over twin-axial copper cable with lengths up to at least 2m.

CALL TO ACTION

- Seen "y" = everything from 18dB to 30dB → need to understand the appropriate applications
- Need evidence of distinct identity
- Define a single-lane 100 Gb/s Attachment User interface (AUI) for C2C electrical operation with a total channel insertion loss of <= "y" dB at 28GHz.
 - Define a single-lane 100Gb/s PHY for operation over electrical backplanes with a total insertion loss of ≤30 dB at 28GHz assuming low loss packages.
- Elephant in the room has been the package
- Define a single-lane 100Gb/s
 Do we have consensus if we state the assumed package for a given loss?
 - Need further evidence of technical feasibility?

STRAW POLLS

- I support...
 - PAM4 modulation for all interfaces
 - Different PAM modulation schemes targeted for each interface-type
 - Further study of all possible
 - No opinion

- For a Chip-to-Module objective, I support the following form:
 - Define a single-lane 100 Gb/s Attachment User interface (AUI) for C2M electrical operation with a total channel insertion loss of <= "x" dB at 28GHz.
 - Define a single-lane 100 Gb/s Attachment User interface (AUI) for C2M electrical operation assuming re-use of FEC and PCS from clause "abc".
 - Another form
 - No Opinion
- I would support an objective for operation over... (select all that apply)
 - Electrical backplanes.
 - Electrical backplanes with a total insertion loss of ≤25 dB at 28GHz.
 - Electrical backplanes with a total insertion loss of ≤30 dB at 28GHz.
 - Electrical backplanes with a total insertion loss of ≤30 dB at 28GHz with low loss packages.
 - Electrical backplanes with total bump-to-bump insertion loss of ≤34dB at 28GHz.
 - Electrical backplanes with total bump-to-bump insertion loss of ≤36dB at 28GHz.
 - None of the above, at this time.

NEXT STEPS

- Prepare content and contributions to substantiate:
 - Technical feasibility
 - Economic Feasibility
 - Broad Market Potential
 - Distinct Identity
 - Compatibility
- January Ad Hocs 8:00-10am PST January 8th, 15th
- January Plenary January 22-26
 - Thursday: Ipm-5:30pm & Friday: 8am-6pm
 - Presentation requests due Next Friday, January 12

Propose to cancel due to

OIF meeting & US holiday

Will be VERY busy;

please socialize before the meeting

THANK YOU!

BACKUP SLIDES: PREVIOUS PROJECT OBJECTIVES