



# Transmission Strategies for 10GBase-T over CAT- 6 Copper Wiring

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# Outline

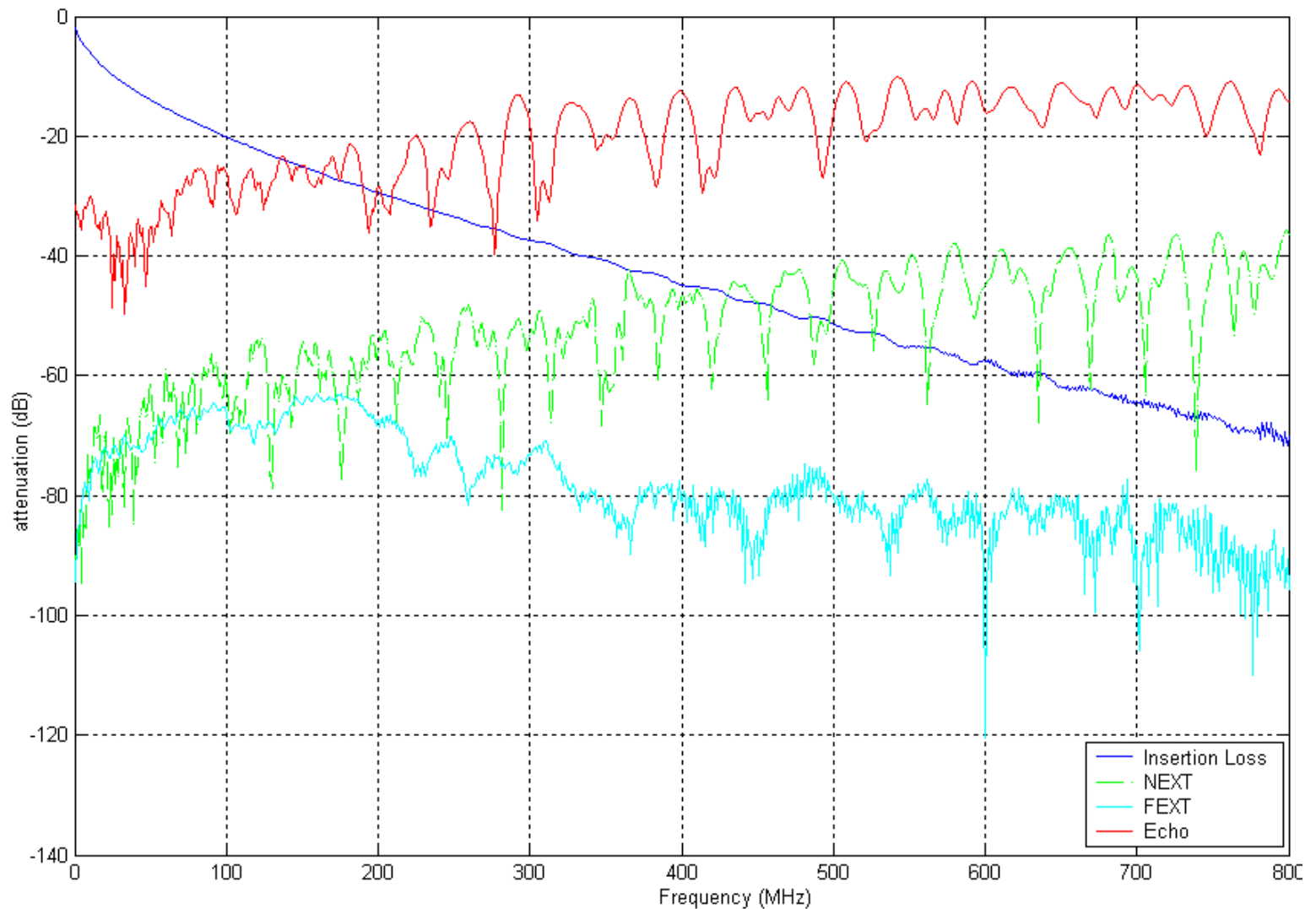
- Transmission Model
- Capacity
- Interference Cancellation
- Combined Channel Equalization and Coding:  
Iterative (Turbo) Structures
- Future work

# *Signal and Systems Impairments*

<u>Signal Impairment</u>	<u>Mitigation Technique</u>
Dispersion	Channel Equalization (using DFE)
Near-End Cross-Talk (NEXT)	NEXT Cancellation
Far-End Cross-Talk (FEXT)	FEXT Cancellation
Return Loss (ECHO)	ECHO Cancellation
Alien-NEXT	A-NEXT Compensation
Residual Noise/Insertion Loss	Channel Coding

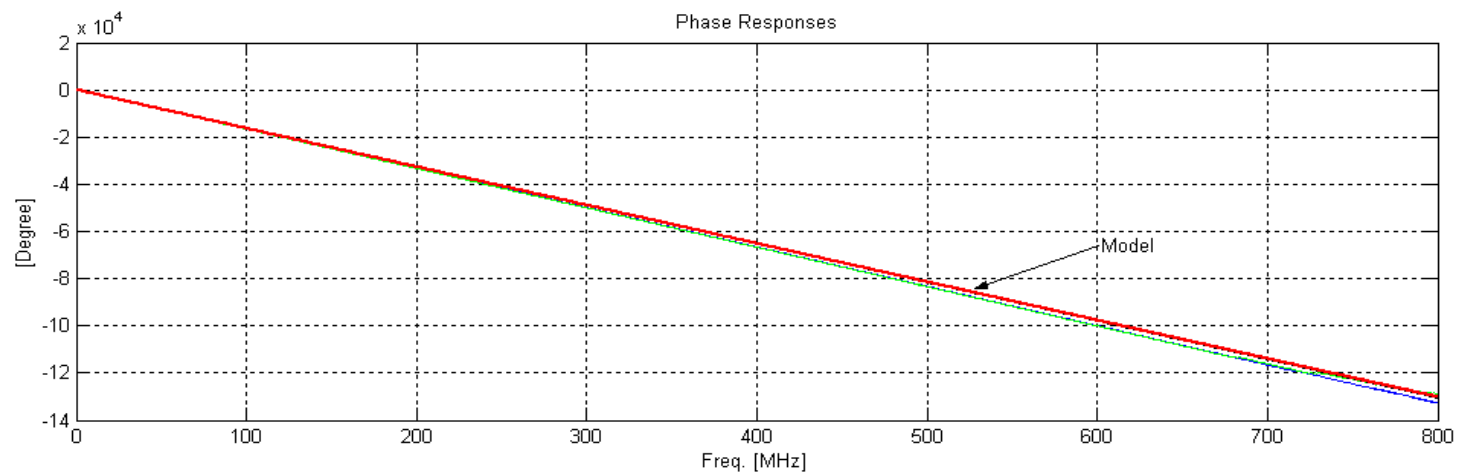
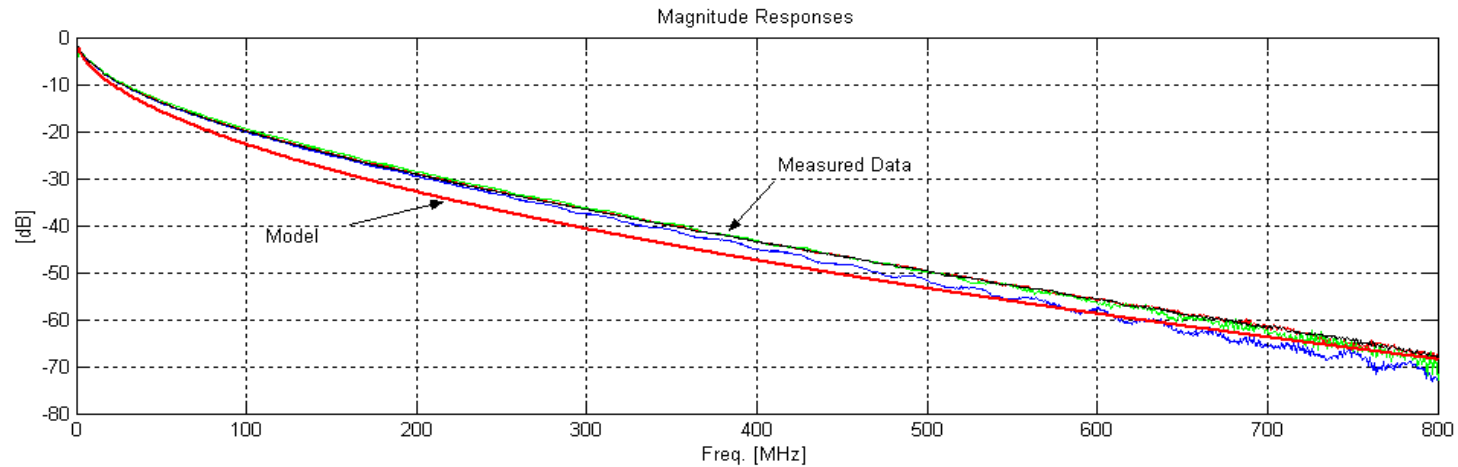
## *Channel Characteristics for CAT-6*

### *Measured data provided by Nexans*

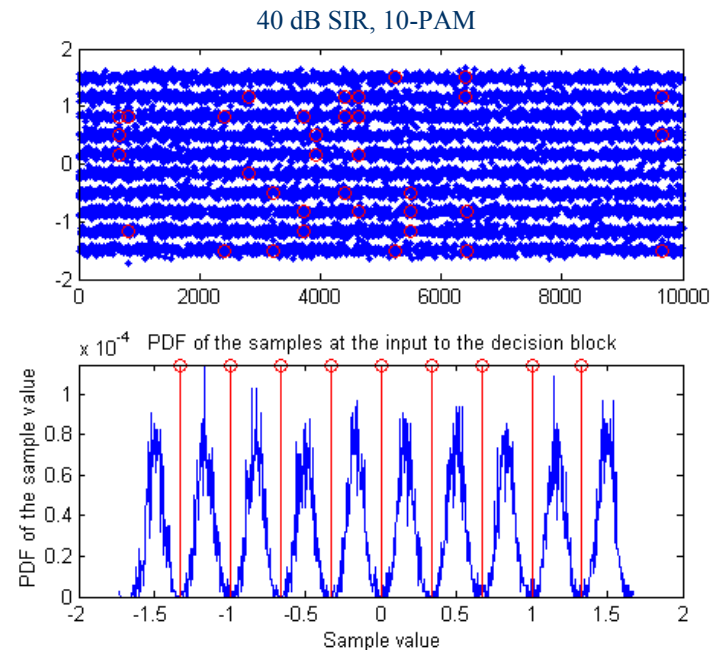
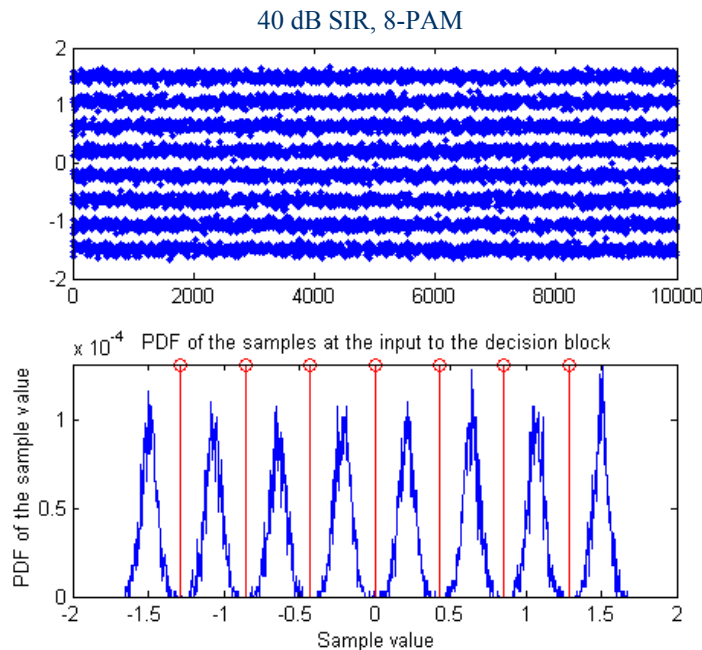


# *CAT- 6 Insertion Loss Responses*

## *Measured data provided by Nexans*



# MIMO DFE Output

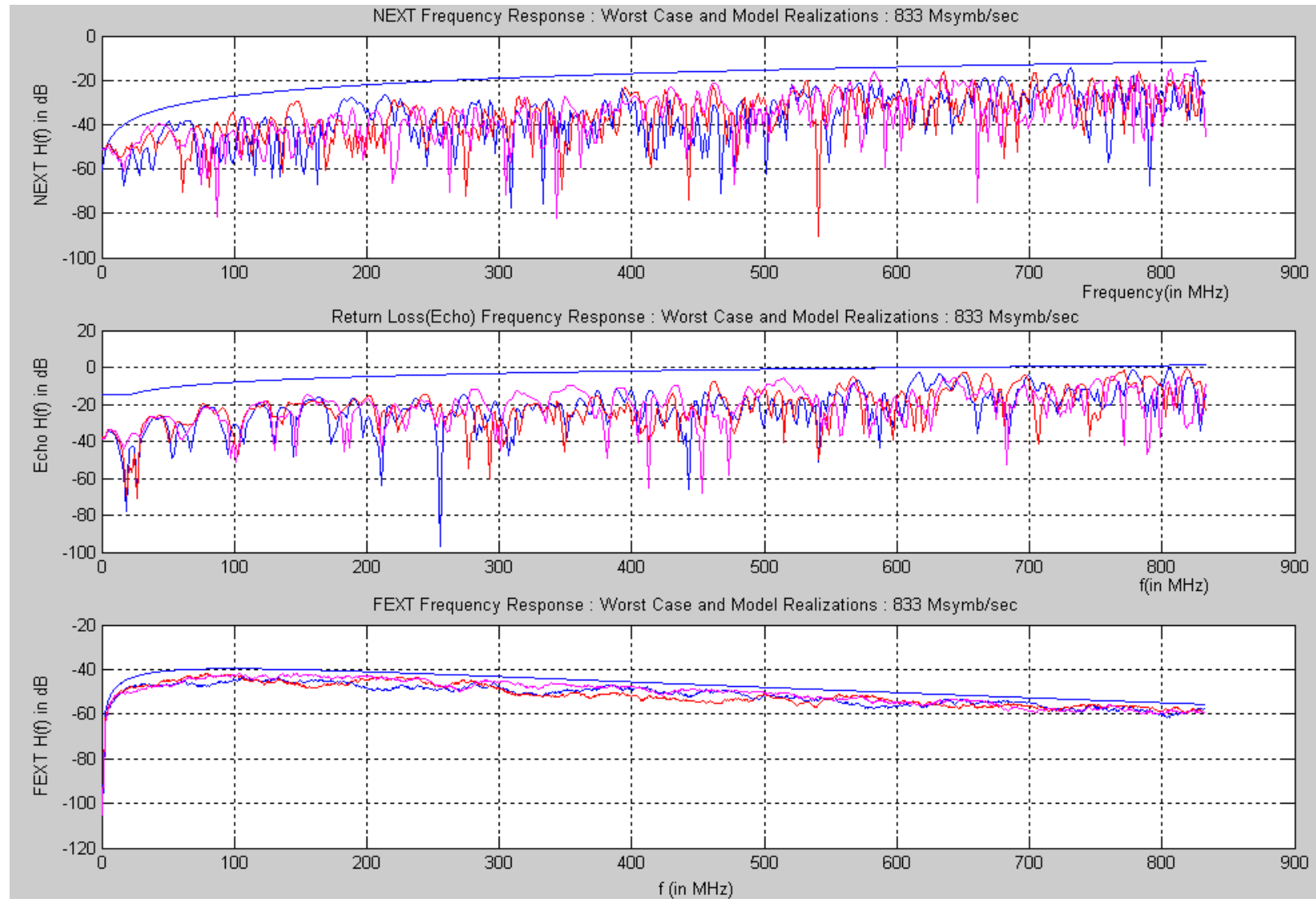


- 10 Gbps Transmission Requires 3 bits/sym at 833 M-Symbols/sec
- 10-PAM ( 3 bits/sym + parity ) Signaling with TCM
- Eye-Closure due to the increased No. of PAM levels  $\Rightarrow$  High coding gain needed

# DFE “open eye” performance

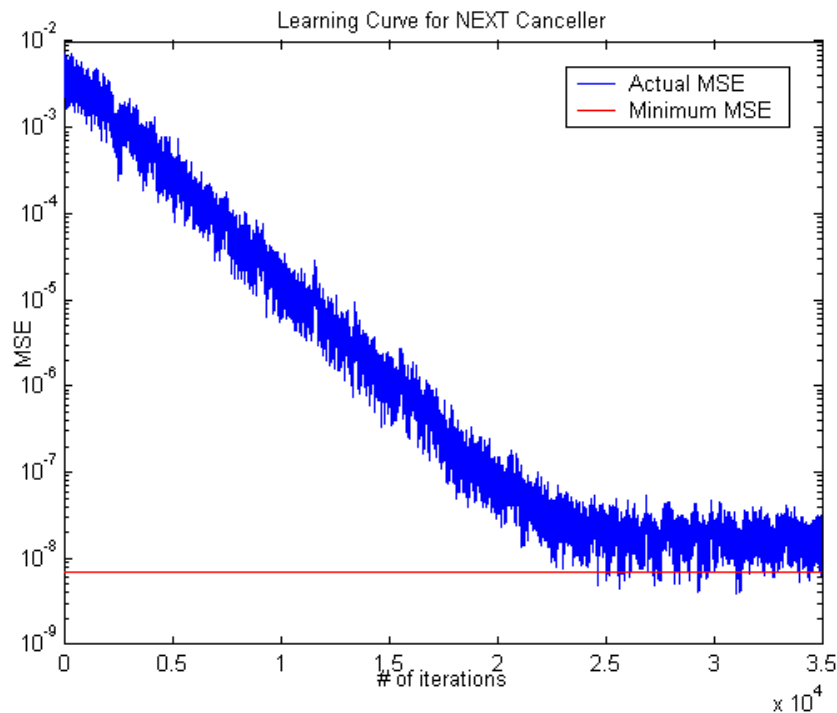
CAT- 6 : 100m , Measured Data , **No Error Correction Coding**

	40 dB SIR	Remarks
625 M Symbols/sec	16-PAM $625 \text{ Msym/sec} * 4 \text{ bit/sym} = 2.5 \text{ Gbps}$ $2.5 \text{ Gbps} * 4 \text{ Lines} = 10 \text{ Gbps}$	SER: $1.08 \times 10^{-3}$
833 M Symbols/sec	8-PAM $833 \text{ Msym/sec} * 3 \text{ bit/sym} = 2.5 \text{ Gbps}$ $2.5 \text{ Gbps} * 4 \text{ Lines} = 10 \text{ Gbps}$	SER: $7.8 \times 10^{-6}$
833 M Symbols/sec	10-PAM $833 \text{ Msym/sec} * 3 \text{ bit/sym} = 2.5 \text{ Gbps}$ $2.5 \text{ Gbps} * 4 \text{ Lines} = 10 \text{ Gbps}$	SER: $3.9 \times 10^{-4}$

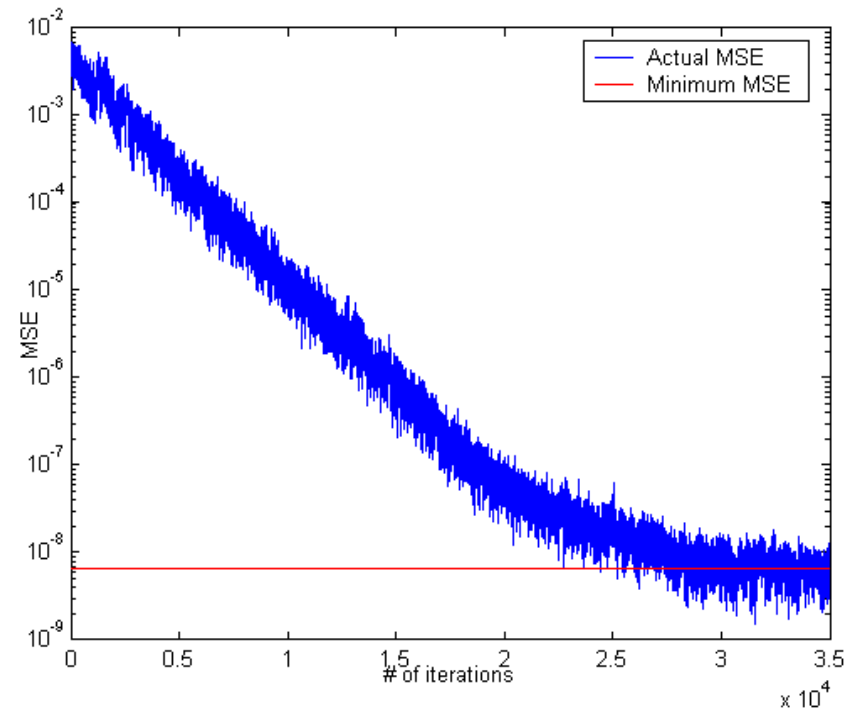




# *Learning Curves for LMS (NEXT) MIMO Cancellor*



(a) : Constant Step Size

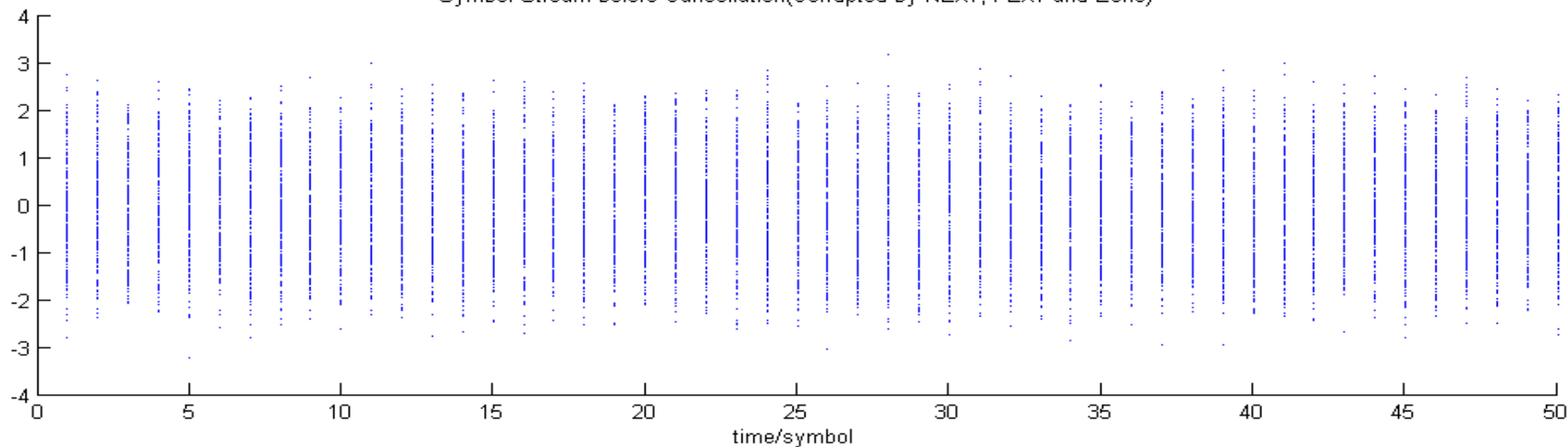


(b) : Varying Step Size

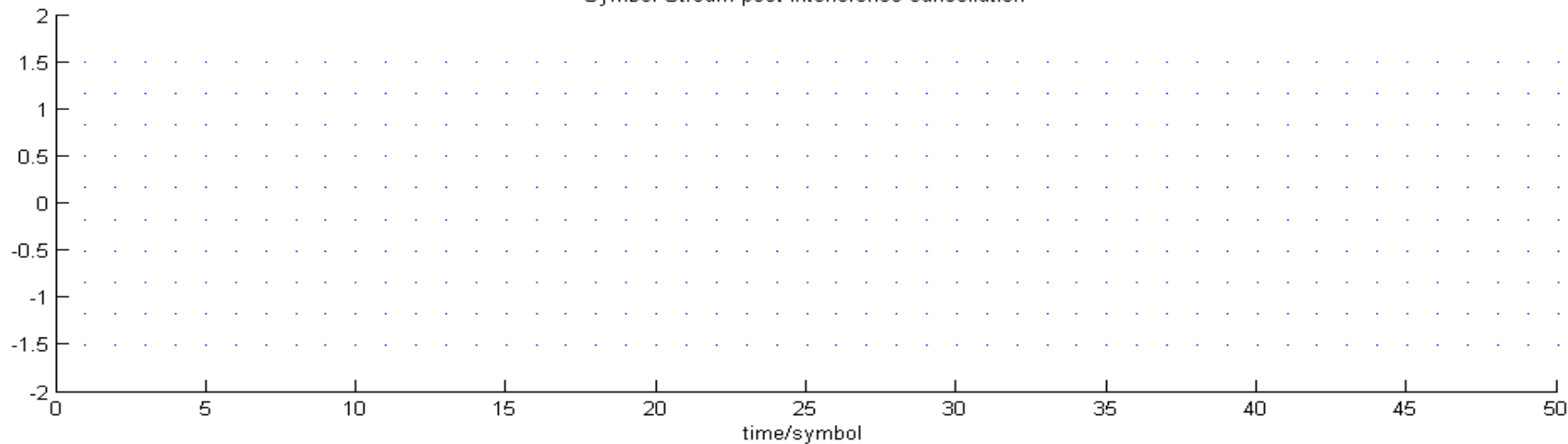


# Results using LMS MIMO Cancellers

Symbol Stream before Cancellation(Corrupted by NEXT, FEXT and Echo)



Symbol Stream post interference cancellation

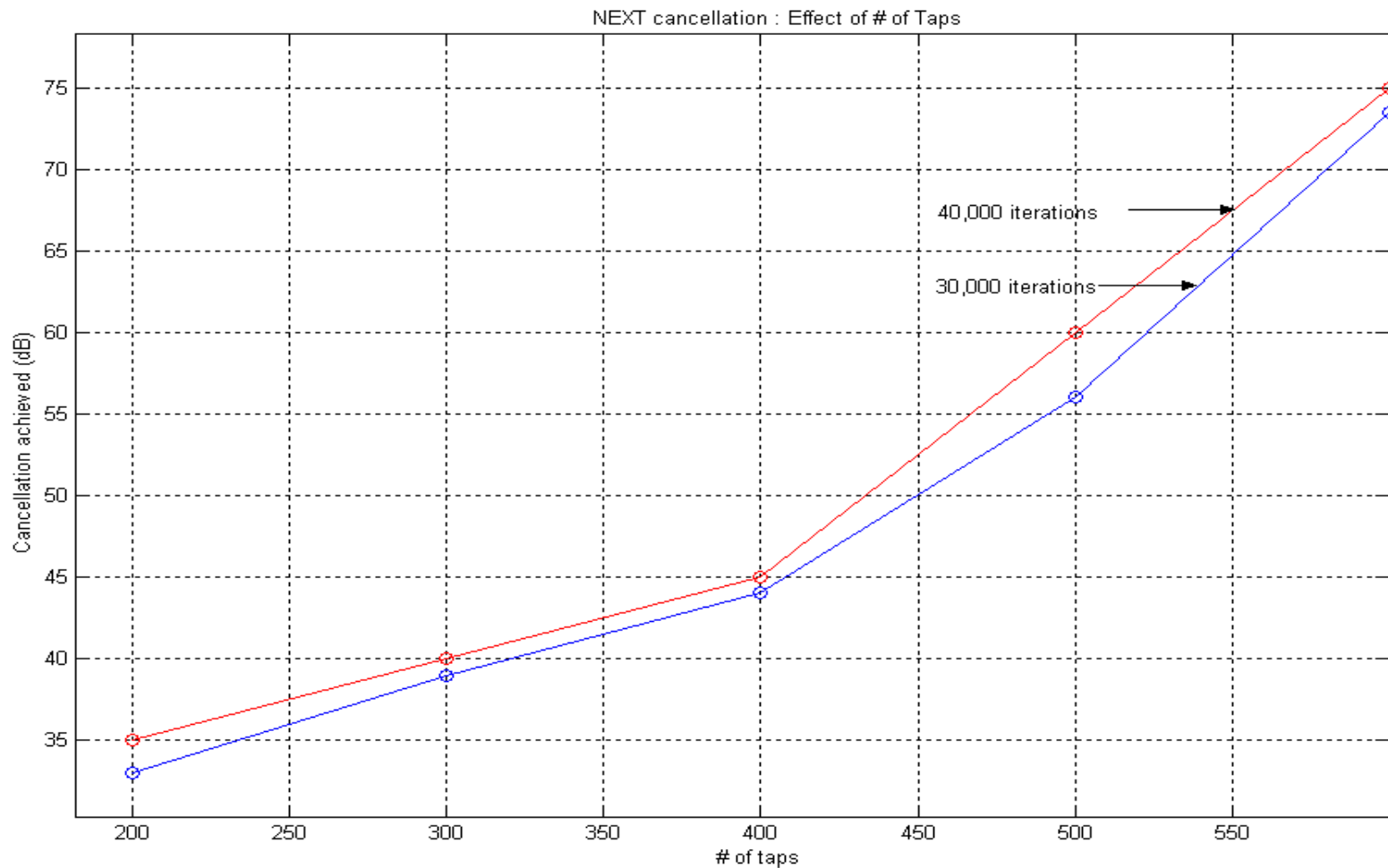




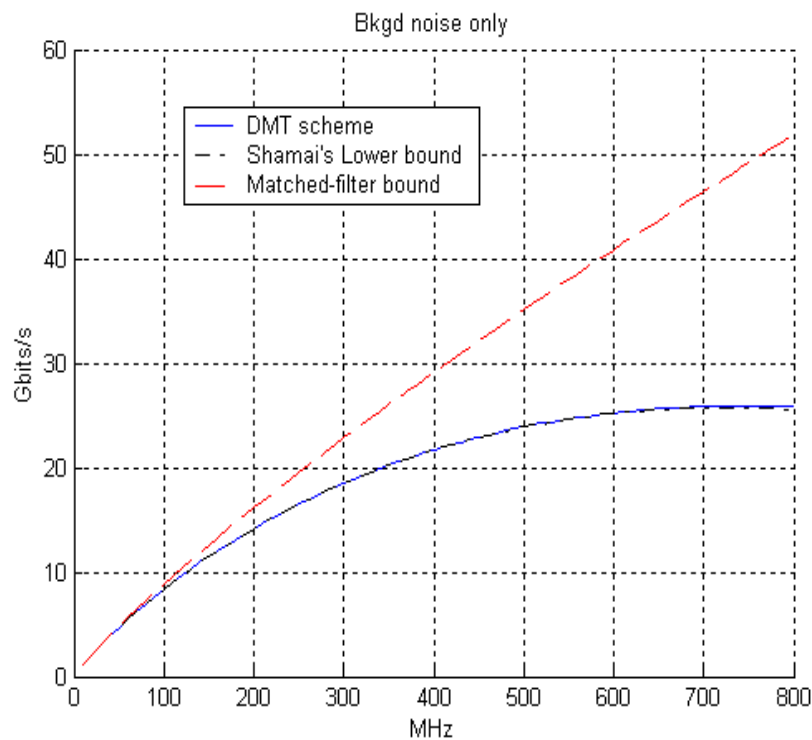
## *Features of LMS MIMO Cancellers*

	<b>NEXT Cancellers</b>	<b>FEXT Cancellers</b>	<b>Echo Cancellers</b>
<b># of taps</b>	<b>530</b>	<b>570</b>	<b>515</b>
<b># of iterations</b>	<b>40000</b>	<b>40000</b>	<b>40000</b>
<b>Algorithm</b>	<b>LMS</b>	<b>LMS</b>	<b>LMS</b>
<b>No. of cancellers required</b>	<b>3</b>	<b>3</b>	<b>1</b>
<b>Cancellation achieved</b>	<b>60dB</b>	<b>34dB</b>	<b>76dB</b>

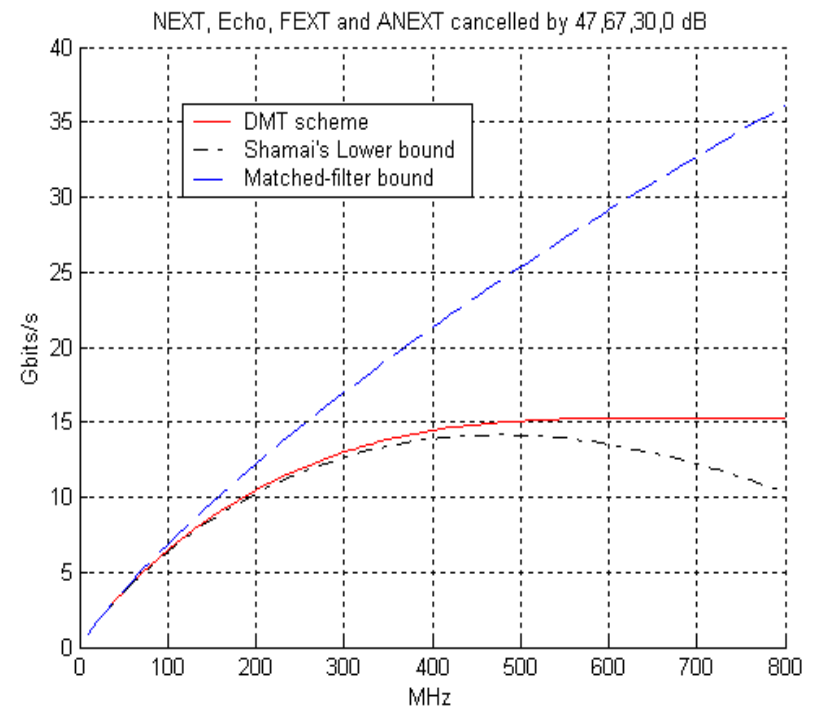
## *Effect of Number of MIMO Cancellers Taps on SIR Performance*



# Theoretical CAT-6 Cables Capacity



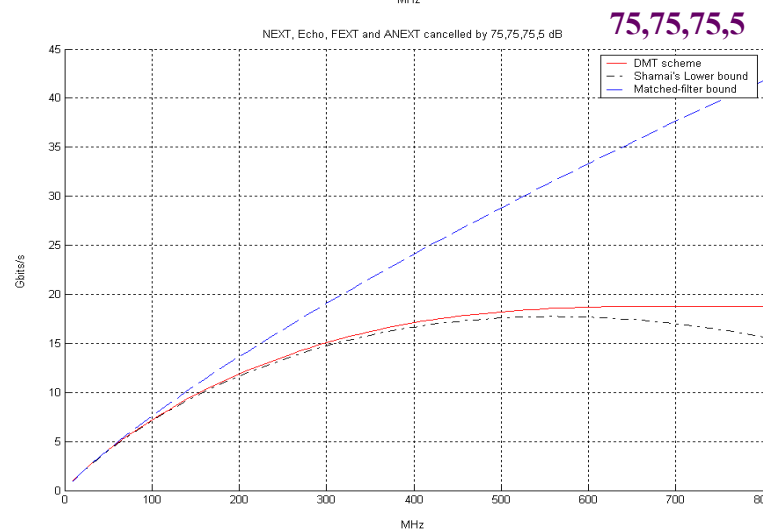
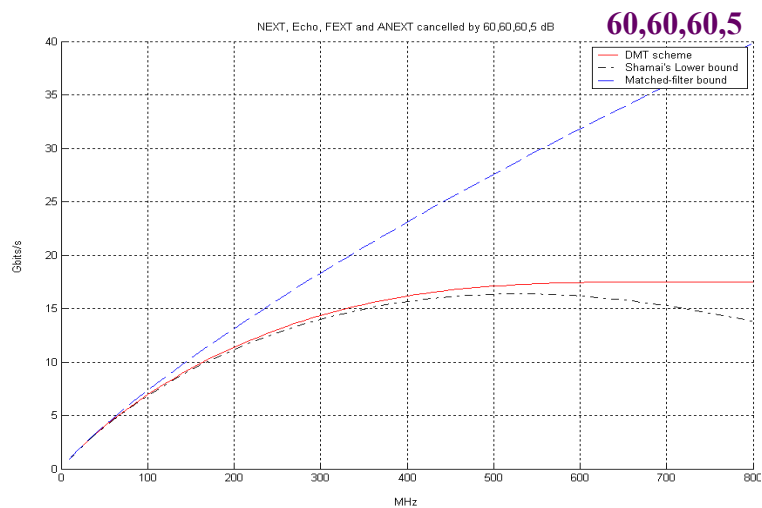
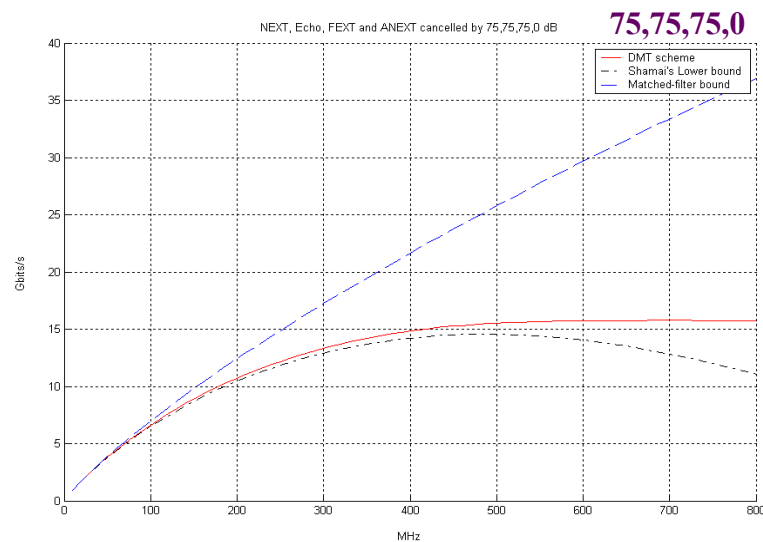
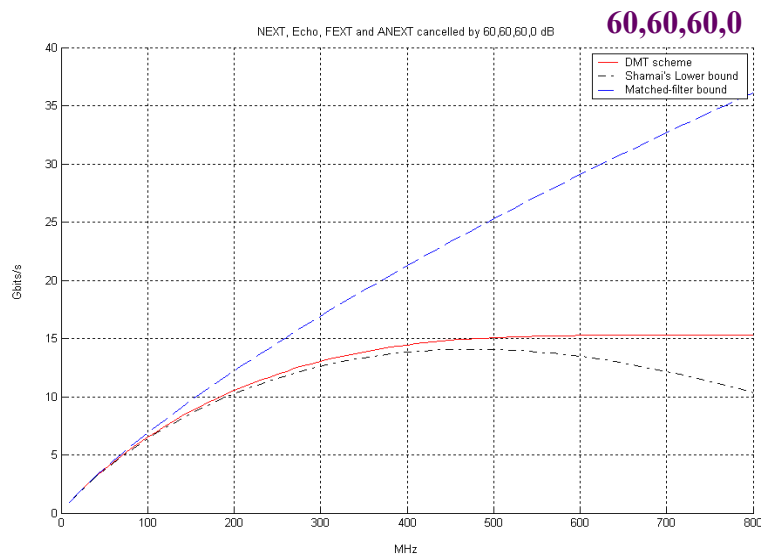
Total capacity with “AWGN only” VS bandwidth of the CAT-6 cable.



Total capacity with “AWGN and residual interference” VS bandwidth of CAT-6 cable.

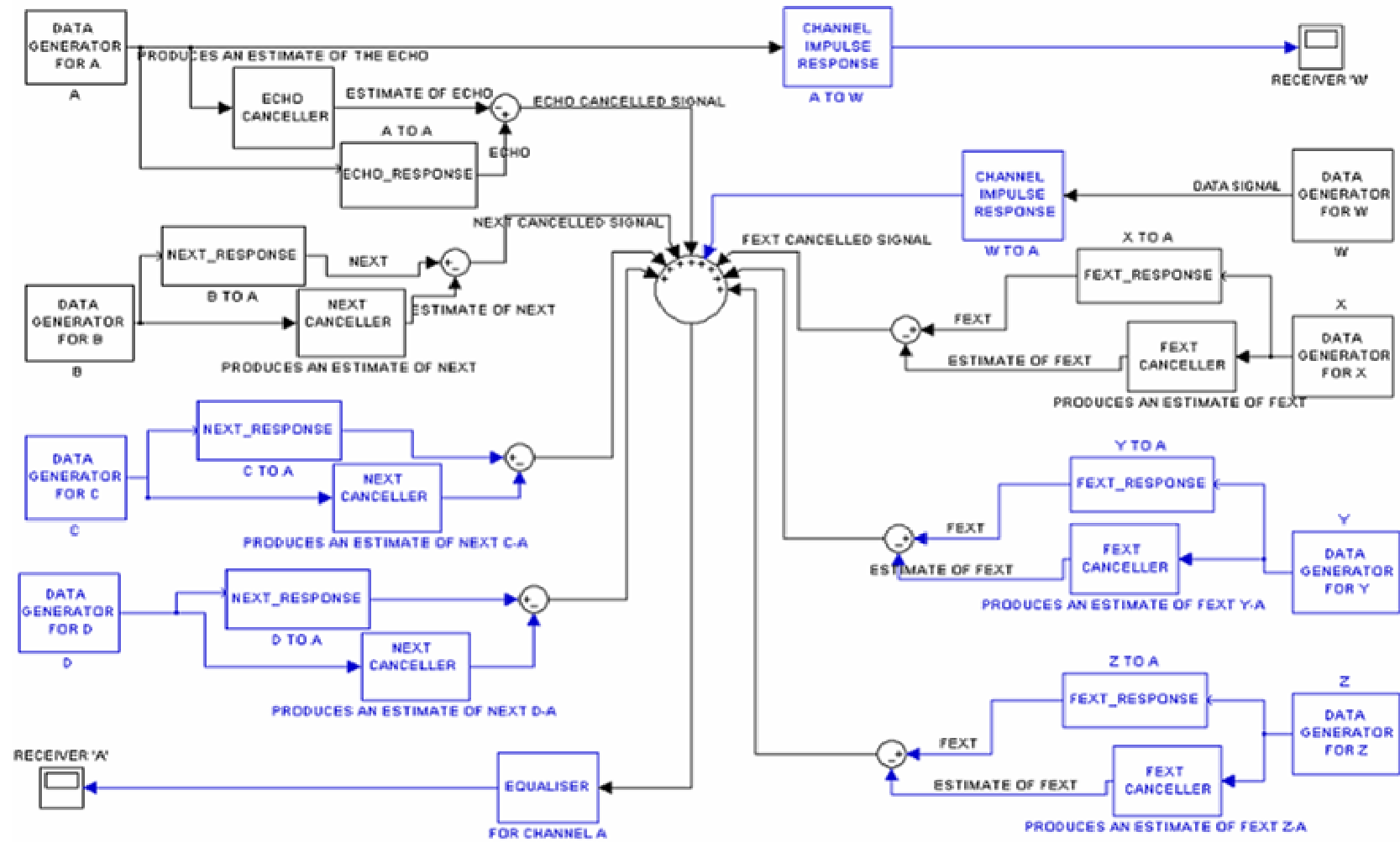


# Theoretical CAT-6 Cables Capacity



# *System Parameters*

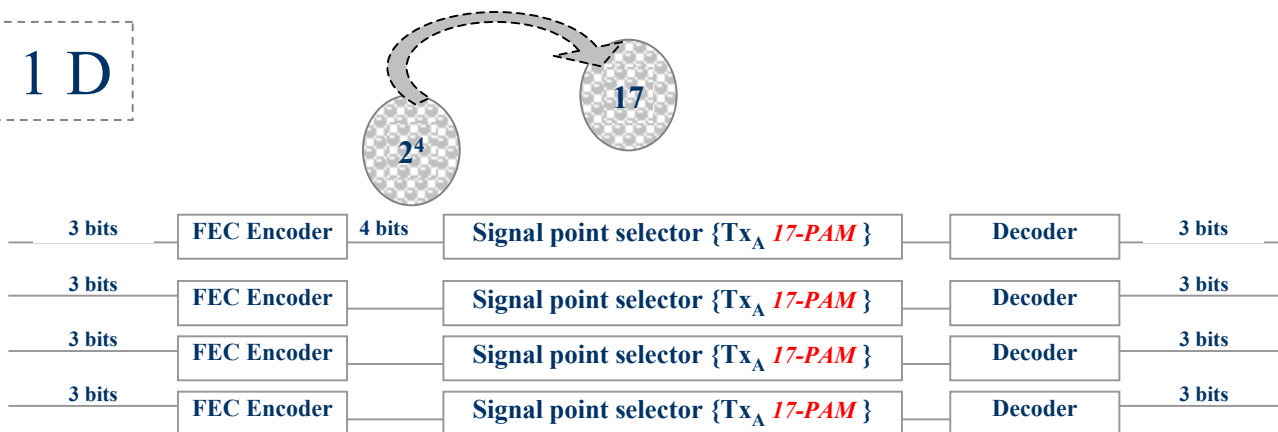
<b>Modulation:</b>	<b>PAM</b>	<b>[M = 10]</b>
<b>Pulse Shape:</b>	<b>Raised cosine</b>	<b>[ <math>\alpha = 8\%</math> ]</b>
<b>MIMO Cancellers:</b>	<b>LMS</b>	<b>[~ 500 taps ]</b>
<b>MIMO Equalizer:</b>	<b>DFE</b>	<b>[120 FF, 100FB]</b>
<b>Baseline FEC:</b>	<b>TCM</b>	<b>[4-D]</b>
<b>Launch Power:</b>		<b>10dBm (3V P-P)</b>
<b>Background Noise Level :</b>		<b>-150dBm/Hz</b>





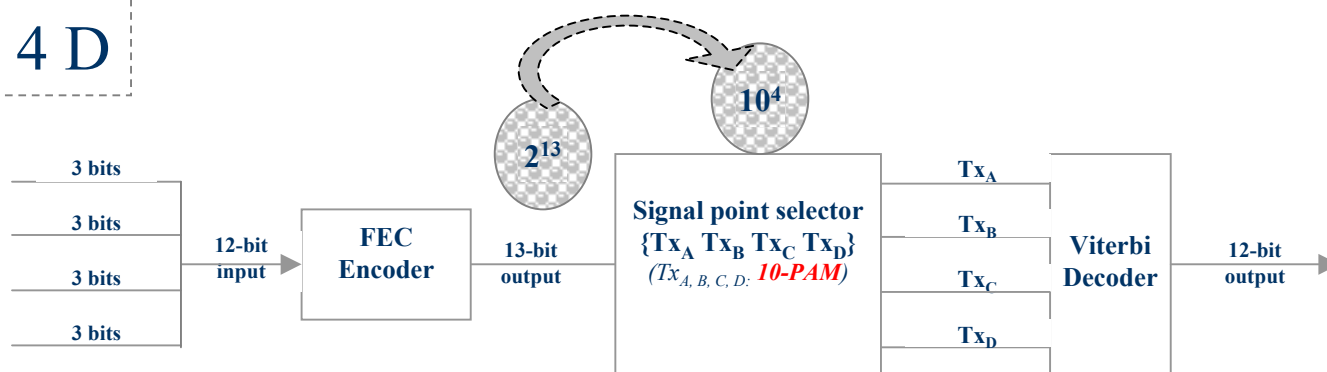
# Why Multi Dimensional

1 D



17 PAM

4 D



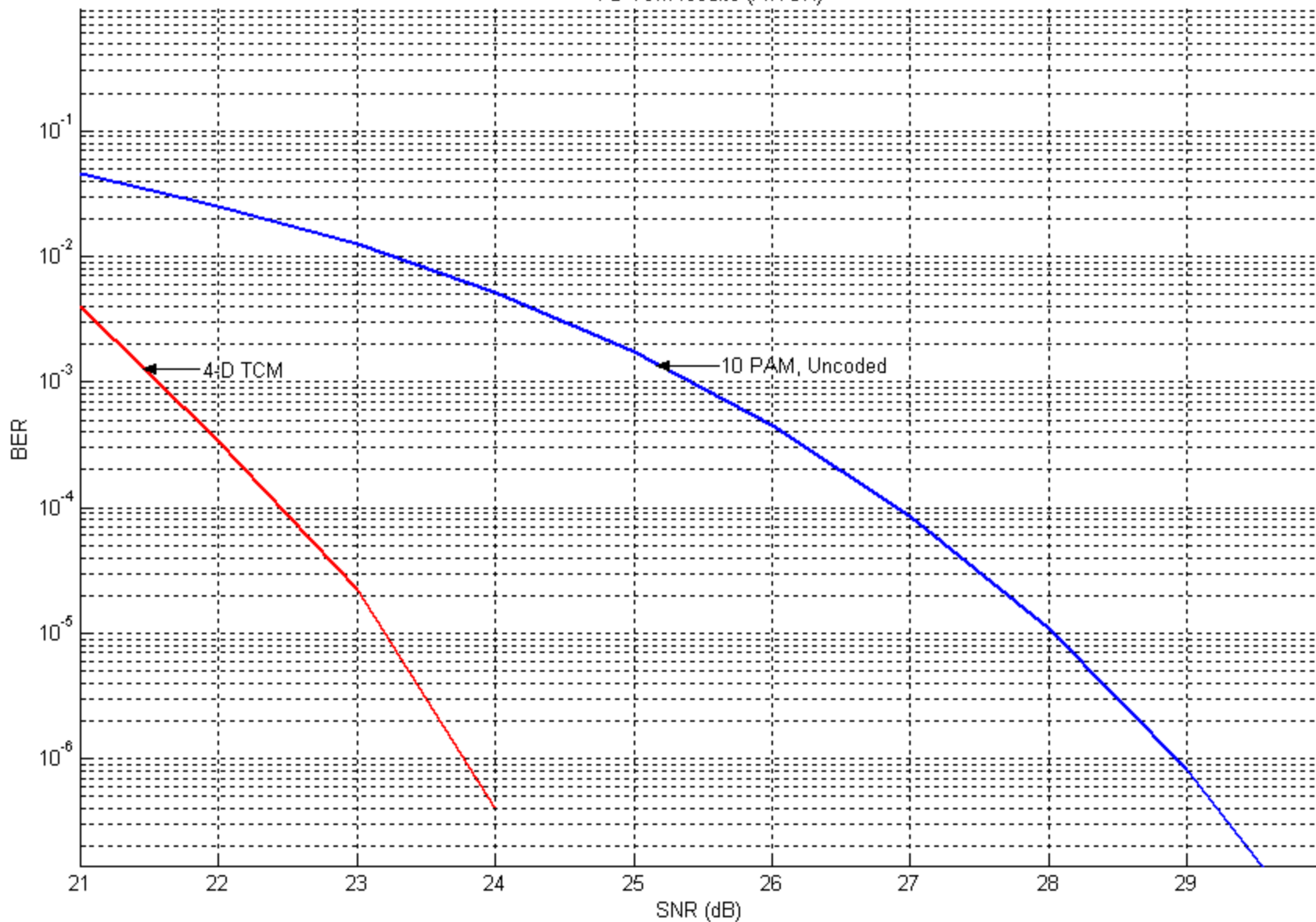
~ 6 dB loss

10 PAM



# 4D TCM (AWGN)

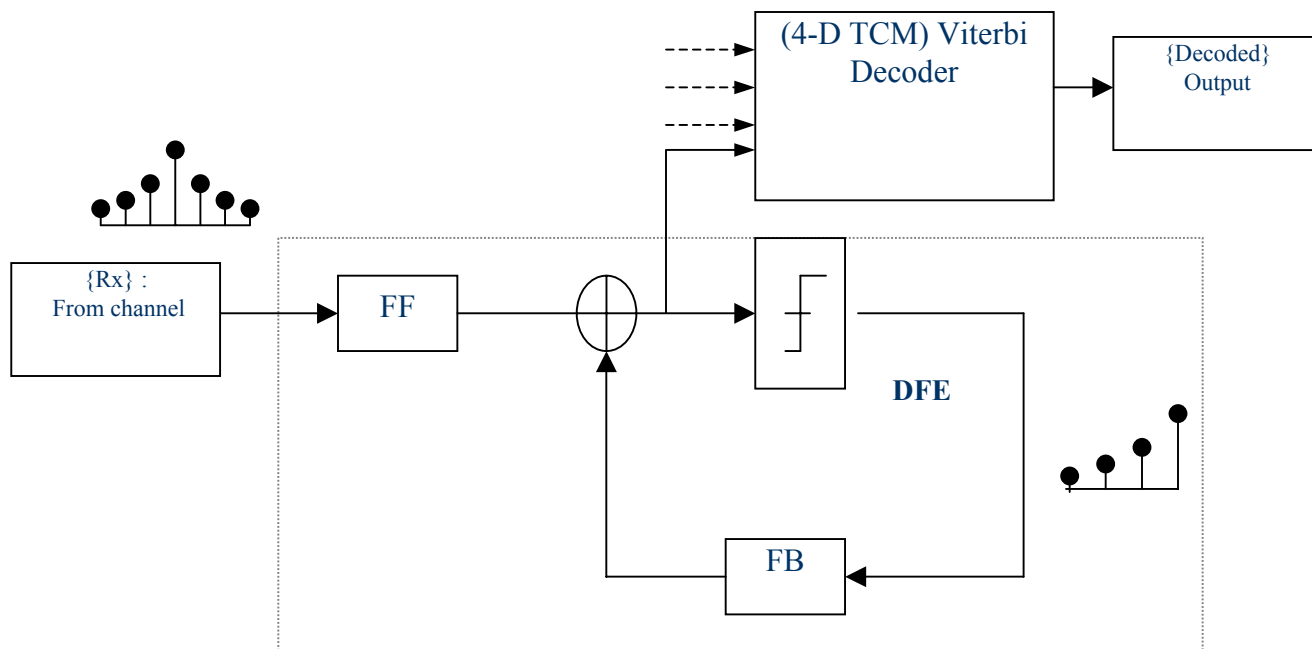
4-D TCM results (AWGN)



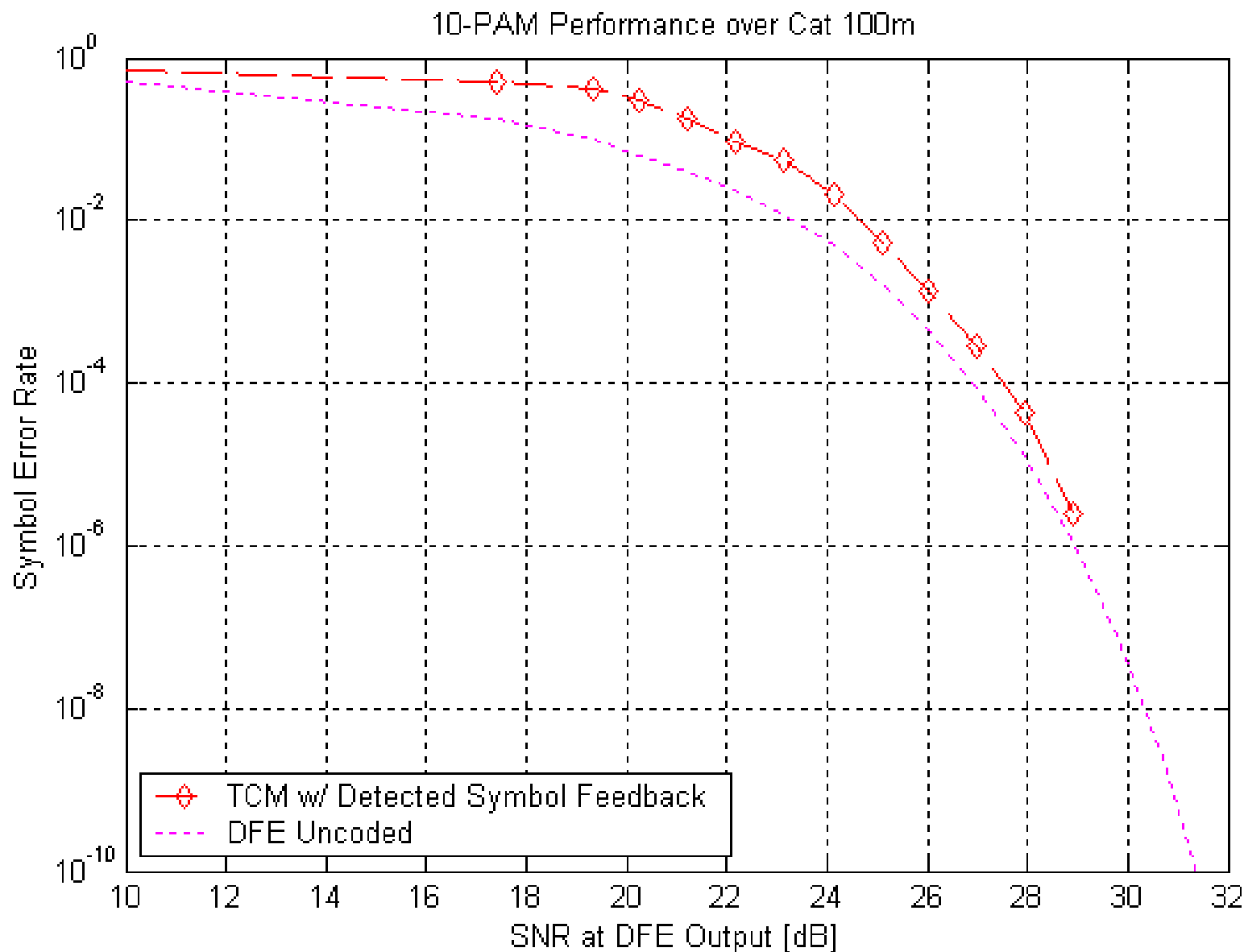


..... the same scheme was  
tested on CAT- 6 (ISI) channel ....

# TCM + DFE : Conventional structure



# TCM + DFE Results (*CAT-6 Channel*)

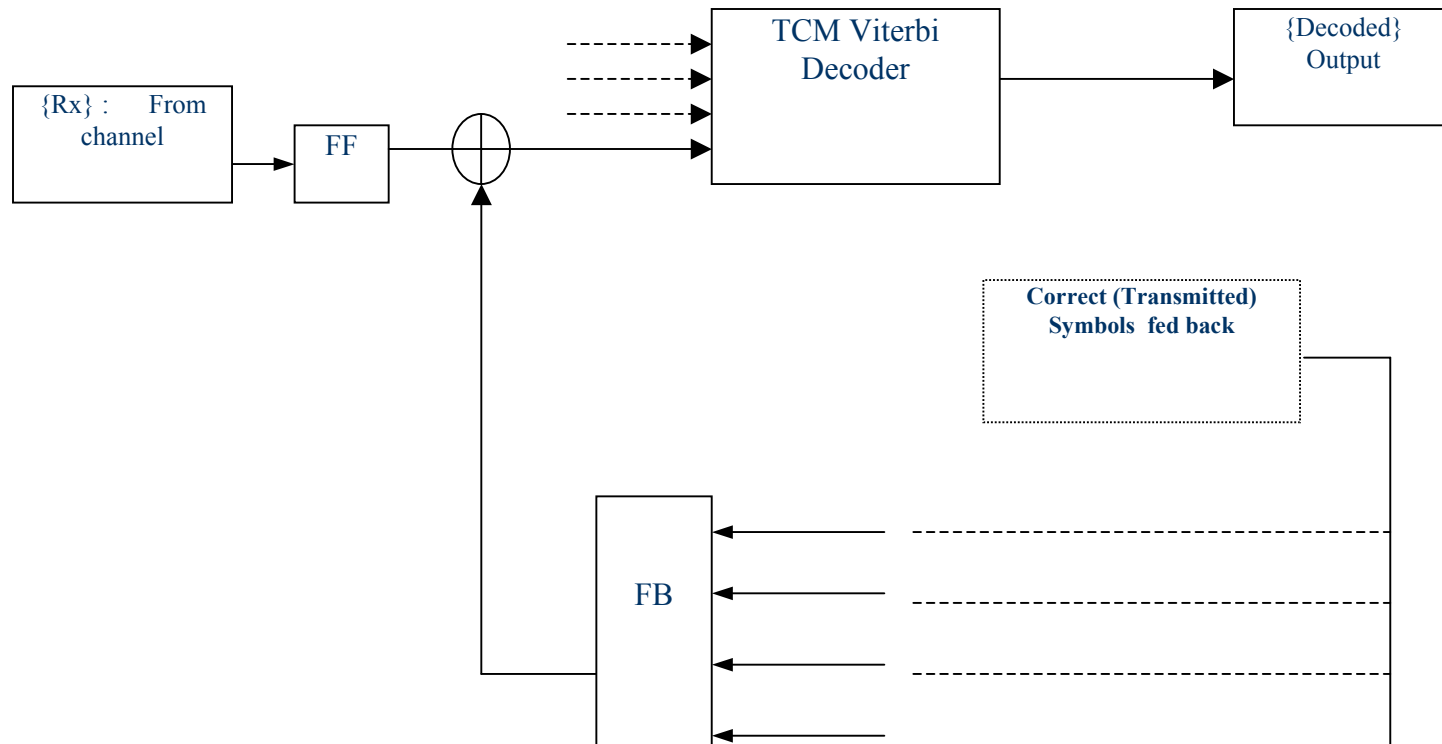


## *TCM + DFE over CAT- 6 Channel*

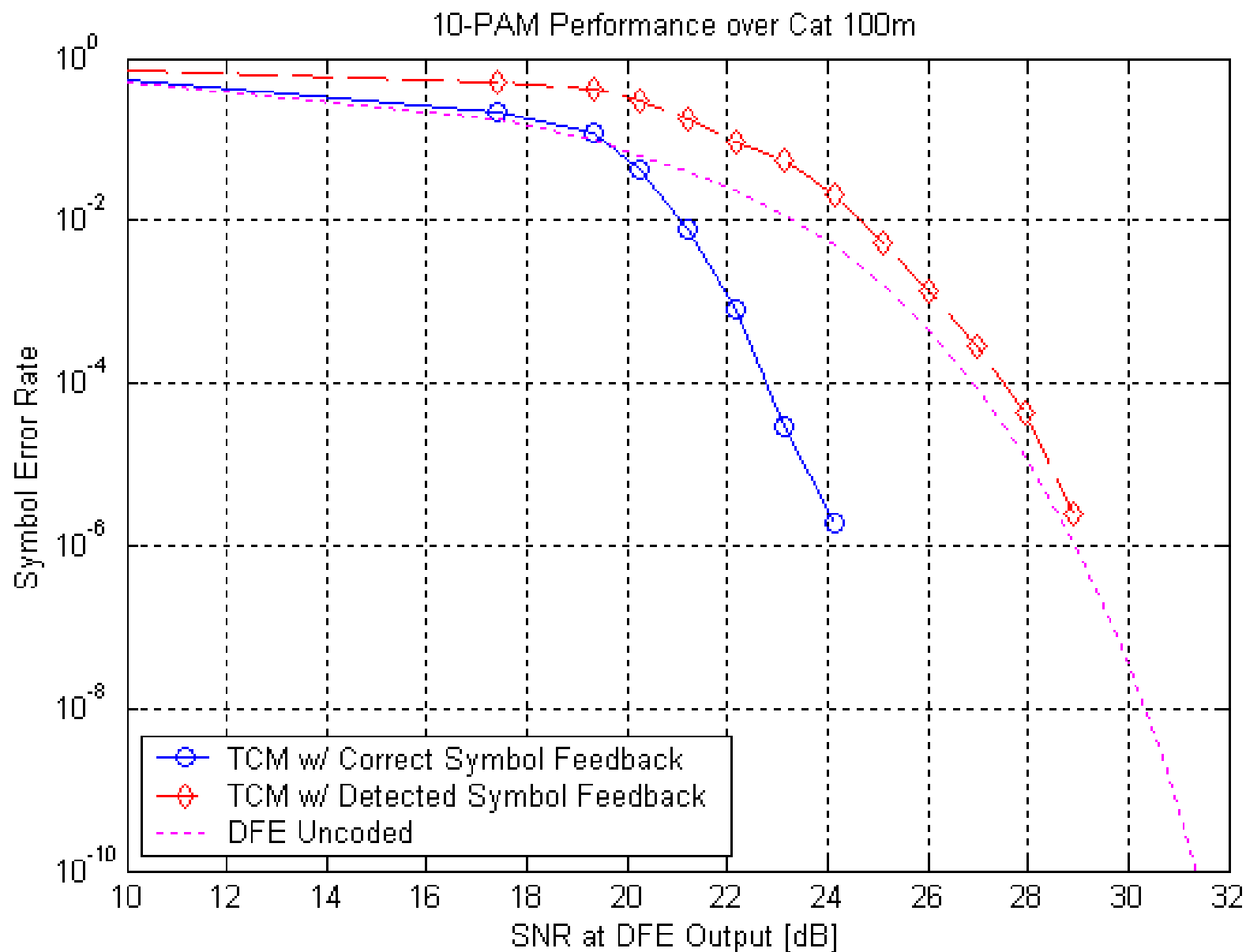
After examining various possibilities, it was found to be caused due to the error feedback and subsequent propagation in the FB section of the DFE.

This was confirmed by feeding back the correct symbols ....

# TCM + DFE : Correct Symbol feedback



## Correct Symbol feedback : Results

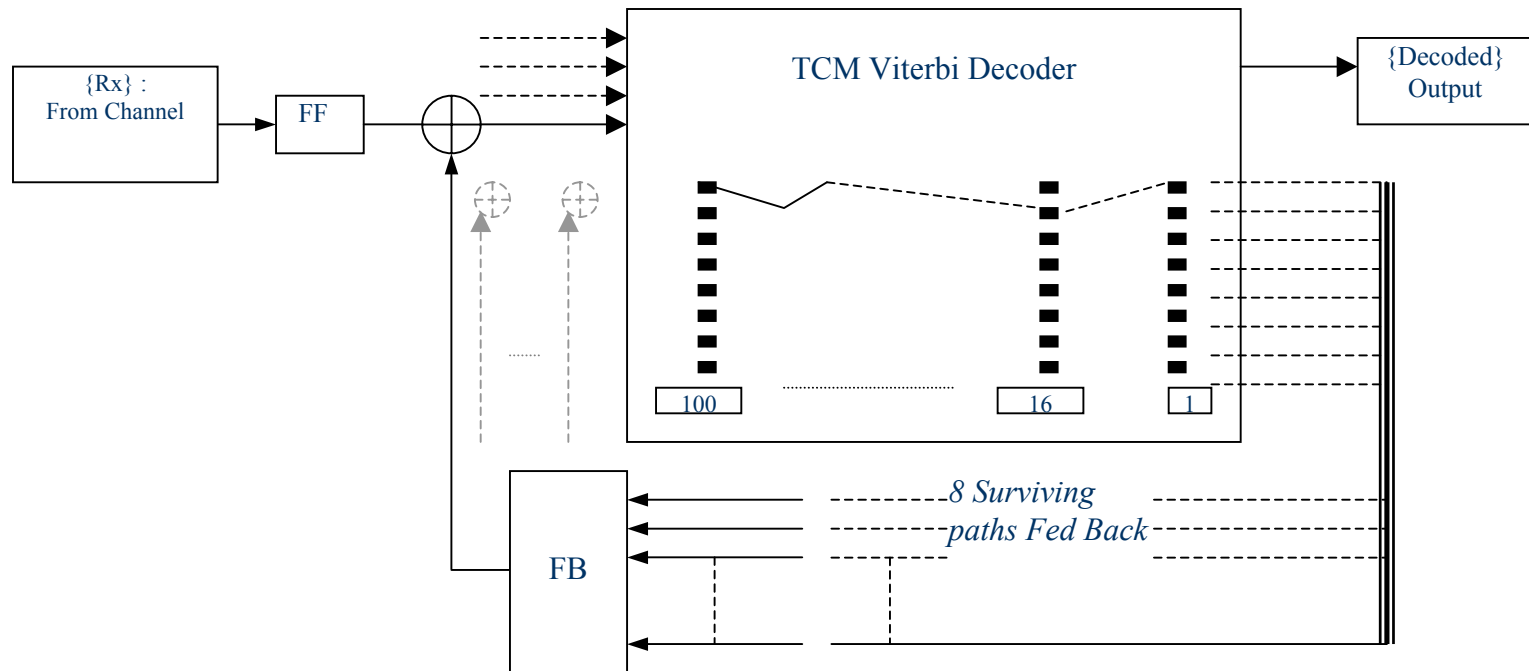




## *Joint Equalization and Decoding : SPFE*

- ◆ The success of correct symbol feedback prompted us to place the decoder inside the DFE loop. This scheme of implementation is called the **S**urvivor **P**ath **F**eedback **E**qualizer (SPFE).

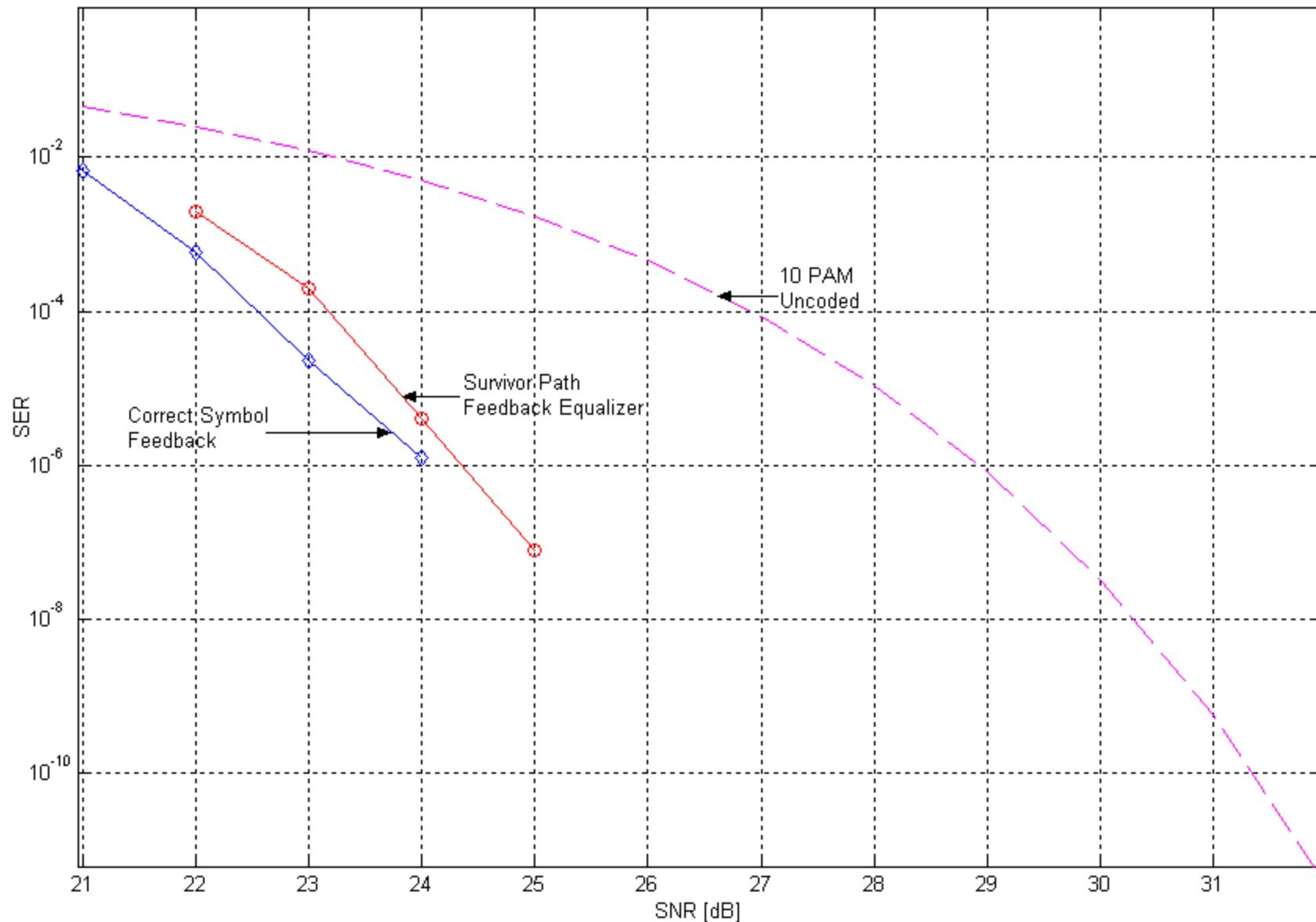
# *Survivor Path Feedback Equalizer*



- Each surviving path in the trellis is considered as a possible Tx
- 1<sup>st</sup> symbol has no pre cursor ISI, if detected correctly, it can correct the ISI in 2<sup>nd</sup> symbol ... and so on ...

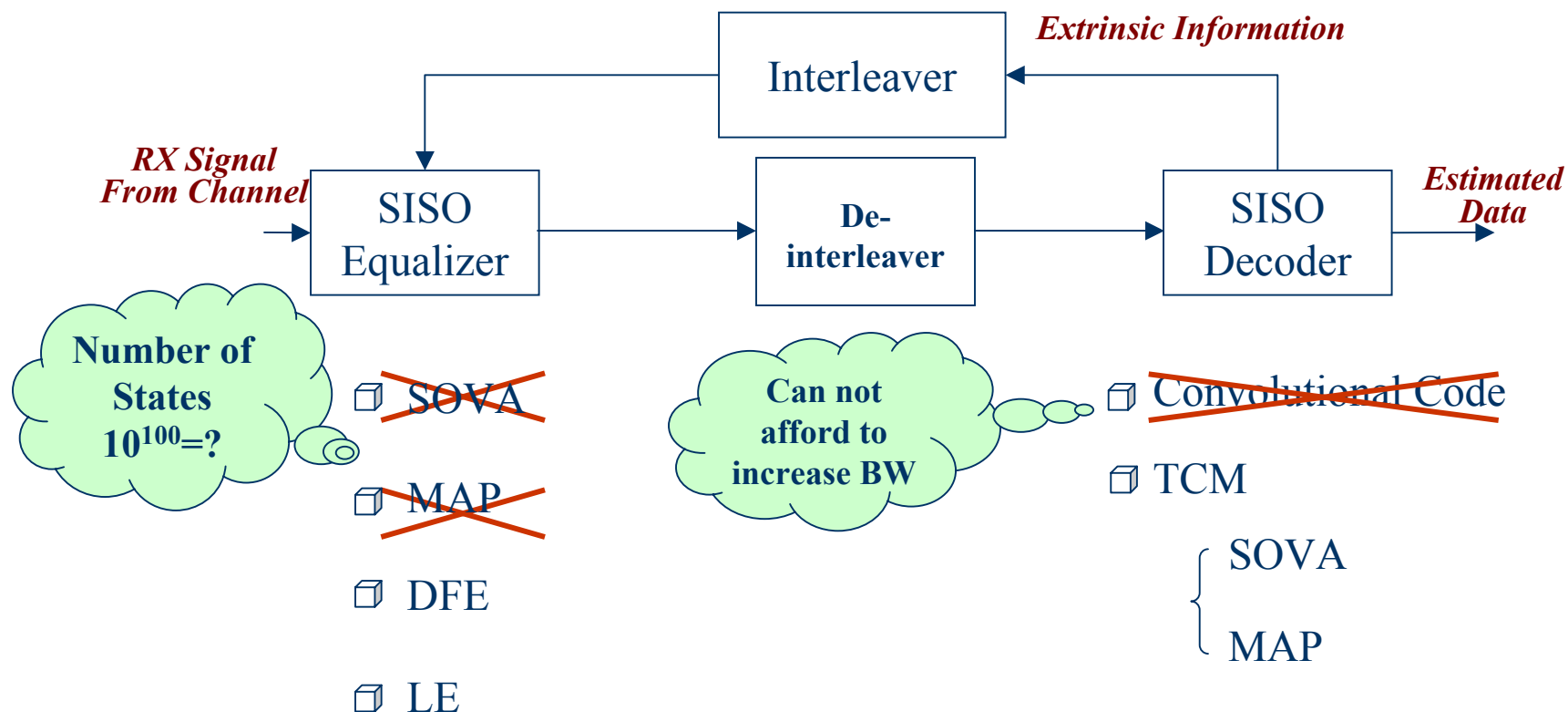


# Survivor Path Feedback Equalizer :Results





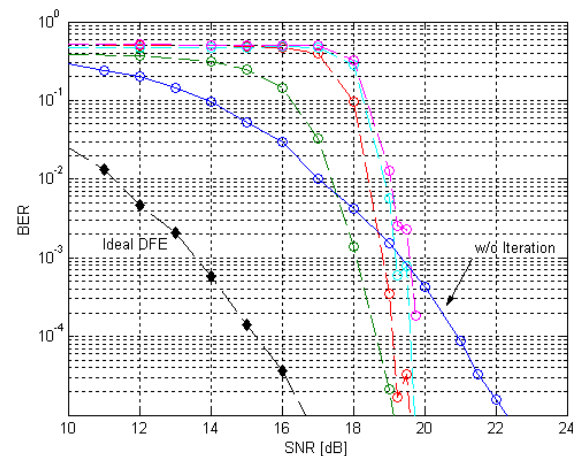
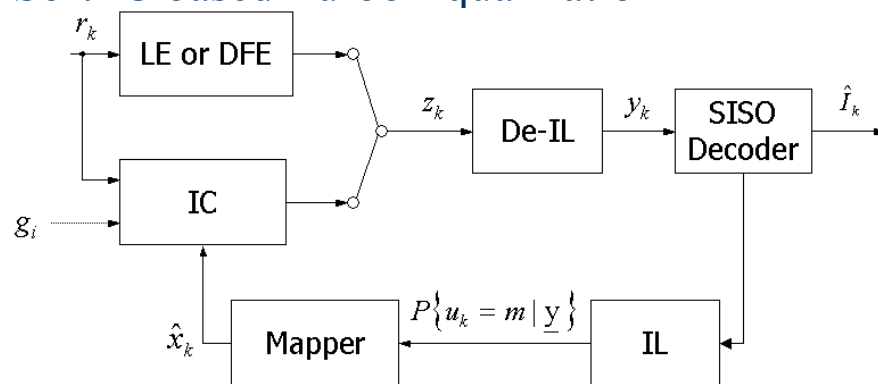
# ***TURBO Equalizer***



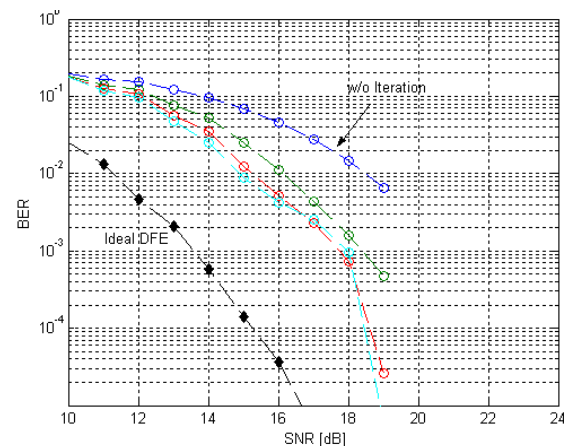
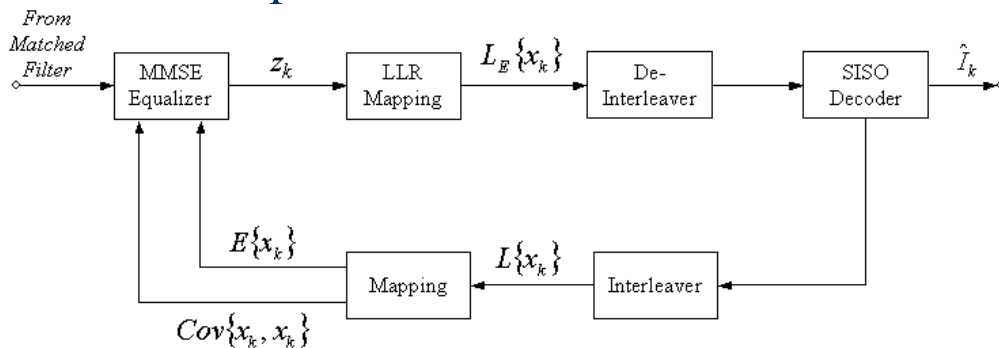
# TURBO Equalization

CAT- 6, BPSK , rate  $\frac{1}{2}$  code , 833Mbaud

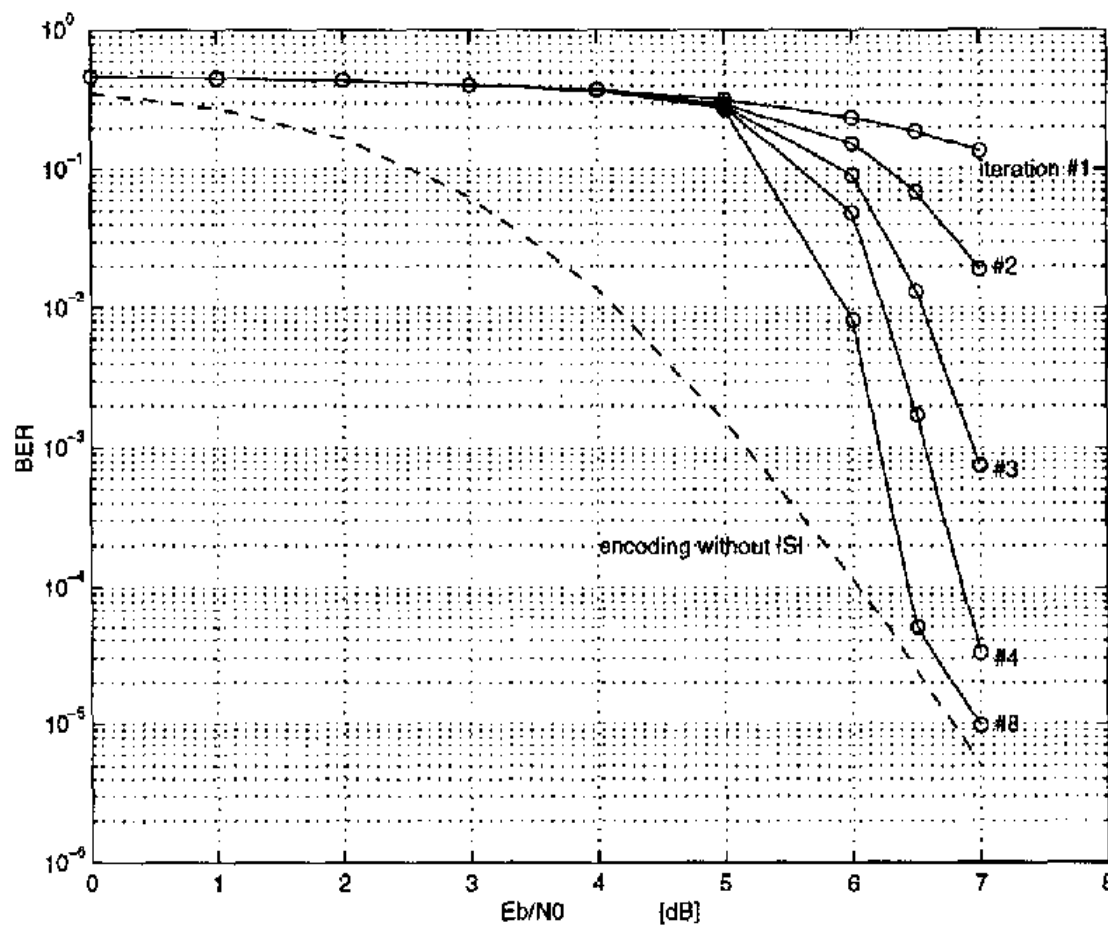
## Soft IC based Turbo Equalization



## MMSE Equalization



# *TURBO Equalizer applied to TCM*



MAP Equalization

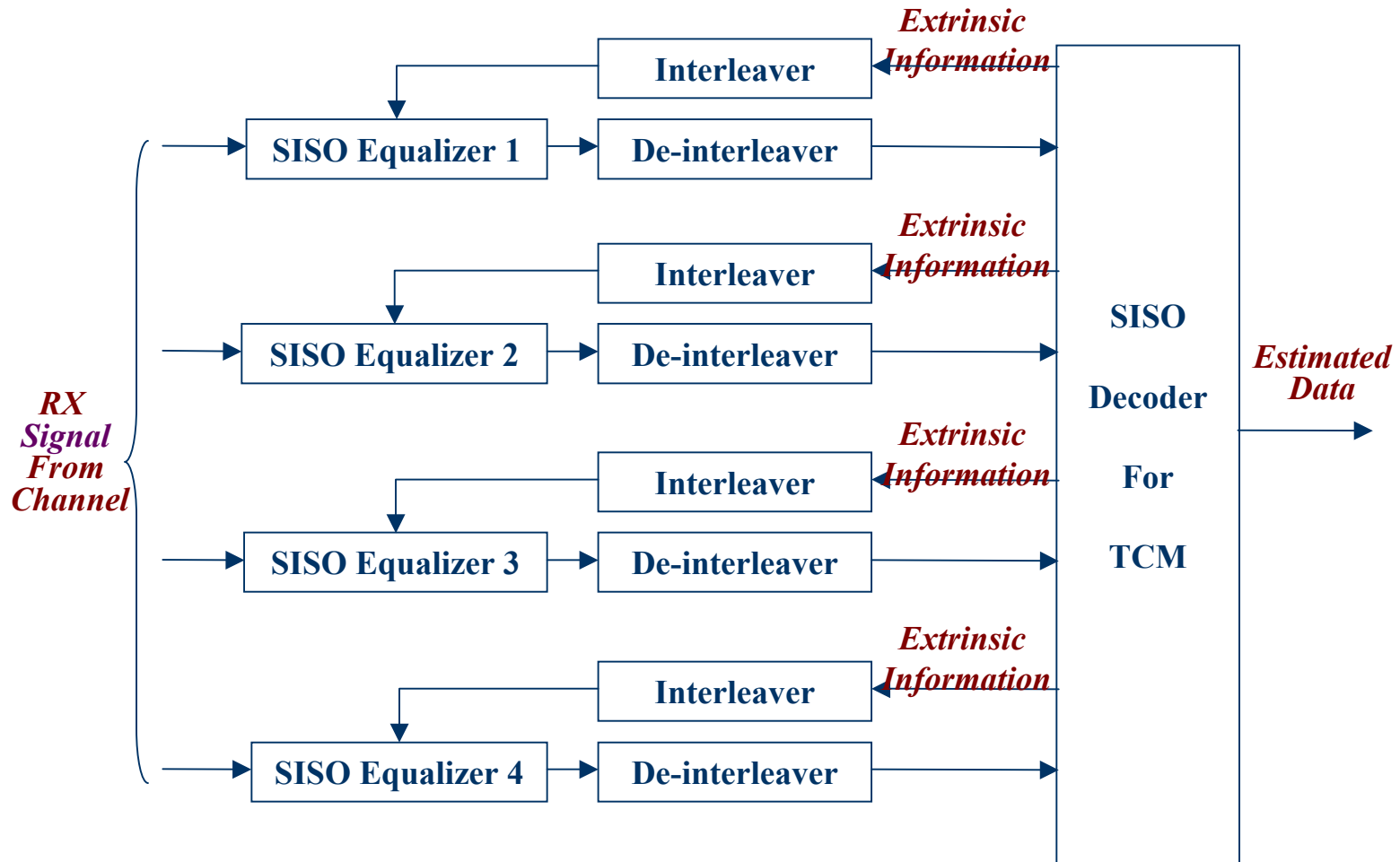
TCM

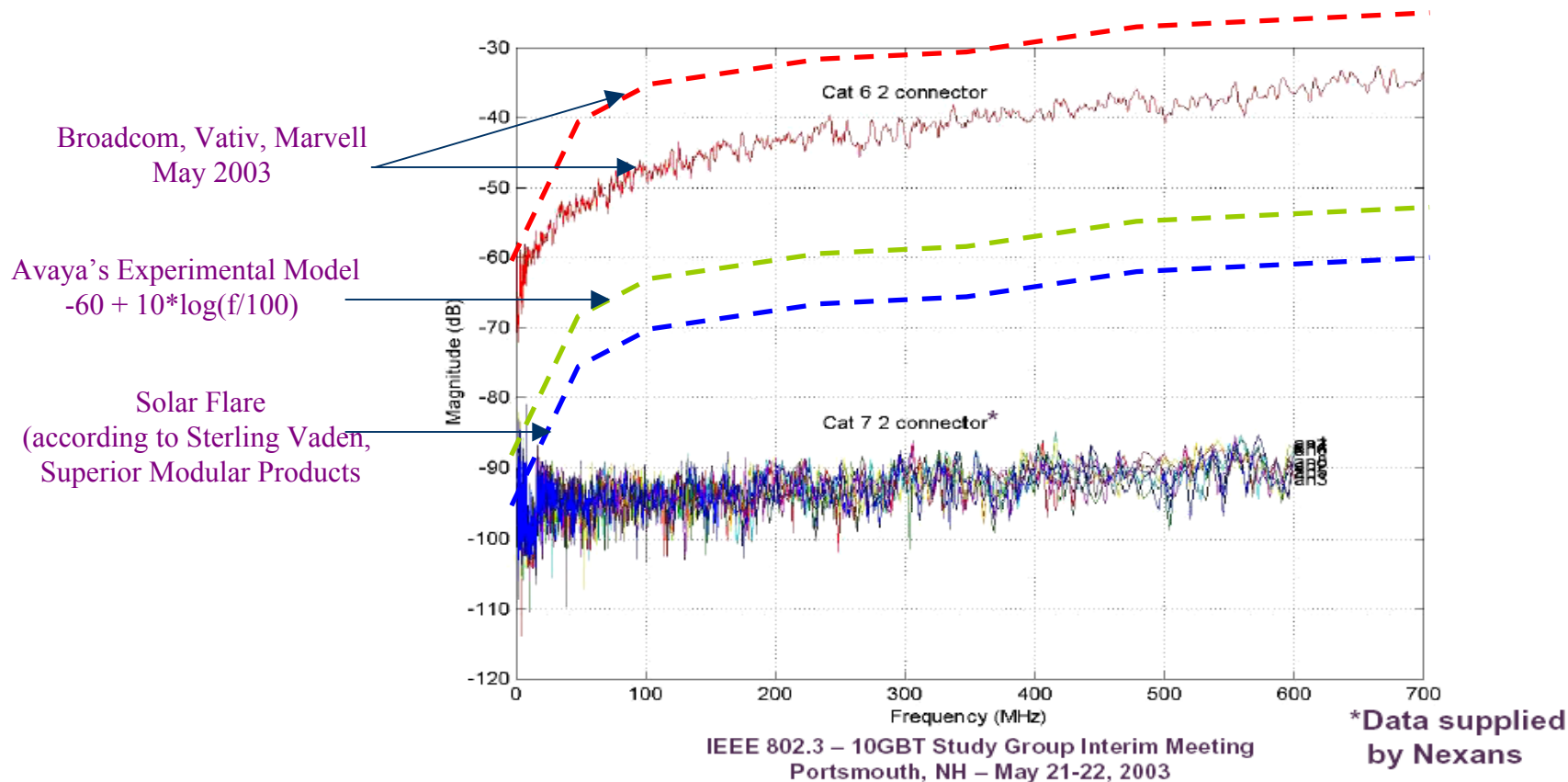
8-PSK

2/3 Code Rate

With memory  $m=3$

## *Anticipated RX structure*







## *Summary*

- Using recent ANEX measurements, there is every indication that 10Gbps transmission over 100m of CAT-6 is possible, given a target average BER of  $10^{-12}$ .
- Since performance is ANEXT-limited, ANEXT specifications need be finalized ASAP.



✧ Thank you ✧