## IEEE 802.3 400 Gb/s Ethernet Study Group Informal Communication

Source: IEEE 802.3 400 Gb/s Ethernet Study Group<sup>1</sup>

> Stephen Chairman, ITU-T Study Group 15 steve.trowbridge@alcatel-lucent.com

Trowbridge

Rapporteur, ITU-T Q6/15 Peter Stassar

peter.stassar@huawei.com To:

Associate Rapporteur, ITU-T Q6/15 Pete Anslow

panslow@ciena.com Rapporteur, ITU-T Q11/15

Mark Jones mark.jones@xtera.com

Chair, IEEE 802 LMSC Paul Nikolich

p.nikolich@ieee.org

Chair, IEEE 802.3 Ethernet Working Group David Law

dlaw@hp.com

Vice-chair, IEEE 802.3 Ethernet Working Group Wael Diab

wdiab@broadcom.com

Secretary, IEEE 802.3 Ethernet Working Group Adam Healey

adam.healey@lsi.com

Chair, IEEE 802.3 400 Gb/s Ethernet Study Group From: John D'Ambrosia

John DAmbrosia@dell.com

Subject: Informal communication reply to ITU-T Study Group 15

Agreed to at IEEE 802.3 400 Gb/s Ethernet Study Group meeting, York, UK, 3 Approval:

September 2013

CC:

Dear Mr. Trowbridge and members of ITU-T Study Group 15,

Thank you for your response to our communication concerning the formation of the 400 Gb/s Ethernet Study Group.

The 400 Gb/s Ethernet Study Group held its 3rd meeting from 2 to 3 September 2013 in York, UK. At our 2nd and 3<sup>rd</sup> meetings, the Study Group adopted the following objectives:

- Support a MAC data rate of 400 Gb/s
- Support full duplex operation only
- Preserve the Ethernet frame format utilizing the Ethernet MAC

<sup>1</sup> This document solely represents the views of the IEEE 802.3 400 Gb/s Ethernet Study Group, and does not necessarily represent a position of the IEEE, the IEEE Standards Association, IEEE 802 or the IEEE 802.3 Working Group

- Preserve minimum and maximum FrameSize of current Ethernet standard
- Provide appropriate support for OTN
- Specify optional Energy Efficient Ethernet (EEE) capability for 400Gb/s PHYs
- Support optional 400 Gb/s Attachment Unit Interfaces for chip-to-chip and chip-to-module applications

Concerning the other aspects mentioned in your liaison statement (overall bit-rate for 400 Gb/s Ethernet including line coding; numbers, relationship and coding of physical and logical lanes; FEC present on the interface; and assumptions and requirements on error performance), you are correct in your understanding that with the exception of error performance, these are the kinds of issues we would expect to address during the consideration of proposals in the task force phase after a PAR (project authorization request) has been approved. The earliest we would expect a PAR to be submitted is at the March 2014 plenary in Beijing.

Our web and email reflector area can be found at: http://ieee802.org/3/400GSG/index.html

Materials from our three meetings held so far can be found at:

Victoria, BC, Canada 17 May 2013 <a href="http://ieee802.org/3/400GSG/public/13">http://ieee802.org/3/400GSG/public/13</a> 05/index.shtml

Geneva, Switzerland 16-18 July 2013 <a href="http://ieee802.org/3/400GSG/public/13">http://ieee802.org/3/400GSG/public/13</a> 07/index.shtml

York, UK 2-3 September 2013 <a href="http://ieee802.org/3/400GSG/public/13">http://ieee802.org/3/400GSG/public/13</a> 09/index.shtml

Sincerely,

John D'Ambrosia
Chair, IEEE 802.3 400 Gb/s Ethernet Study Group
<John DAmbrosia@dell.com>