

# Codeword Examples –

in support of Comment #107 on 802.3df D3.0

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# Comment #107

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*Cl* **172**      *SC* **172.2.4**                      *P***211**                      *L* **10**                      # **I-107**  
Dawe, Piers J G                                      NVIDIA  
*Comment Type*    **TR**                      *Comment Status*    **D**                                      *encoding example*

There is an informative Annex 119A, 200GBASE-R and 400GBASE-R PCS FEC codeword examples.

## *Suggested Remedy*

As the Clause 172 PCS is subtly different to Clause 119, with partly different alignment markers and the block distribution and synchronised alignment marker groups of the two flow method, there are new opportunities for ambiguity and misunderstanding that 119A won't catch. So, please prepare a similar annex for Clause 172. Add text here and at the beginning of 172 and 169.2.3 mentioning it. Revise the amendment description on page 14.

Please prepare a plain-text file with the large tables for convenient reading into a program, and post it on the project web site for review with future drafts.

*Proposed Response*                      *Response Status*    **W**

PROPOSED REJECT.

A new annex as suggested would be an improvement to the draft. However, a contribution providing a complete proposal including values for the tables, start-up conditions, etc. is required.

# Proposed solution: Adding Annex 172A for 800GBASE-R PCS FEC example codewords

- Language in Annex 119A can be mostly reused
- Use the same example payload (scrambled idles), Flow 0 seed and PRBS9 seed(s) as 119A
- Flow 0 is mostly the same as for 400G in Table 119A–2 and Table 119A–5
  - Parts of alignment markers and the parity differ – indicated for flow 0 only by red bars
- Per 119A, italics show alignment markers, bold shows padding for the alignment markers
- There are further processing steps, not illustrated: distribution and interleaving in 172, and 32:8 restricted bit-level multiplexing in 173
- Detailed Example payload and initial states:
  - Data sent: scrambled idle.  
`tx_xcoded<0:256> = 007000000000000000780000000000000078000000000000007800000000000000`
  - Scrambler seeds were different for the two flows:  
Flow 0: `S<0:57> = 24E 6959 D0FA 5DBD`  
Flow 1: `S<0:57> = 1FB 5885 7D81 624F`
  - PRBS9 seeds for AM padding were the same for both flows, `P<0:8> = 0x100`.
  - `tx_am_sf<2:0> = {0,0,0}`, for both flows.





Thank you!