

Objectives

IEEE 802.3 Beyond 400 Gb/s Ethernet Study Group

John D'Ambrosia,
Chair, IEEE 802.3 Beyond 400 Gb/s Ethernet Study Group
Futurewei, U.S. Subsidiary of Huawei

July 14, 2021

B400G Adopted Objectives

* Adopted by B400G SG, Apr 2021
** Adopted by B400G SG Apr 26, 2021
*** Adopted by B400G SG May 3, 2021
**** Adopted by B400G SG May 17, 2021
Adopted by B400G SG Jun 3, 2021
Adopted by B400G SG Jul 13, 2021
Approval by 802.3 WG Pending

- **Non-Rate Specific**

- Support full-duplex operation only *
- Preserve the Ethernet frame format utilizing the Ethernet MAC *
- Preserve minimum and maximum FrameSize of current IEEE 802.3 standard *
- Support a BER of better than or equal to 10^{-13} at the MAC/PLS service interface (or the frame loss ratio equivalent) **
- Provide support to enable mapping over OTN ***

- **200 Gb/s Related**

- Support a MAC data rate of 200 Gb/s ##
- Support optional single-lane 200 Gb/s attachment unit interfaces for chip-to-module and chip-to-chip applications ##
- Define a physical layer specification that supports 200 Gb/s operation:
 - over 1 pair of SMF with lengths up to at least 500 m ##
 - over 1 pair of SMF with lengths up to at least 2 km ##

- **400 Gb/s Related**

- Support a MAC data rate of 400 Gb/s ##
- Support optional two-lane 400 Gb/s attachment unit interfaces for chip-to-module and chip-to-chip applications ##
- Define a physical layer specification that supports 400 Gb/s operation:
 - over 2 pairs of SMF with lengths up to at least 500 m ##

B400G Adopted Objectives

* Adopted by B400G SG, Apr 2021
** Adopted by B400G SG Apr 26, 2021
*** Adopted by B400G SG May 3, 2021
**** Adopted by B400G SG May 17, 2021
Adopted by B400G SG Jun 3, 2021
Adopted by B400G SG Jul 13, 2021
Approval by 802.3 WG Pending

- **800 Gb/s Related**

- Support a MAC data rate of 800 Gb/s *
- Support optional eight-lane 800 Gb/s attachment unit interfaces for chip-to-module and chip-to-chip applications ****
- Support optional four-lane 800 Gb/s attachment unit interfaces for chip-to-module and chip-to-chip applications ****
- Define a physical layer specification that supports 800 Gb/s operation:
 - over 8 pairs of MMF with lengths up to at least 50 m *
 - over 8 pairs of MMF with lengths up to at least 100 m *
 - over 8 pairs of SMF with lengths up to at least 500 m *
 - over 8 pairs of SMF with lengths up to at least 2 km #
 - over 4 pairs of SMF with lengths up to at least 500 m *
 - over 4 pairs of SMF with lengths up to at least 2 km *
 - over 4 wavelengths over a single SMF in each direction with lengths up to at least 2 km *
 - over a single SMF in each direction with lengths up to at least 10 km *
 - over a single SMF in each direction with lengths up to at least 40 km *

- **1.6 Tb/s Related**

- Support a MAC data rate of 1.6 Tb/s #
- Support optional eight-lane 1.6 Tb/s attachment unit interfaces for chip-to-module and chip-to-chip applications #
- Define a physical layer specification that supports 1.6 Tb/s operation:
 - over 8 pairs of SMF with lengths up to at least 500 m #
 - over 8 pairs of SMF with lengths up to at least 2 km #