

# **Near Term 400Gb/s Ethernet Project Characteristics**

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Higher Speed Ethernet  
Consensus Industry Connections Ad Hoc  
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# Background

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- There is a strong desire by some IDC Operators and System OEMs for near term availability of 400Gb/s Ethernet for high end switching platforms
- At or below 100Gb/s Ethernet bit/sec cost
- Modest initial port volume
- Unfortunately short development cycle, no cost premium, and modest volume are not consistent with advanced technology development

# 400Gb/s Ethernet Project Characteristics

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- The only viable approach for a near term 400Gb/s Ethernet Project is to reuse 100Gb/s Ethernet technology and specifications
- Elements of a near-term 400Gb/s Ethernet Project
  - 400Gb/s MAC and PCS layer
  - 400Gb/s electrical I/O re-using CAUI-4 technology
  - 400Gb/s SMF PMD re-using 100GE-LR4 or –nR4 technology
  - 400Gb/s MMF PMD re-using 100GE-SR4 technology
  - 400Gb/s Cu PMD re-using 100GE-CR4 technology
- Future follow-on 400Gb/s Ethernet Project will define long-term, high-volume, low-cost electrical I/O and PMDs