Unconfirmed Meeting Minutes: IEEE 802.3 Isolation Ad Hoc

June 22, 2017
IEEE 802 Plenary
Berlin, Germany

Prepared by Jon Lewis

Meeting called to order Thursday July 13 at 11:00am.

Chaired by Jon Lewis, Dell EMC

Introductions and affiliations.

Presentation #1 – Agenda presented by – Jon Lewis, Dell EMC

URL: http://www.ieee802.org/3/ad_hoc/isolation/public/isolation_ad_hoc_agenda_071317.pdf

Mr. Lewis went over the agenda for the meeting –

- Presentations
- Patent policy
- Participation in the IEEE 802 Meetings
- Ad Hoc Information
- Goals for the Ad Hoc

Presentation #2 - IEC 60950 (-1) in IEEE 802.3-2015 and published amendments – Jon Lewis, Dell EMC

URL: http://www.ieee802.org/3/ad_hoc/isolation/public/802.3_isolation_071317a.pdf

I was noted that the current information was based on IEEE Std. 802.3-2015 and that it would be helpful to have the same information pulled from the latest maintenance revision draft. It would also be helpful to understand all differences in their specific clauses. The Chair agreed to have this by the next ad hoc call.

The next meeting of the 802.3 Isolation ad hoc will be 7:00AM Thursday, 3 August, 2017.

After concluding the agenda for the meeting the Chair adjourned the meeting at 11:48am.

Attendance:

IEEE 802.3 Isolation Ad Hoc				7/13/17
Last Name	**First Name**	**Employer**	**Affiliation**	**Thursday**
Abedy	Yossuf	Pulse Electronics	Pulse Electronics	X
Flatman	Alan	LAN Technologies	LAN Technologies	X
Graber	Steffen	Pepperl+Fuchs	Pepperl+Fuchs	X
Law	David	Hewlett Packard Enterprise	Hewlett Packard Enterprise	X
Lewis	Jon	Dell | EMC	Dell | EMC	X
Munson	Carrie	Pulse Electronics	Pulse Electronics	X
Peker	Arkadiy	Microsemi	Microsemi	X
Rashidi	Habib	Pulse Electronics	Pulse Electronics	X
Shariff	Masood	Commscope	Commscope	X
Thompson	Geoff	GraCaSI SA.	Independent	X
Vaden	Sterling	Surtec, Ind.	Vaden Enterprises	X
Walker	Dylan	Cisco	Cisco	X
Zimmerman	George	CME Consulting	Commscope, Aquantia, LTC, CME Consulting, Cisco, ADI, BMW	X
Lukacs	Miklos	Silicon Labs	Silicon Labs	X
Lemahieu	Jocis	On Semiconductor	On Semiconductor	X