

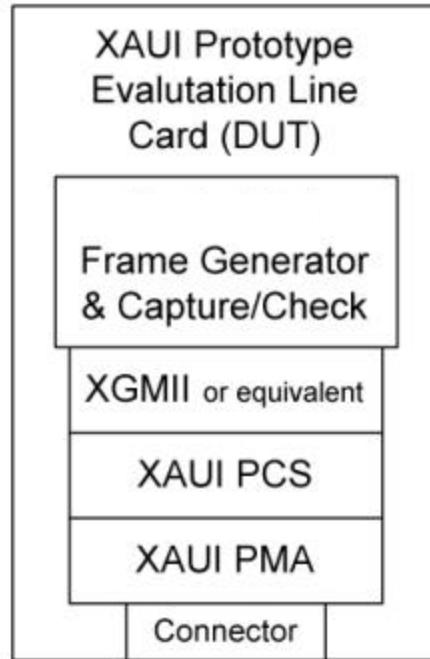
10GEA - XAUI Interoperability Group Test Proposal

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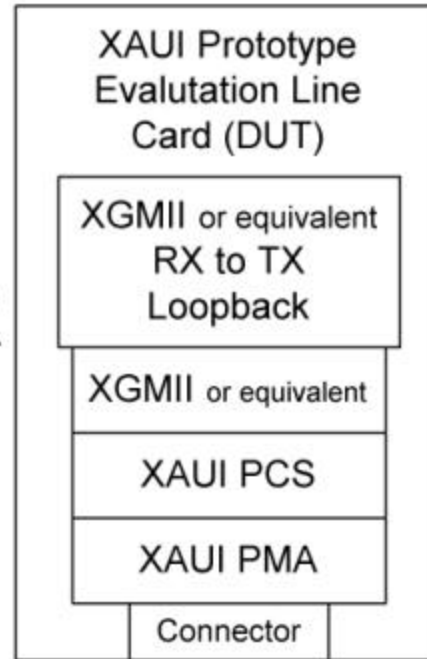
Proposal - Passed

- Every interoperability test card must support:
 - a frame tester (Strongly Preferred)
 - XAUI CRPAT Generation (Annex 48A.4)
 - and optionally CJPAT (Annex 48A.5)
 - Payload Verification (allowing for clock rate tolerance)
 - If not supported, a far-end (from XAUI) loop back, eg:
 - Optical loop back
 - XGMII Rx to XGMII Tx
 - Note - this implementation will only allow testing against a vendor with pattern generation

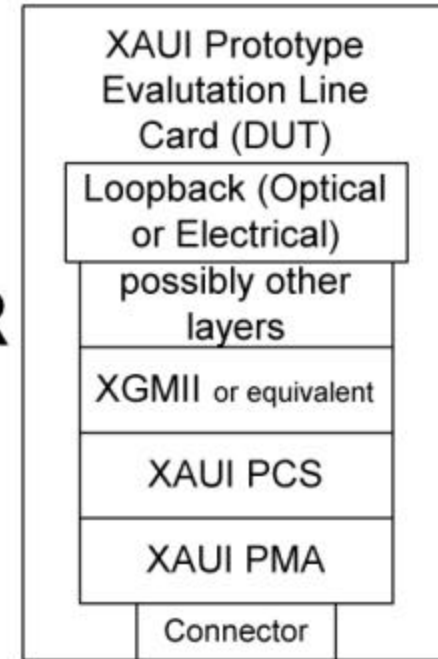
Type #1



Type #2a: *DTE* XGXS

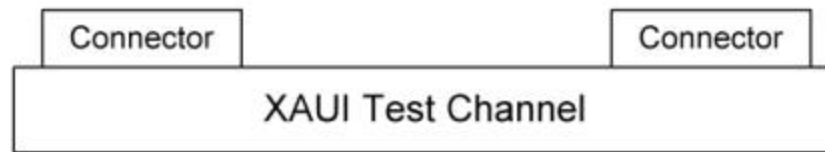


Type #2b: *PHY* XGXS



OR

OR



Of the two line cards connected to the test channel - at least one must be type #1

Contact Information

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 - http://www.amp.com/simulation/files/papers/20GC014_Revb.pdf
 - http://www.amp.com/simulation/files/papers/20GC015_1_RevA.pdf