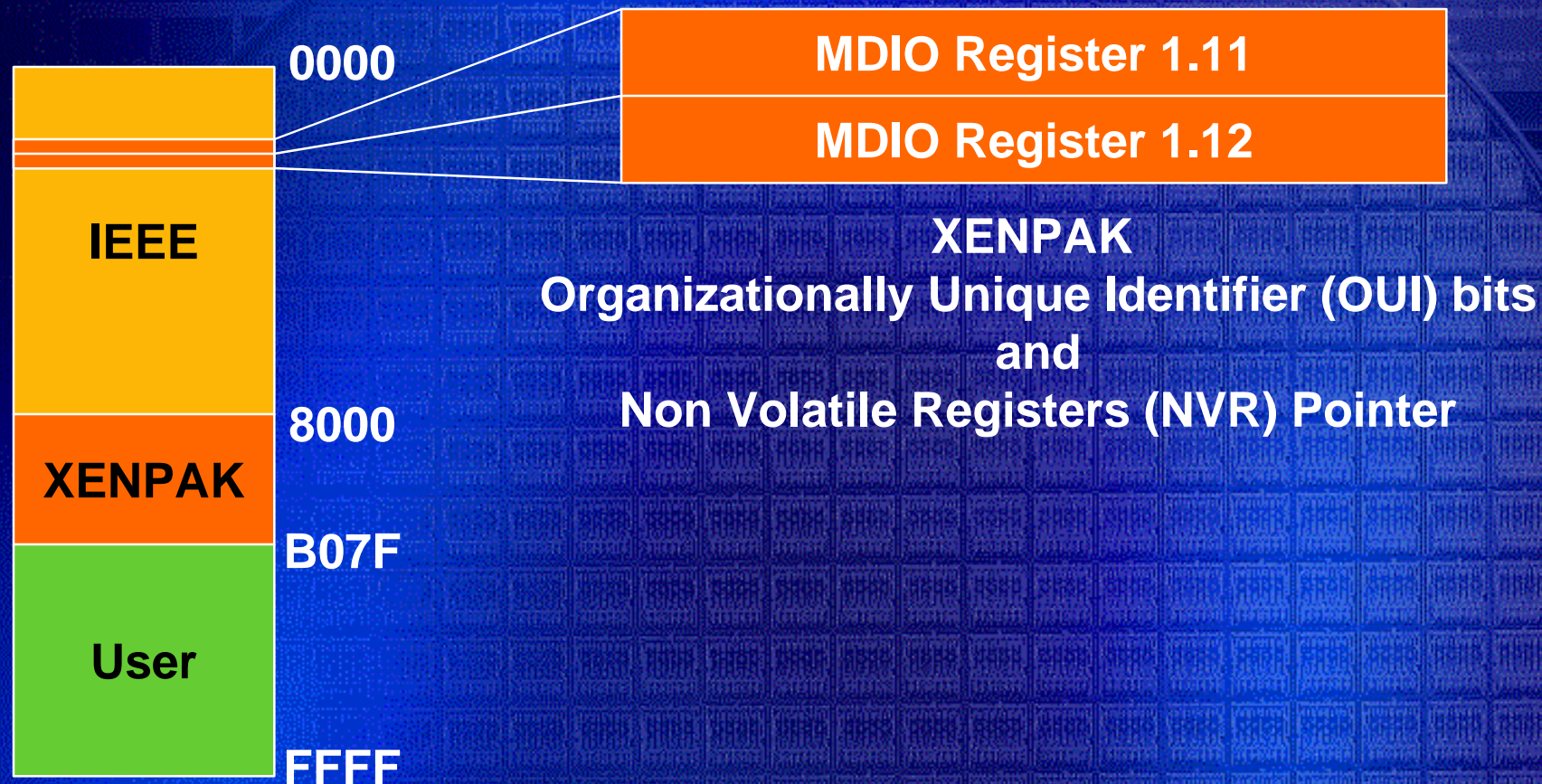


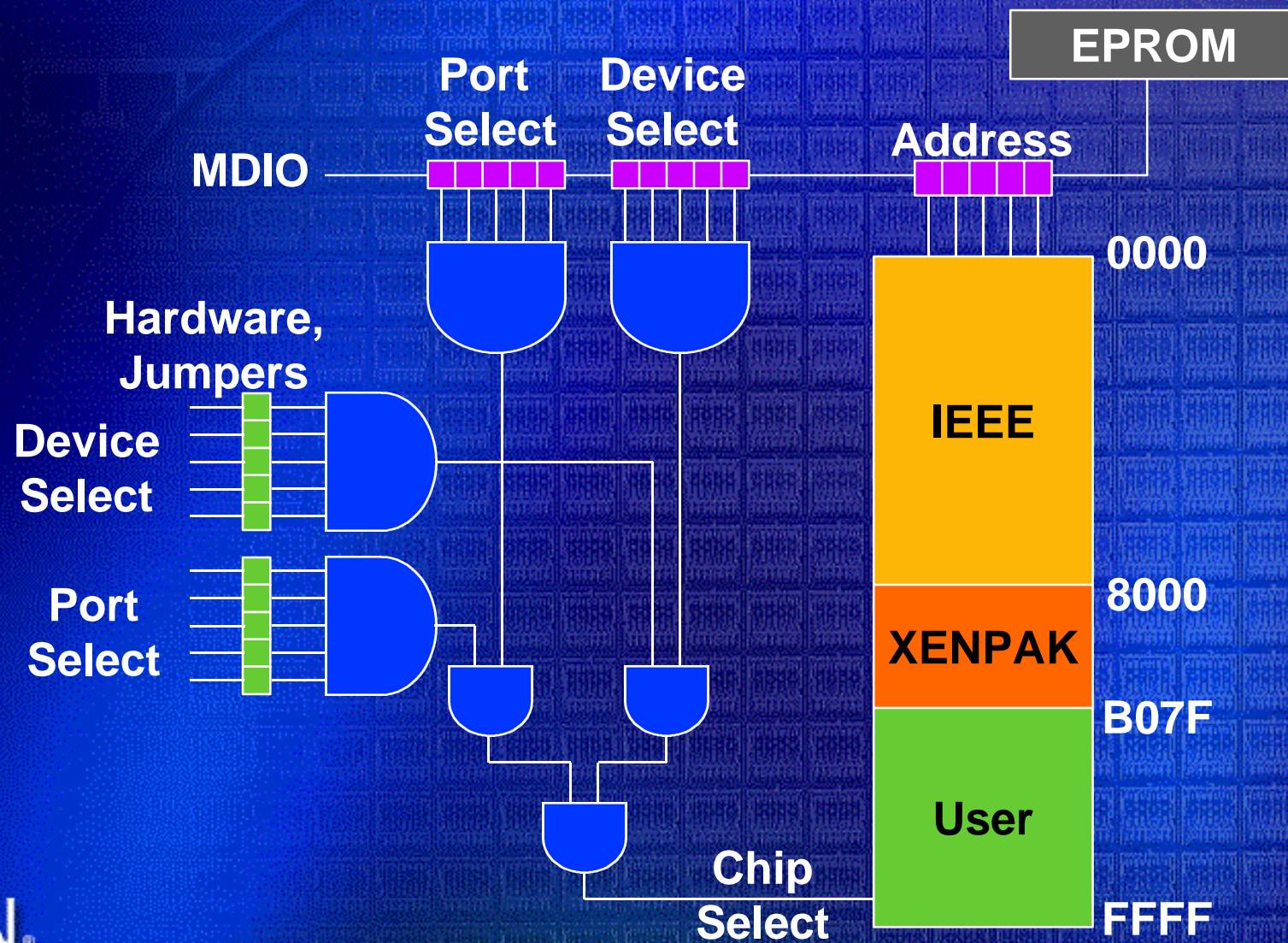
MDIO and Module Device Addressing

Schelto van Doorn
Don Alderrou

XENPAK Address space



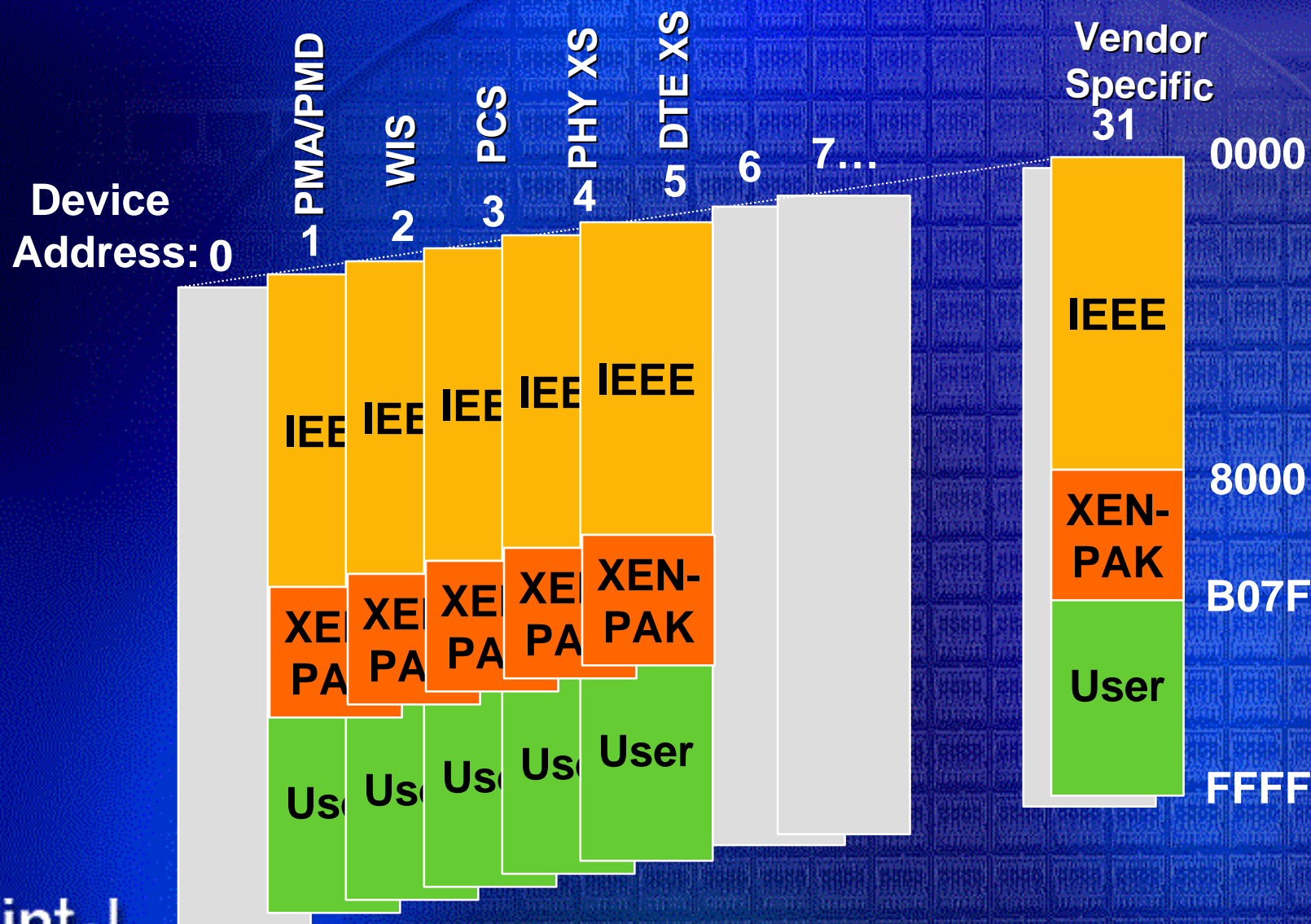
Selecting the memory bank



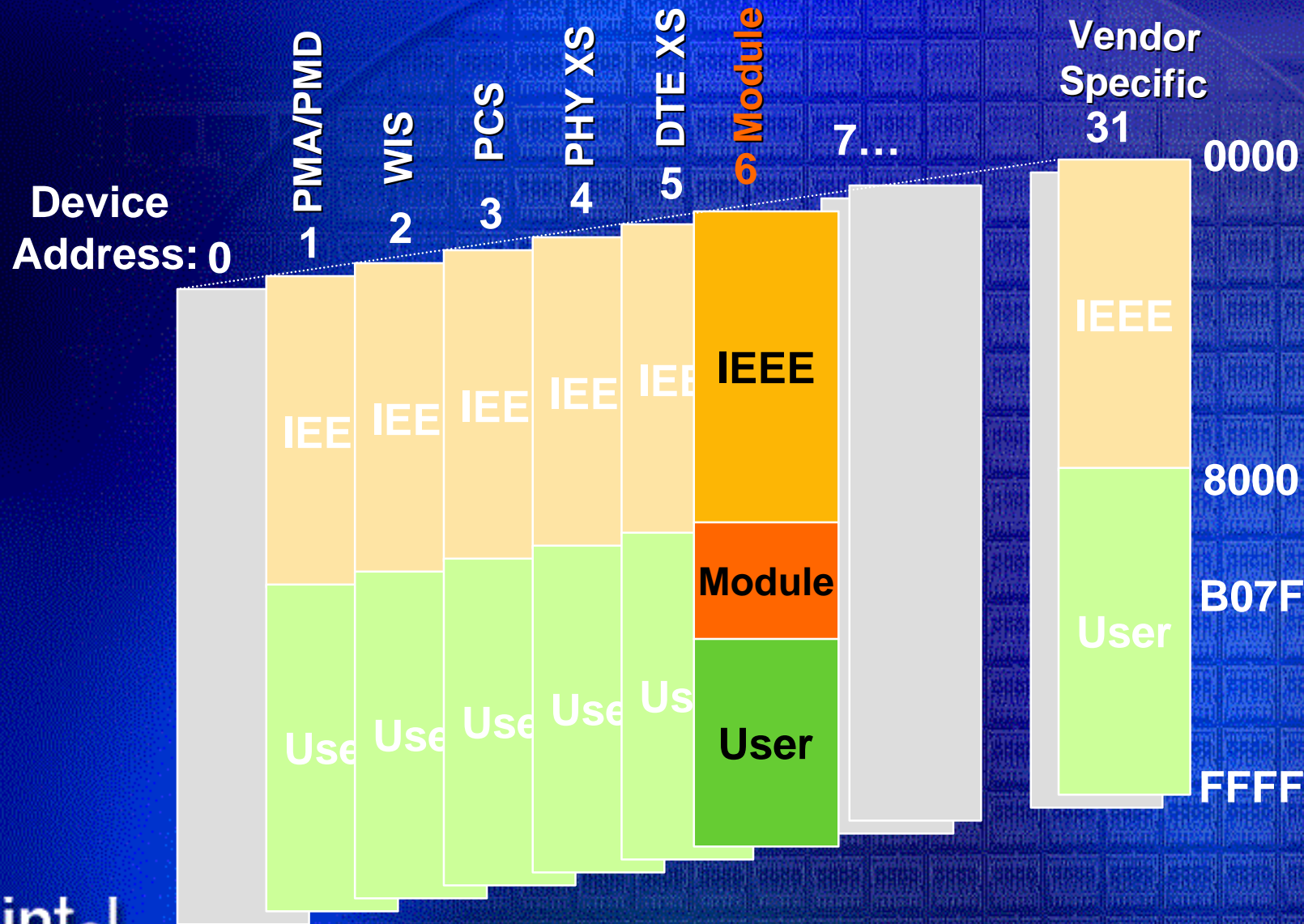
IEEE 802.3 Device addresses

0	Reserved
1	PMA/PMD (XENPAK)
2	WIS (XENPAK)
3	PCS (XENPAK)
4	PHY XS (XENPAK)
5	DTE XS (XENPAK)
6	Module (new proposal)
7 through 30	Reserved
31	Vendor Specific (XENPAK)

Blocking of address space



Module address space



New Module Device address

- Clearly defines the module space
- Eliminates the need for a complicated transceiver Identification algorithm
- Leaves the full address space for each device
- Does not block address space in multiple devices
- Eliminates contention in multiple devices address spaces