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Implementation of SiGe IC solution for 10G equalization of PMD (Polarization Mode Dispersion)

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¹ Outline

- u Position of our work
- u PMD induced distortion
- u Numerical analysis of equalization
- u SiGe equalizer ICs
- u PMD mitigation experiments
- u Adaptation schemes
- u Conclusion
- u References



- ≥ 10Gb/s, SONET (OC-192)
- Long link length, >100km
- Single-mode fiber !
- Optical amplification (EDFA, 1.55μm)

- Transmission limited by
 - ▶ Optical noise (OSNR limited)
 - ▶ Chromatic dispersion (GVD)
 - ▶ Fiber non-linearity (SPM, FWM, XPM, Raman)
 - ▶ **PMD** (some fibers)

- Mitigation of these impairments
 - ▶ Optical signal processing (e.g. DCF,...)
 - ▶ **Post-detection electronic signal processing**



- u PMD model = cascaded optical birefringences of transmission fiber
 - w. signal splitting at butt coupling points
- u leads to multi path propagation

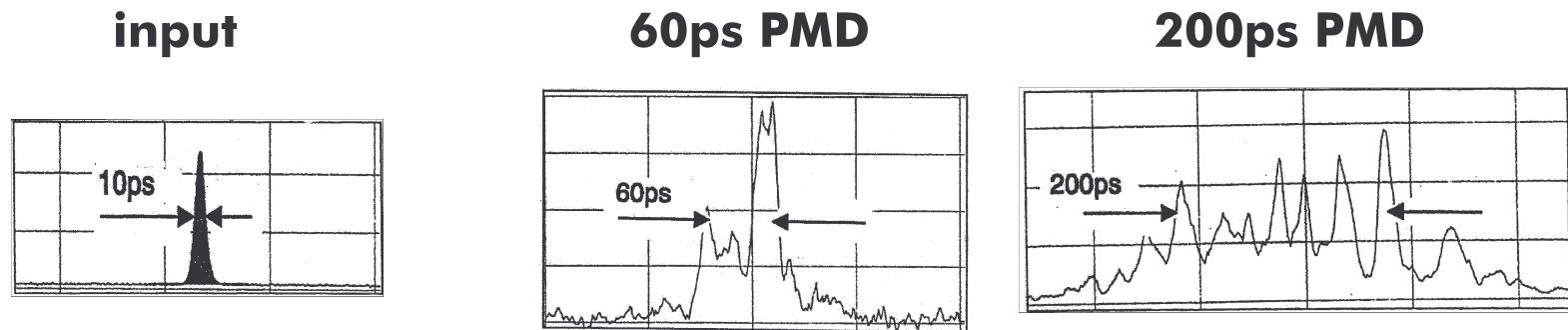


- u Similar to DMD where the signal spreads among many waveguide modes (= paths) w. different group delays

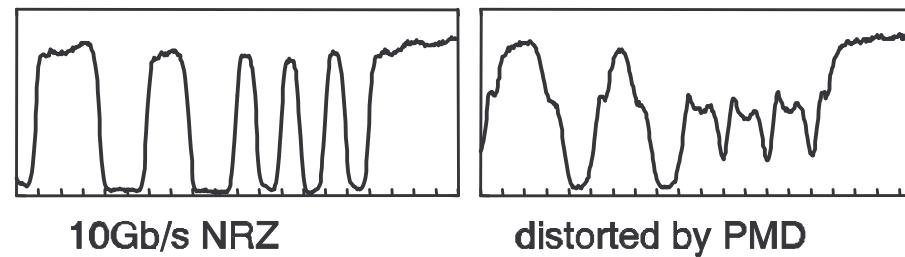
Electronic PMD equalization PMD induced distortion



- u Pulse responses of high PMD lab fibers w. 60ps and 200ps PMD



- u First-order PMD leads to dual path propagation

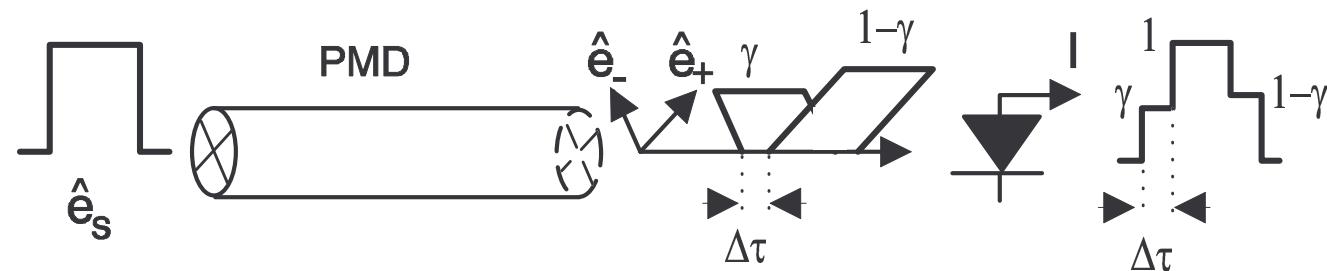


Electronic PMD equalization

Numerical analysis of 1st-order PMD equalization 1



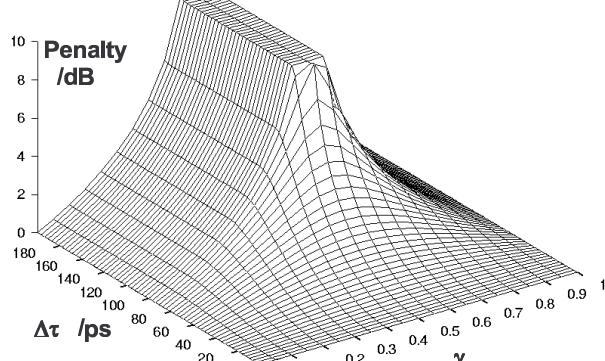
- u 1st-order PMD distortion quantified by
 - differential group delay (DGD) $\Delta\tau$
 - relative fast axis power γ



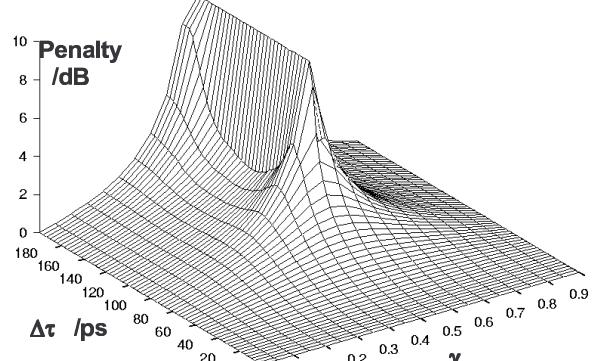


- Residual penalty (reduction of receiver sensitivity)
vs. PMD distortion plane

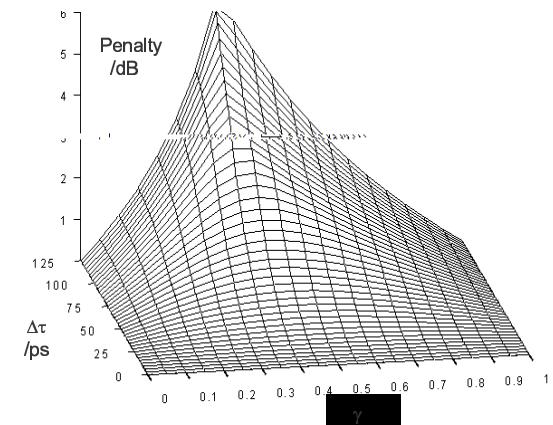
w/o. equalizer



FFE



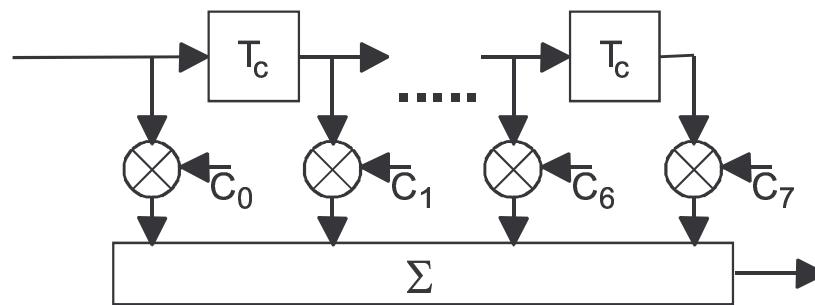
DFE
(1 feedback)



- PMD induced high penalty and penalty poles can be mitigated by electronic equalization



- u Transversal filter (TF) = Feed forward equ. (FFE)
 - ▶ 10Gb/s
 - ▶ 8 taps
 - ▶ 55ps spacing (fractionally spaced)
 - ▶ "analog" processing (HBT)
 - 1 delays by amp cascades
 - 1 $\approx 200\text{mV}$ differential signals



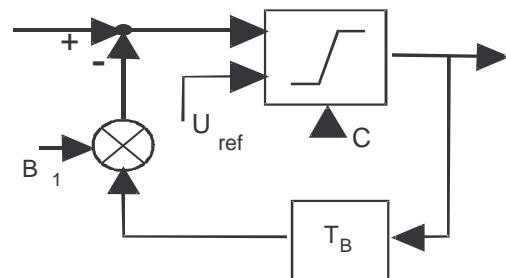


u Decision feedback equalizer

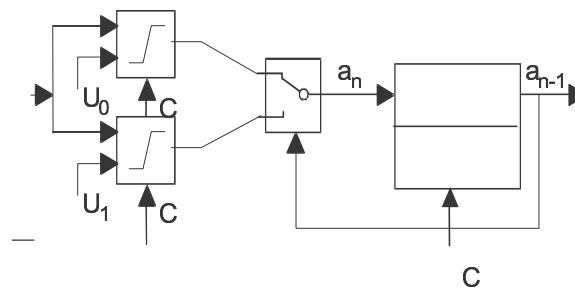
- ▶ 10Gb/s
- ▶ 1 feedback loop
- ▶ high-speed realization
- ▶ "analog" processing (HBT)

(S.Kasturia, J.H.Winters, J. on Select. Areas Commun., 1991, pp. 71)

u Functionality



u Realization

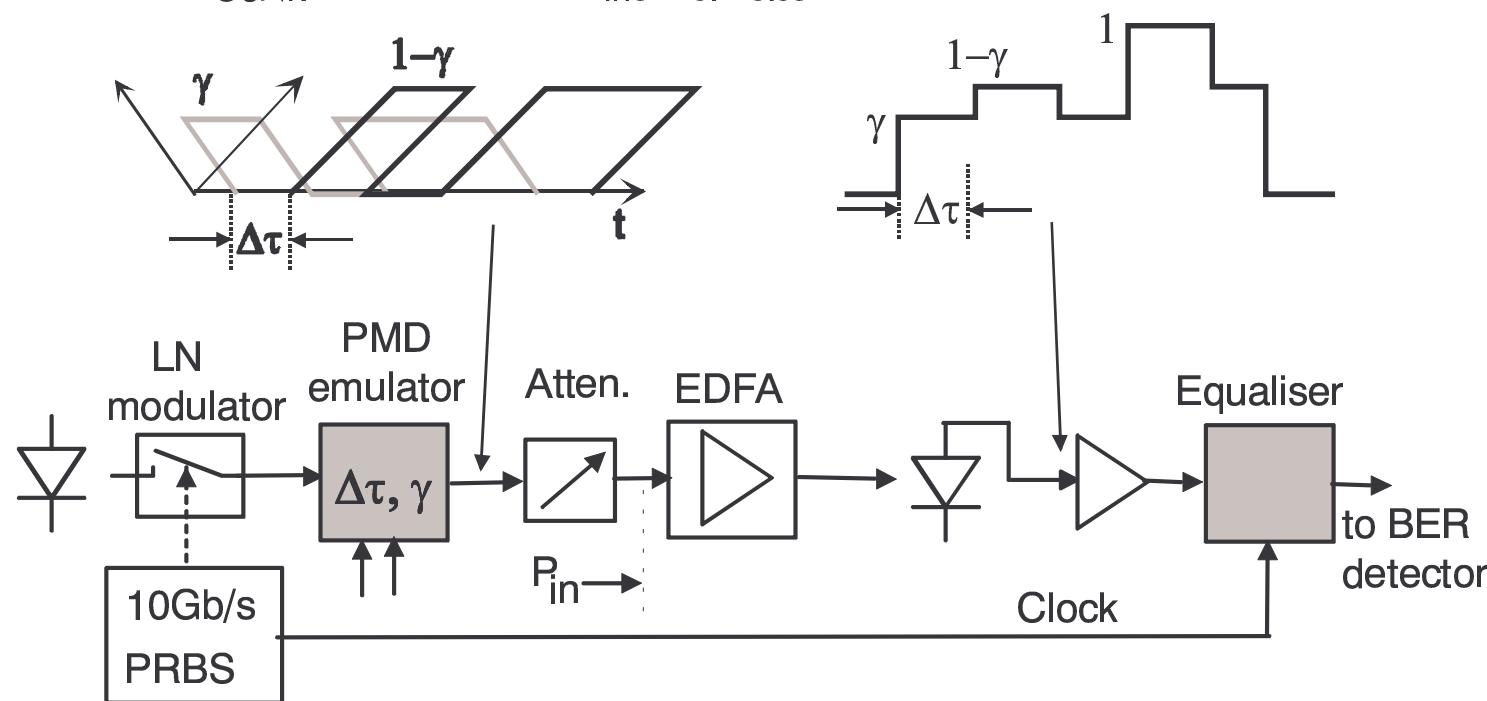


Electronic PMD equalization

Set-up for measurement of PMD equalization



- u 10Gb/s
- u 1st-order PMD emulator
- u manual optimization of taps
- u EDFA preamp determines noise
(optical signal-to-noise ratio limited, OSNR)
⇒ $\text{penalty}_{\text{OSNR}} \approx 2 \times \text{penalty}_{\text{thermal noise}}$

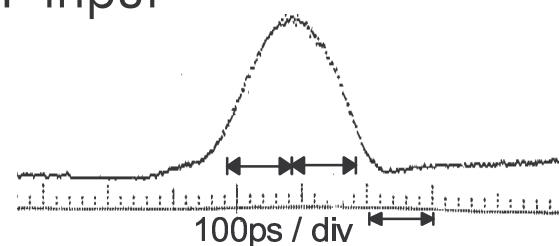


Electronic PMD equalization Measured transversal filter signals 1

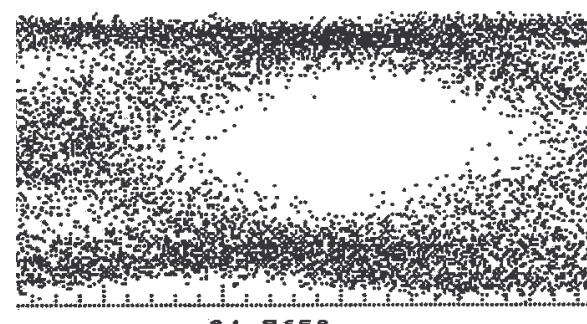
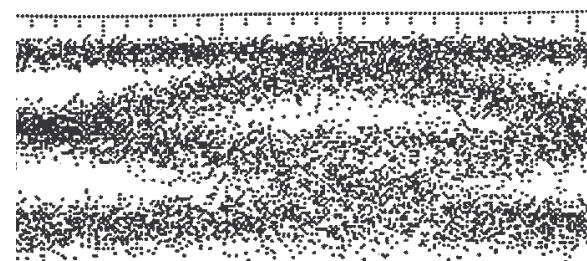
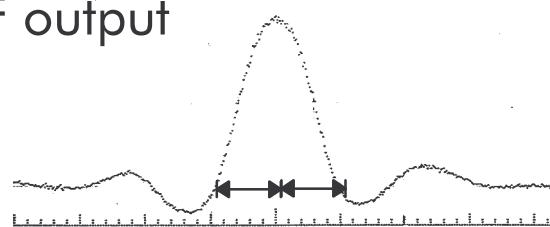


PMD distortion of $\gamma = 0.5$ and $\Delta\tau = 70$ ps

TF input



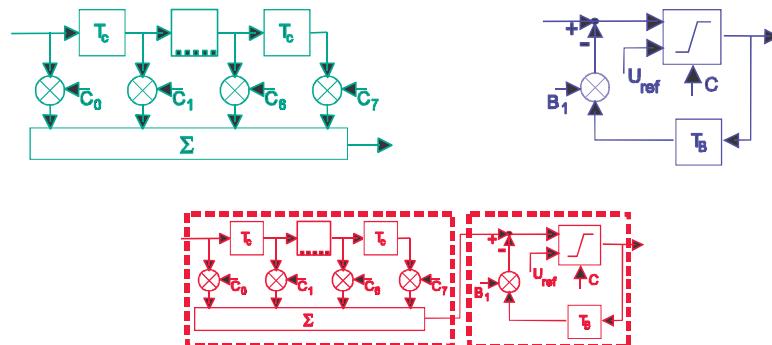
TF output



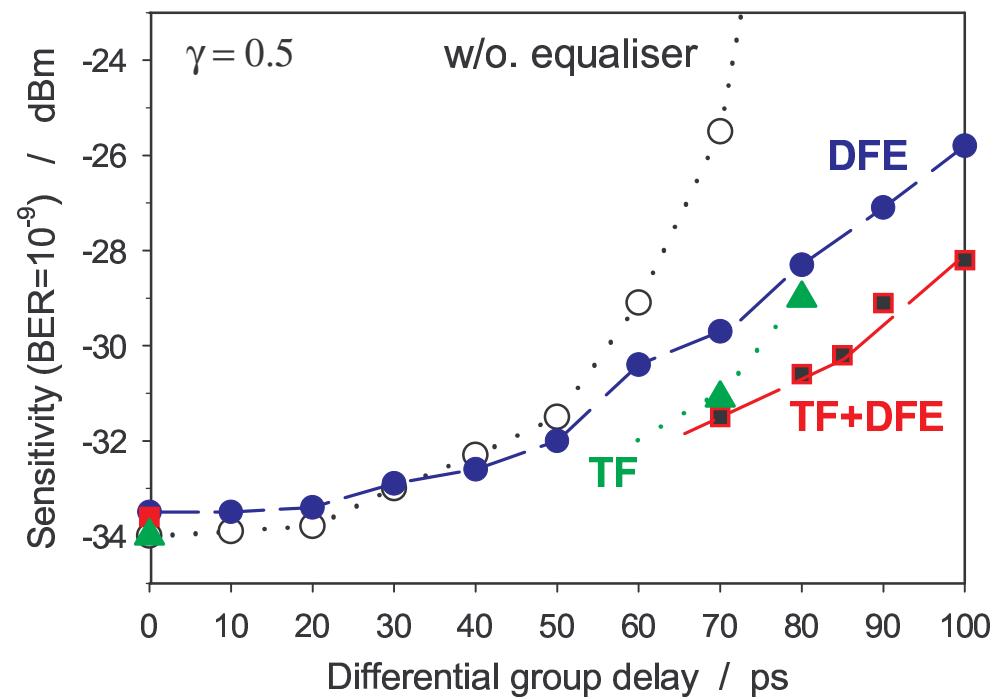
Electronic PMD equalization

Measured residual penalty

Since OSNR limited (EDFA)
 \Rightarrow more than 2x thermal noise penalty (dB)



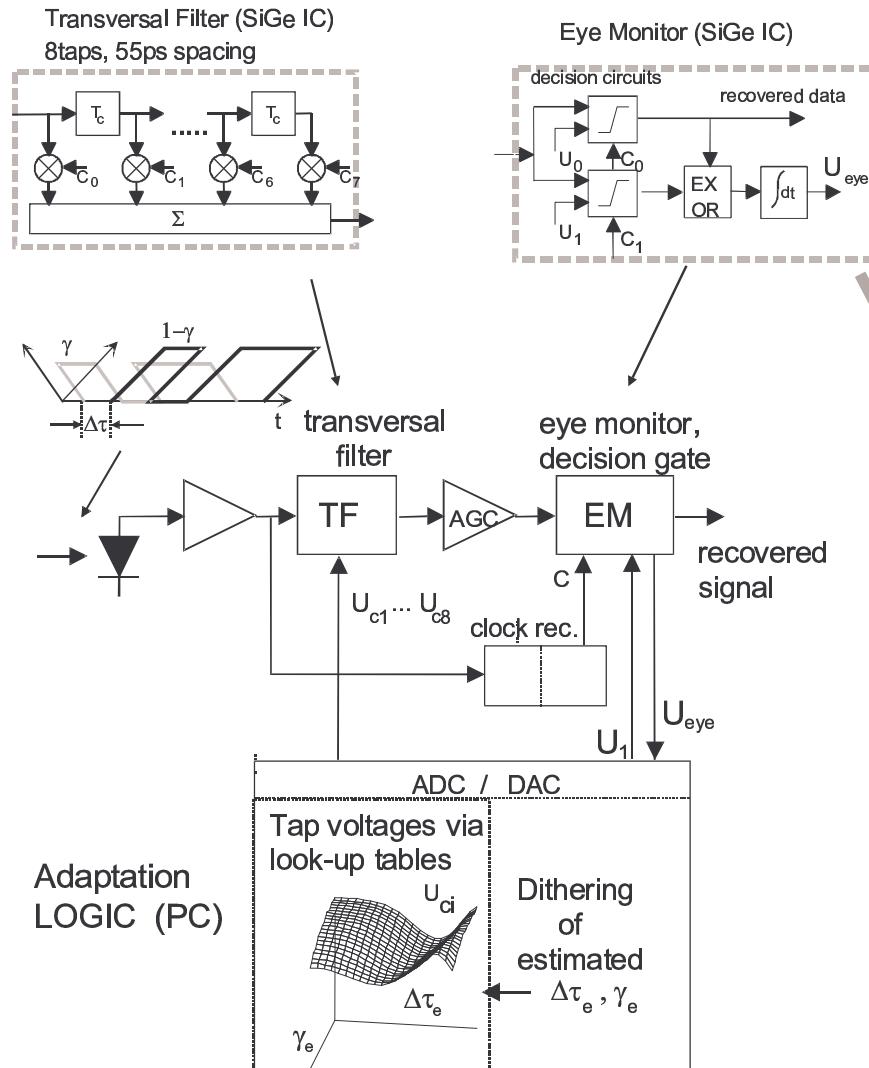
Electron. Lett., v.36, n.2, 2000, pp.163



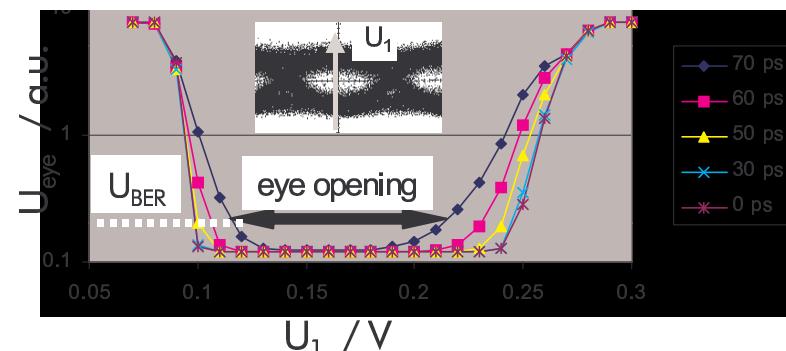
u FFE (TF) + DFE superior to TF or DFE

Electronic PMD equalization

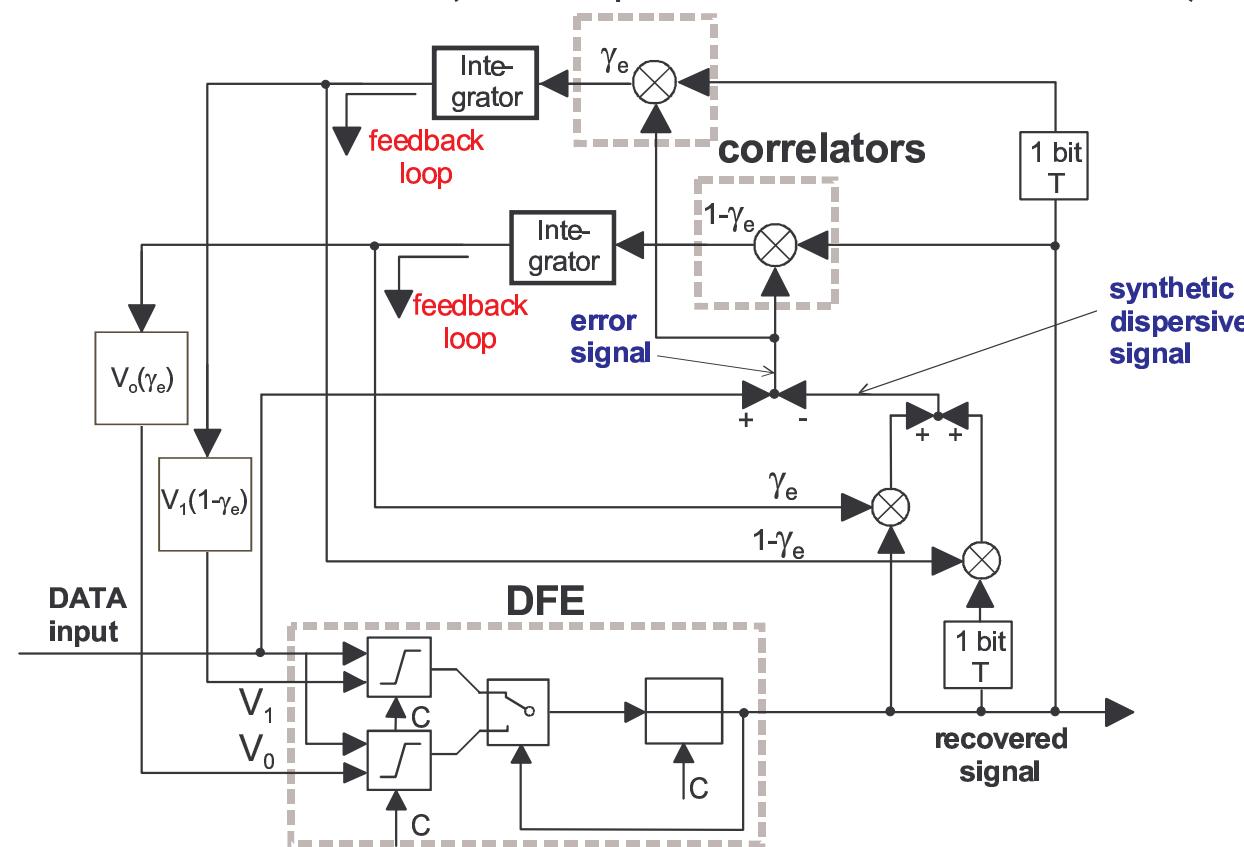
Adaptation 1



- u Transversal filter adaptation by optimization of output signal
 - TF output signal analyzed by eye monitor IC (SiGe)
- u Dithering of taps
 - some 10ms speed



- u DFE (+ 1 FFE tap) adaptation by zero-forcing scheme
 - hybrid setup for feasibility demo
 - speed well below 1ms (w/o. optimization of electronics)





- u Detected PMD of single-mode fiber \approx DMD of multi-mode fiber
- u Post-detection signal processing can be applied to mitigate PMD
- u Equalizers ICs in SiGe (HBT) have been manufactured for analog processing at 10G
 - ▶ 8 tap FFE
 - ▶ 1 bit delay DFE
- u Concept confirmed experimentally by demonstration of strong penalty reduction
- u First adaptation experiments
 - ➡ equalization at 10G works for PMD
 - ➡ attractive concept also for DMD mitigation

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