

IEEE 802.3ak
10GBASE-CX4 Task Force
Interim Meeting – May 20th, 21st 2003
Agenda
(Proposed – Subject to change)

Tuesday – May 20th

Administration: (8:00a –8:30a)

- Appoint/Volunteer Secretary
- Review /Approve Agenda
- Review /Approve March minutes
- Review Schedule
- Review IEEE Standards Bylaws regarding Intellectual Property

Presentations: (8:30a – 5:00p)

- “Loss Compensation using Analog Pre-emphasis” – Clark Foley (20 min)
- “10GBASE-CX4 Link Analysis” – Howard Baumer (90 min)
- **BREAK** (10:30 – 10:40)
- “10GBASE-CX4 Crosstalk Impact on System Performance – Petre Popescu (20 min)
- “10GBASE-CX4 Full System Performance” – Ze’ev Roth (30 min)
- “Jitter Budget” – Steve Dreyer (20 min)
- **LUNCH** (12:00 – 1:00)
- “Characterization of 802.3ak cable assemblies” – Dean Vermeersch (35 min)
- Belated Introductions -

Process: (2:10 – 9:00p)

- Comment Resolution (2:10 – 5:00)
- BREAK (3:00 – 3:15)
- DINNER (5:00-7:00)
- Comment Resolution (7:00 – 9:00)

Wednesday May 21st

Process: (8:00 – 5:00p)

- Comment Resolution (8:00 – 4:00)
- **BREAK** (10:30 – 10:40)
- **LUNCH** (12:00 – 1:00)
- **BREAK** (3:00 – 3:15)
- Review Schedule Status, Make Motions, and Plan for Upcoming Meeting (4:00 – 5:00)

***** Please plan to attend the WHOLE meeting *****