Annex tbd
(informative)

FEC frame encoding examples

This Annex provides an example FEC frame encoding with RS (255,223) code. See 92.2.3.4 for the format of the FEC frame.

tbd.1 Input to the RS (255, 223) Encoder

The Table tbd–1 provides an example 64b/66b block stream at the input to the RS (255,223) encoder. The example shows a stream of 27 scrambled 64b/66b symbols generated from the output of PCS layer when the link was sending out IDLEs. The transmission is from left to right within each row and from top to bottom between rows. The first bit out on the wire is at the top left hand corner.

Sync [0:1]	64bit payload in hex [0:63]	Sync [0:1]	64bit payload in hex [0:63]	Sync [0:1]	64bit payload in hex [0:63]	Sync [0:1]	64bit payload in hex [0:63]
10	40ea1e77eed301ec	10	ad5a3bf86d9acf5c	10	de55cb85df0f7ca0	10	e6ccff8e8212b1c6
10	d63bc6c309000638	10	70e3b0ce30e0497d	10	dc8df31ec3ab4491	10	66fb9139c81cd37b
10	b57477d4f05e3602	10	8cfd495012947a31	10	e7777cf0c6d06280	10	44529cf4b4900528
10	85ce1d27750ad61b	10	456d5c71743f5c69	10	c1bf62e5dc5464b5	10	dc6011be7ea1ed54
10	1cf92c450042a75f	10	cc4b940eaf3140db	10	77bb612a7abf401f	10	c22d341e90545d98
10	ce6daf1f248bbd6d	10	dd22d0b3f9551ed6	10	574686c3f9e93898	10	2e52628f4a1282ce
10	f20c86d71944aab1	10	55133c9333808a2c	10	1aa825d8b817db4d		

tbd.2 Output of the RS (255, 223) Encoder

The Table tbd–2 provides the 64b/66b symbols carrying parity that are generated by the RS (255, 223) encoder for the input symbols in table tbd.1 above.

The RS (255, 223) encoder appends the parity symbols subsequent to its transmission of the input symbols to the PMA (92.2.3.4). The transmission is from left to right within each row and from top to bottom between rows. The first bit out on the wire is at the top left hand corner.

Sync [0:1]	64bit payload in hex [0:63]	Sync [0:1]	64bit payload in hex [0:63]	Sync [0:1]	64bit payload in hex [0:63]	Sync [0:1]	64bit payload in hex [0:63]
00	4248BADA8C24BD16	11	5AE0396DC6AF5C8F	11	787312470938B14D	00	532AC5D6C5FC1B0D