

[Add to table 45-65:]

3.74	10GBASE-R clause 92 BER Monitor Control
3.75 through 3.32 767	reserved

[Add new paragraph 45.2.3.29:]

45.2.3.29 10GBASE-R clause 92 BER Monitor Control register (Register 3.74)

The 10GBASE-R clause 92 BER Monitor Control register[0:7] is an 8 bit value that specifies the duration (in microseconds) of the timer used by the 10GEAPON BER monitor function (92.2.4.6.?.1).

The 10GBASE-R clause 92 BER Monitor Control register[8:15] is an 8 bit value that the error threshold used by the 10GEAPON BER monitor function (92.2.4.6.?.1).

[Insert the following new subclauses 92.2.4.6.6 and 92.2.4.6.7 and renumber subsequent subclauses:]

92.2.4.6.6 Timers

State diagram timers follow the conventions of 14.2.3.2.

interval_timer

Timer that is triggered every *BER_monitor_interval* μs +1%, -25%.

92.2.4.6.7 PCS Management

The following objects apply to 10GEAPON PCS management. If an MDIO Interface is provided (see Clause 45), they are accessed via that interface. If not, it is recommended that an equivalent access be provided.

92.2.4.6.7.1 BER Monitor control

BER_Threshold

Indicates the threshold value of invalid sync headers associated with the BER monitor function. When *BER_Threshold* bad headers are encountered within the BER Monitor_Interval period, the BER Monitor raises the *hi_ber* flag. When the number of bad sync headers encountered within the BER_Monitor_interval period less than the BER_Threshold, the *hi_ber* flag is turned off. This value is reflected in MDIO register ??

BER_Monitor_Interval

Indicates the time window associated with the BER monitor function. The timers in the BER monitor state diagram depend on this configurable variable. This value is reflected in MDIO register ??

[Insert state diagram to state diagram subclause:]

92.2.4.6.1 BER Monitor State Diagram

The BER Monitor function is present in the ONU only.

