

Add to the end of table 45.5.3.3 PMA/PMD management functions:

MM119	Writes to this register have no effect	45.2.1.11		PMA:M	Yes [] No [] N/A []
-------	--	-----------	--	-------	------------------------------

Add to the end of table 45.5.3.7 PCS management functions

RM79	FEC corrected blocks counter is reset when read or upon PHY reset.	45.2.3.32		CPR:M	Yes [] No [] N/A []
RM80	FEC corrected blocks counter is held at all ones in the case of overflow	45.2.3.32		CPR:M	Yes [] No [] N/A []
RM81	FEC uncorrected blocks counter is reset when read or upon PHY reset.	45.2.3.33		CPR:M	Yes [] No [] N/A []
RM82	FEC uncorrected blocks counter is held at all ones in the case of overflow	45.2.3.33		CPR:M	Yes [] No [] N/A []

Add to the end of table 45.5.3.6 PCS options

*CPR	Implementation of 10GBASE-PR or 10/1GBASE-PRX PCS	45.2.3		PCS:O	Yes [] No [] N/A []
------	---	--------	--	-------	------------------------------