

Logic Sub-Task Force Closing Report

IEEE 802.3ba TF
New Orleans January 2010

Mark Gustlin – Cisco

Summary

- Thanks to the logic track editors: Ilango Ganga, Hugh Barrass, Arthur Marris and Steve Trowbridge
- Logic Track (Clauses 00, 01, A, 4A, 80, 99, 30, 31B, 45, 73, 74, 81, 82, 83)
Total Comments reviewed and resolved: ~289 (T/TR/ER/G/GRs)
- All open items have been closed, except for #395 which will be closed in the task force.

Top Issues Closed

- Top Issues closed:

#266, 267, Clarify that lanes are not identified as PCS lanes until after alignment marker lock is achieved in clause 82 and 45. Add mapping registers to provide the current mapping.

Many comments to re-align the multi-lane registers addresses in clause 45 to support future growth in the number of lanes (moving to even 100 chunks of address)

Many comments to clean-up PICS in the various clauses.