
**Copper “Cu” Sub-Task Force
Opening Report
San Francisco, CA - July 2009**

**Cu Sub-Task Force Chair
Chris Di Minico**

**Editor(s) – Ryan Latchman
Arthur Marris**

Cu Sub-Task Force

- **Cu Sub-Task Force Chair and Editor for 40GBASE-CR4 and 100GBASE-CR10, clause 85**
 - **Chris DiMinico**
- **Editor, 40 Gb/s (XLAUI) and 100 Gb/s (CAUI) interfaces, Annex 83A, 83B**
 - **Ryan Latchman**
- **Editor, Clauses 69, 74, 84, Annexes 69A, 69B**
 - **Arthur Marris**

Copper Sub-Task Force Schedule

	Sun	Mon	Tues	Wed	Thur	Fri				
8:00		SEC	Task Force			Logic	Copper	Optics	Task Force Motions & Closing Business	
8:30										
9:00										
9:30										
10:00										
10:30			Logic	Copper	Optics					
11:00		802.0 Plenary								
11:30										
12:00		Lunch	Lunch			Lunch	Lunch			
12:30										
13:00	Task Force	802.3 Plenary	Logic	Copper	Optics	Logic	Copper	Optics	802.3 Plenary	SEC
13:30										
14:00										
14:30										
15:00										
15:30										
16:00										
16:30										
17:00										
17:30	Dinner	Dinner			Dinner					
18:00		Tutorial #1								
18:30	Task Force	Tutorial #2	Logic	Copper	Optics	Social				
19:00										
19:30										
20:00										
20:30										
21:00										
21:30										
22:00		CFI								

Agenda Tuesday and Wednesday

- **Schedule (start Tuesday morning 8:30 AM to completion)**
 1. **Clause 85, 40GBASE-CR4 and 100GBASE-CR10**
 2. **Backplane Clauses 69, 73, 74, 84, Annexes 69A, 69B**
 3. **Annex 83A, 83B 40 Gb/s (XLAUI) and 100 Gb/s (CAUI) interfaces**
- **Priority**
 - **Review T/TRs for each clause**
- **Comment status**

Clause/Annexes	Editor	TR	T	ER	E	Total
85	Chris DiMinico	56	42	10	11	119
69, 74, 84, 69A, 69B	Aurthur Marris	4	1	0	6	11
83A,83B	Ryan Latchman	24	4	10	4	42
		84	47	20	21	172

5 late comments considered

Key items clause 85

Insertion loss allocation between PCB trace loss, connector, and cable

- Possible impact to cable reach objective

IL cable assembly fit

- Difference in backplane versus cable assembly IL- polynomial fit proposed

Cable assembly specifications

- IL and FEXT

TP2 and TP3 specification refinements

Return loss

- Consideration for RL budget related to ILD and power penalty IL allocation

Key items Annexes 69,69A,69B,84

- 69B- frequency range of specified parameters**
- 69 - architecture diagrams**

Key items Annexes 83A,83B

- Lane speed descriptions and designations
- Inclusion of shall statements and PICS entries
- Rise and fall times
- Consideration for PPI specifications
- Use of MCB and HCB