

**IEEE 802.3ba 10X MDI Pin Mapping & Mating Faces**  
**Fogg #549 Add table for MDI Mapping**

<b>Pin</b>	<b>Symbol</b>	<b>Description</b>
D1	Tx0p	Transmitter Non-Inverted Data Output
D2	Tx0n	Transmitter Inverted Data Output
D3	Tx2p	Transmitter Non-Inverted Data Output
D4	Tx2n	Transmitter Inverted Data Output
D5	Tx4p	Transmitter Non-Inverted Data Output
D6	Tx4n	Transmitter Inverted Data Output
D7	Tx6p	Transmitter Non-Inverted Data Output
D8	Tx6n	Transmitter Inverted Data Output
D9	Tx8p	Transmitter Non-Inverted Data Output
D10	Tx8n	Transmitter Inverted Data Output
D11	Res	Reserved
D12	Res	Reserved
D13	Res	Reserved
D14	Res	Reserved
C1	Tx1p	Transmitter Non-Inverted Data Output
C2	Tx1n	Transmitter Inverted Data Output
C3	Tx3p	Transmitter Non-Inverted Data Output
C4	Tx3n	Transmitter Inverted Data Output
C5	Tx5p	Transmitter Non-Inverted Data Output
C6	Tx5n	Transmitter Inverted Data Output
C7	Tx7p	Transmitter Non-Inverted Data Output
C8	Tx7n	Transmitter Inverted Data Output
C9	Tx9p	Transmitter Non-Inverted Data Output
C10	Tx9n	Transmitter Inverted Data Output
C11	Res	Reserved
C12	Res	Reserved
C13	Res	Reserved
C14	Res	Reserved

<b>Pin</b>	<b>Symbol</b>	<b>Description</b>
B1	Rx0p	Receiver Non-Inverted Data Input
B2	Rx0n	Receiver Inverted Data Input
B3	Rx2p	Receiver Non-Inverted Data Input
B4	Rx2n	Receiver Inverted Data Input
B5	Rx4p	Receiver Non-Inverted Data Input
B6	Rx4n	Receiver Inverted Data Input
B7	Rx6p	Receiver Non-Inverted Data Input
B8	Rx6n	Receiver Inverted Data Input
B9	Rx8p	Receiver Non-Inverted Data Input
B10	Rx8n	Receiver Inverted Data Input
B11	Res	Reserved
B12	Res	Reserved
B13	Res	Reserved
B14	Res	Reserved
A1	Rx1p	Receiver Non-Inverted Data Input
A2	Rx1n	Receiver Inverted Data Input
A3	Rx3p	Receiver Non-Inverted Data Input
A4	Rx3n	Receiver Inverted Data Input
A5	Rx5p	Receiver Non-Inverted Data Input
A6	Rx5n	Receiver Inverted Data Input
A7	Rx7p	Receiver Non-Inverted Data Input
A8	Rx7n	Receiver Inverted Data Input
A9	Rx9p	Receiver Non-Inverted Data Input
A10	Rx9n	Receiver Inverted Data Input
A11	Res	Reserved
A12	Res	Reserved
A13	Res	Reserved
A14	Res	Reserved