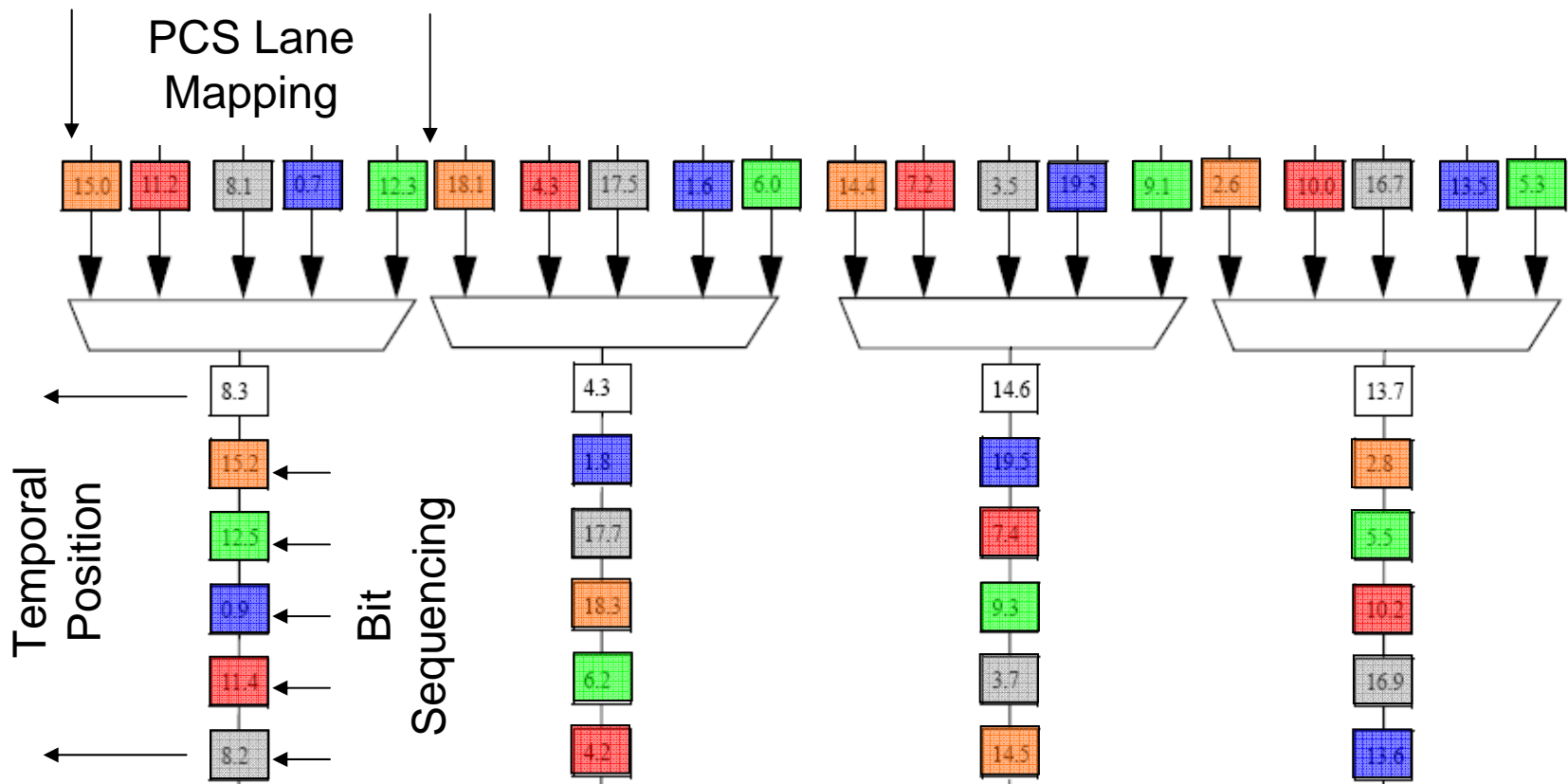


Comment #142: Description of Mapping

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Introduction

- Reference Comment #142 (D'Ambrosia) against D2.2:
- There is some concern regarding the use of the term mapping and how it relates to what is illustrated in Fig 83-6. The use of the word "mapping" seems to address how input lanes are directed to output lanes, but in the commenter's opinion does not do an adequate job addressing the sequencing of bits on the output lanes, which may lead to interpretation issues.



Excerpt from IEEE P802.3ba Draft 2.2 Fig 83-6

So...

- Mapping and sequencing are not specified
- May not change once link is brought up
- May change between successive times that a given link is brought up
- But text focuses only on mapping and does not address sequencing
- Will this be clear to those who have not been involved in IEEE P802.3ba?
- See marked up text in dambrosia_03_0909 for discussion